



# JUNCTION FIELD EFFECT TRANSISTOR 2SK3718

# N-CHANNEL SILICON JUNCTION FIELD EFFECT TRANSISTOR FOR IMPEDANCE CONVERTER OF ECM

# DESCRIPTION

The 2SK3718 is suitable for converter of ECM.

# **FEATURES**

- Low noise
- NV = -117 dB TYP. (V<sub>DS</sub> = 4.5 V, C = 10 pF, R<sub>L</sub> = 1.0 kΩ)
- Especially suitable for telephone, cellular phone & audio
- Small package SC-89 (TUSM)

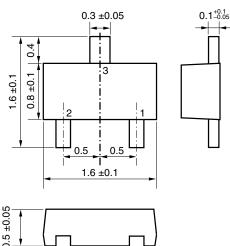
# **ORDERING INFORMATION**

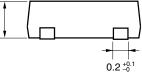
PART NUMBER	PACKAGE
2SK3718	SC-89 (TUSM)

#### ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^{\circ}C$ )

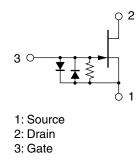
Drain to Source Voltage (V <sub>GS</sub> = $-1.0$ V)	VDSX	20	V
Gate to Drain Voltage	Vgdo	-20	V
Drain Current	lо	10	mA
Gate Current	lg	10	mA
Total Power Dissipation	Рт	100	mW
Junction Temperature	Tj	125	°C
Storage Temperature	Tstg	–55 to +125	°C

# PACKAGE DRAWING (Unit: mm)





#### EQUIVALENT CIRCUIT



Caution Please take care of ESD (Electro Static Discharge) when you handle the device in this document.

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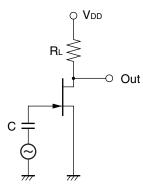
# \* ELECTRICAL CHARACTERISTICS (TA = 25°C)

CHARACTERISTICS	SYMBOL	TEST CONDITIONS MIN		TYP.	MAX.	UNIT
Zero Gate Voltage Drain Cut-off Current	IDSS	V <sub>DS</sub> = 5.0 V, V <sub>GS</sub> = 0 V	90	250	430	μA
Gate Cut-off Voltage	V <sub>GS(off)</sub>	V <sub>DS</sub> = 5.0 V, I <sub>D</sub> = 1.0 μA		-0.37	-1.0	V
Forward Transfer Admittance	y <sub>fs1</sub>	V <sub>DS</sub> = 5.0 V, I <sub>D</sub> = 30 <i>µ</i> A, f = 1.0 kHz	300	480		μS
	<b>y</b> fs2	V <sub>DS</sub> = 5.0 V, V <sub>GS</sub> = 0 V, f = 1.0 kHz	750	1600		μS
Input Capacitance	Ciss	V <sub>DS</sub> = 5.0 V, V <sub>GS</sub> = 0 V, f = 1.0 MHz		3.9		pF
Voltage Gain	Gv	$V_{DD}$ = 4.5 V, C = 10 pF, RL = 1 k $\Omega$ ,		-1.3		dB
		V⊪ = 10 mV, f = 1 kHz				
Noise Voltage	NV1	$V_{DD}$ = 2.0 V, C = 5 pF, RL = 2.2 kΩ,		-109.5		dB
		A-curve				
	NV2	$V_{DD}$ = 4.5 V, C = 10 pF, RL = 1 k $\Omega$ ,		-117	-112	dB
		A-curve				

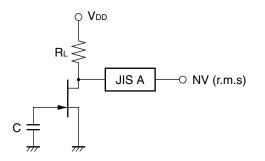
### **IDSS CLASSIFICATION**

MARKING	AE	AF	AH	AJ
Ibss (µA)	90 to 180	150 to 240	210 to 350	320 to 430

# **\* VOLTAGE GAIN TEST CIRCUIT**

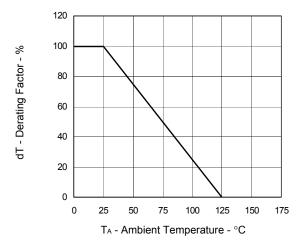


# NOISE VOLTAGE TEST CIRCUIT



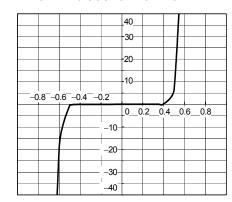
# **TYPICAL CHARACTERISTICS (TA = 25^{\circ}C)**

DERATING FACTOR OF POWER DISSIPATION

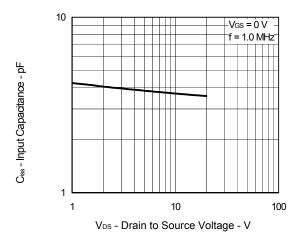


GATE TO SOURCE CURRENT vs. GATE TO SOURCE VOLTAGE

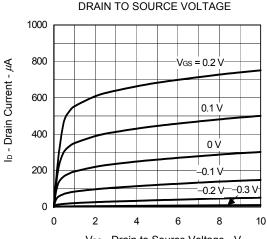
 $_{\rm IGS}$  - Gate to Source Current -  $\mu {\rm A}$ 



V<sub>GS</sub> - Gate to Source Voltage - V



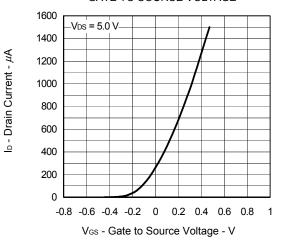
#### INPUT CAPACITANCE vs. DRAIN TO SOURCE VOLTAGE



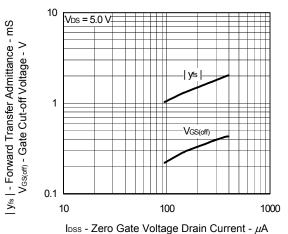
DRAIN CURRENT vs.

VDS - Drain to Source Voltage - V

DRAIN CURRENT vs. GATE TO SOURCE VOLTAGE

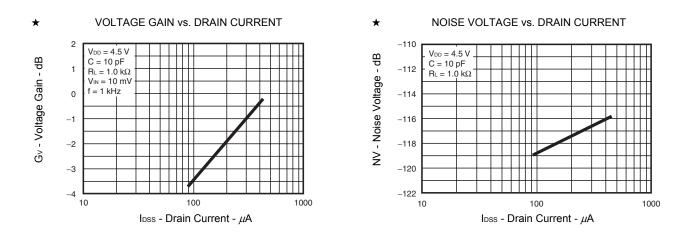


FORWARD TRANSFER ADMITTANCE AND GATE CUT-OFF VOLTAGE vs. ZERO GATE VOLTAGE DRAIN CURRENT



Data Sheet D16786EJ2V0DS





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