TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (U-MOSIII)

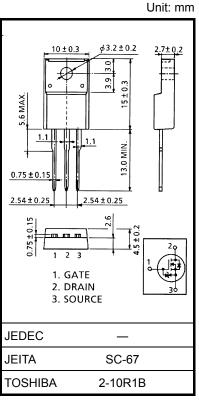
2SK3754

Relay Drive, DC-DC Converter and Motor Drive Applications

- 4.5-V gate drive
- Low drain-source ON resistance: $RDS(ON) = 71 \text{ m}\Omega \text{ (typ.)}$
- High forward transfer admittance: $|Y_{fs}| = 5.0 \text{ S (typ.)}$
- Low leakage current: $IDSS = 10 \mu A (max) (VDS = 30 V)$
- Enhancement-model: $V_{th} = 1.3 \sim 2.5 \text{ V (V}_{DS} = 10 \text{ V}, I_D = 1 \text{ mA)}$

Absolute Maximum Ratings (Ta = 25°C)

Characteristics			Symbol	Rating	Unit
Drain-source voltage			V_{DSS}	30	V
Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$)			V_{DGR}	30	V
Gate-source voltage			V_{GSS}	±20	V
Drain current	DC	(Note 1)	I _D	5	^
	Pulse	(Note 1)	I _{DP}	15	Α
Drain power dissipation (Tc = 25°C)			P_{D}	25	W
Single pulse avalanche energy (Note 2)			E _{AS}	4.0	mJ
Avalanche current			I _{AR}	2.5	Α
Repetitive avalanche energy (Note 3)			E _{AR}	2.5	mJ
Channel temperature			T _{ch}	150	°C
Storage temperature range			T _{stg}	-55~150	°C



Weight: 1.9 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristics	Symbol	Max	Unit	
Thermal resistance, channel to case	R _{th (ch-c)}	5.0	°C/W	
Thermal resistance, channel to ambient	R _{th (ch-a)}	62.5	°C/W	

Note 1: Please use devices on conditions that the channel temperature is below 150°C.

Note 2: V_{DD} =24 V, T_{ch} = 25°C (initial), L = 0.5 mH, R_G = 25 Ω , I_{AR} = 2.5 A

Note 3: Repetitive rating: Pulse width limited by maximum channel temperature

This transistor is an electrostatic sensitive device. Please handle with caution.



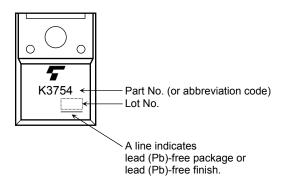
Electrical Characteristics (Ta = 25°C)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I _{GSS}	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±10	μА
Drain cut-off curre	nt	I _{DSS}	$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}$		_	10	μА
Drain-source breakdown voltage		V (BR) DSS	S I _D = 10 mA, V _{GS} = 0 V		_	_	V
		V (BR) DSX	$I_D = 10 \text{ mA}, V_{GS} = -20 \text{ V}$	15	_	_	V
Gate threshold vol	tage	V _{th}	$V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA}$	1.3	_	2.5	V
Drain-source ON resistance		R _{DS} (ON)	$V_{GS} = 4.5 \text{ V}, I_D = 2.5 \text{ A}$		78	99	- mΩ
		NDS (ON)	$V_{GS} = 10 \text{ V}, I_D = 2.5 \text{ A}$		71	89	
Forward transfer admittance		Y _{fs}	$V_{DS} = 10 \text{ V}, I_D = 2.5 \text{ A}$	2.5	5.0		S
Input capacitance		C _{iss}			1250		
Reverse transfer capacitance		C _{rss}	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		155	_	pF
Output capacitance		Coss			170		
Switching time	Rise time	t _r	$V_{GS}^{10 \text{ V}}$ $V_{GS}^{10 \text{ V}}$ $V_{GS}^{10 \text{ V}}$ $V_{OUT}^{10 \text{ V}}$ $V_{OUT}^{10 \text{ V}}$ $V_{OUT}^{10 \text{ V}}$		7		ns
	Turn-on time	t _{on}		_	16		
	Fall time	t _f	$V_{DD} \simeq 15 \text{ V}$		18		113
	Turn-off time	t _{off}	Duty ≦ 1%, t _w = 10 μs	_	69		
Total gate charge		Qg		_	25	_	nC
Gate-source charge		Q _{gs}	$V_{DD} \simeq 24 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 5 \text{ A}$	_	20	_	
Gate-drain charge		Q _{gd}		_	5		

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	_	_	_	5	Α
Pulse drain reverse current (Note 1)	I _{DRP}	_	_	_	15	Α
Reverse recovery time	t _{rr}	$I_{DR} = 5 \text{ A}, V_{GS} = 0 \text{ V},$	_	37	_	ns
Reverse recovery charge	Q _{rr}	dI _{DR} /dt = 50 A/μs	_	20	_	nC

Marking



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20070701-EN

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