TOSHIBA Field Effect Transistor Silicon N-Channel MOS Type (π- MOSIV)

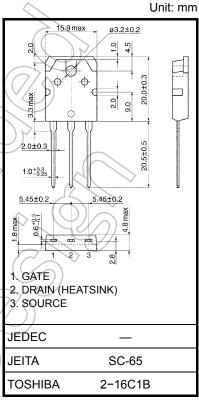
2SK3878

Switching Regulator Applications

- Low drain-source ON-resistance: $R_{DS(ON)} = 1.0 \Omega$ (typ.)
- High forward transfer admittance: $|Y_{fS}| = 7.0 \text{ S (typ.)}$
- Low leakage current: I_{DSS} = 100 μA (max) (V_{DS} = 720 V)
- Enhancement model: V_{th} = 2.0 to 4.0 V (V_{DS} = 10 V, I_D = 1 mA)

Absolute Maximum Ratings (Ta = 25°C)

Characteristic			Symbol	Rating	Unit
Drain-source voltage			V_{DSS}	900	$\langle \rangle$
Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$)			V_{DGR}	900) v
Gate-source voltage			V _{GSS}	±30	V
Drain current	DC	(Note 1)	ID	9	A
	Pulse	(Note 1)	I _{DP}	27	A
Drain power dissipation (Tc = 25°C)			PD	150	W
Single pulse avalanche energy (Note 2)			E _{AS}	778	mJ
Avalanche current			I _{AR})) 9	Α
Repetitive avalanche energy (Note 3)			EAR	15	mJ
Channel temperature			T _{ch}	150)°C
Storage temperature range			Tstg	-55 to 150	_e



Weight: 4.6 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

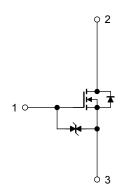
Characteristic	Symbol	Max	Unit
Thermal resistance, channel to case	Rth (ch-c)	0.833	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	50	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C during use of the device.

Note 2: $V_{DD} = 90 \text{ V}$, $T_{ch} = 25^{\circ}\text{C}$, L = 17.6 mH, $R_G = 25 \Omega$, $I_{AR} = 9 \text{ A}$

Note 3: Repetitive rating: pulse width limited by max junction temperature

This transistor is an electrostatic-sensitive device. Handle with care.



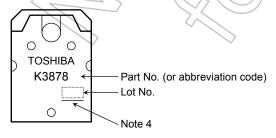
Electrical Characteristics (Ta = 25°C)

Char	acteristic	Symbol	Test Condition	Min	Тур.	Max	Unit		
Gate leakage curi	rent	I _{GSS}	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±10	μΑ		
Gate-source brea	kdown voltage	V (BR) GSS	$I_G=\pm 10~\mu A,~V_{DS}=0~V$	±30	_	_	V		
Drain cutoff curre	nt	I _{DSS}	V _{DS} = 720 V, V _{GS} = 0 V	100		100	μΑ		
Drain-source brea	akdown voltage	V (BR) DSS	$I_D = 10$ mA, $V_{GS} = 0$ V	900	/		V		
Gate threshold voltage		V _{th}	V _{DS} = 10 V, I _D = 1 mA	2.0) /_	4.0	V		
Drain-source ON resistance		R _{DS} (ON)	V _{GS} = 10 V, I _D = 4 A) 	1.0	1.3	Ω		
Forward transfer admittance		Y _{fs}	V _{DS} = 15 V, I _D = 4 A	3.5	7.0	_	S		
Input capacitance		C _{iss}			2200	_			
Reverse transfer capacitance		C _{rss}	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz	_	45	_	pF		
Output capacitance		Coss		_	190	/			
Switching time	Rise time	t _r	V _{GS} T _D =4A	- (25	7			
	Turn-on time	t _{on}	0 V		65) —	ns		
	Fall time	t _f	$\begin{array}{c c} G & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ &$	7 (5)	20				
	Turn-off time	t _{off}	Duty \leq 1%, $t_{W} = 10 \mu s$ $V_{DD} \approx 400 \text{ V}$) –	120				
Total gate charge (gate-source plus gate-drain)		Qg		_	60	_			
Gate-source charge		Q _{gs}	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 9 \text{ A}$		34		nC		
Gate-drain ("Miller") charge		Qgd		_	26				

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	_	_	_	9	Α
Pulse drain reverse current (Note 1)	IDRP		_	_	27	Α
Forward voltage (diode)	V _{DSF}	I _{DR} = 9 A, V _{GS} = 0 V	_	_	-1.7	V
Reverse recovery time	∕> t _{rr}	I _{DR} = 9 A, V _{GS} = 0 V,	_	1.4	_	μS
Reverse recovery charge	Qrr	dI _{DR} /dt = 100 A/μs		16	_	μС

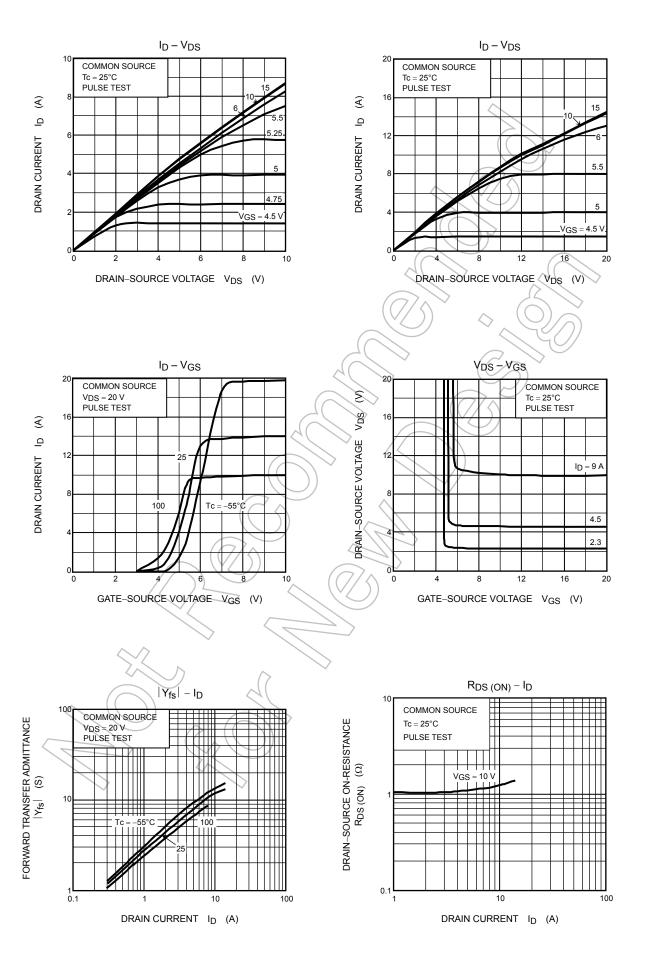
Marking

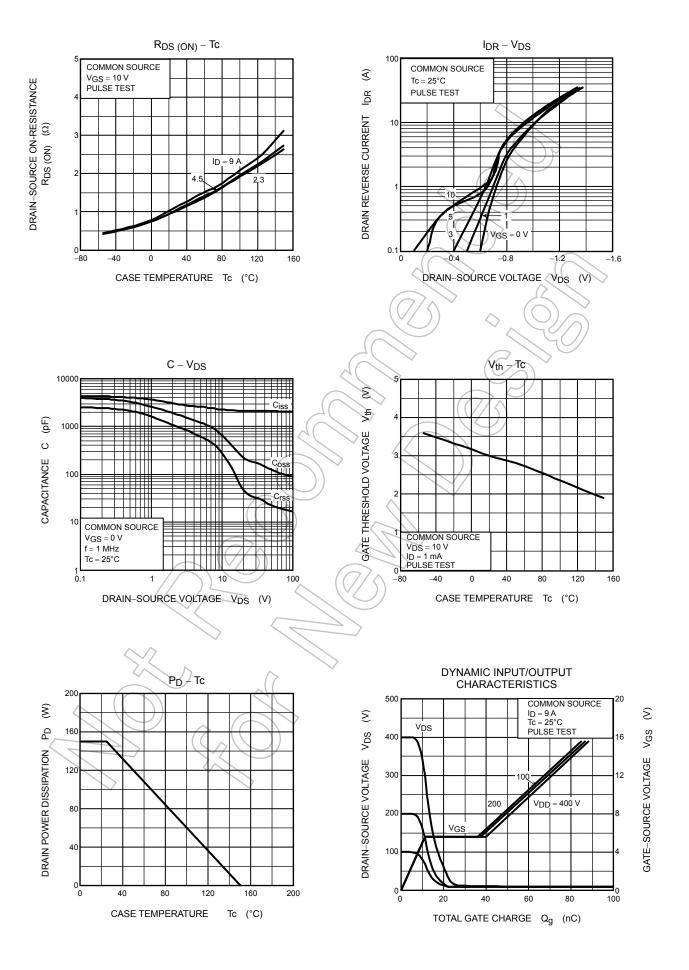


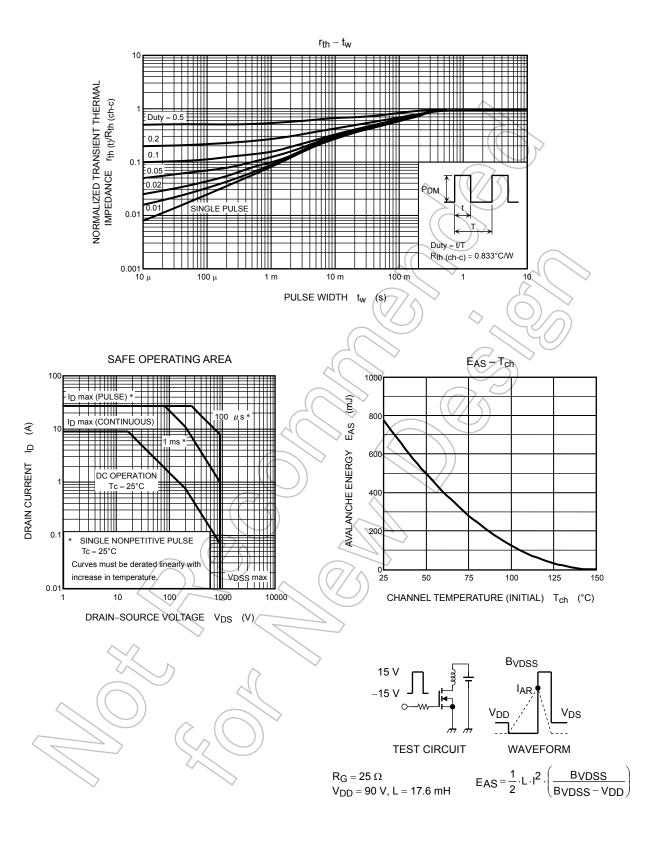
Note 4: A line under a Lot No. identifies the indication of product Labels.

Not underlined: [[Pb]]/INCLUDES > MCV Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

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