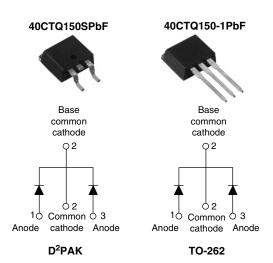




Vishay High Power Products

Schottky Rectifier, 2 x 20 A



PRODUCT SUMMARY			
I _{F(AV)} 2 x 20 A			
V_R	150 V		

FEATURES

- 175 °C T_J operation
- · Center tap TO-220 package
- Very low forward voltage drop





COMPLIANT

- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- Lead (Pb)-free ("PbF" suffix)
- · Designed and qualified for industrial level

DESCRIPTION

The 40CTQ... center tap Schottky rectifier has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	VALUES	UNITS	
I _{F(AV)}	Rectangular waveform	40	А	
V _{RRM}		150	V	
I _{FSM}	t _p = 5 μs sine	1500	Α	
V _F	20 Apk, T _J = 125 °C (per leg)	0.71	V	
TJ		- 55 to 175	°C	

VOLTAGE RATINGS				
PARAMETER	TER SYMBOL		UNITS	
Maximum DC reverse voltage	V_{R}	150	V	
Maximum working peak reverse voltage	V_{RWM}	150	V	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average per le	1	(AV) 50 % duty cycle at $T_C = 140$ °C, rectangular waveform 40		20	
forward current See fig. 5 per device	I _{F(AV)}			40	
Maximum peak one cycle non-repetitive surge current per leg See fig. 7		5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	1500	A
		10 ms sine or 6 ms rect. pulse		250	
Non-repetitive avalanche energy per leg	E _{AS}	$T_J = 25 ^{\circ}\text{C}$, $I_{AS} = 1.5 \text{A}$, $L = 0.9 \text{mH}$		mJ	
Repetitive avalanche current per leg	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T_J maximum $V_A = 1.5$ x V_R typical		Α	

^{*} Pb containing terminations are not RoHS compliant, exemptions may apply

40CTQ150SPbF/40CTQ150-1PbF

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	L TEST CONDITIONS VALUES		UNITS	
Maximum forward voltage drop per leg See fig. 1	V _{FM} ⁽¹⁾	20 A	- T _J = 25 °C	0.93	
		40 A		1.16	V
		20 A	T _J = 125 °C	0.71	
		40 A		0.85	
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	50	μΑ
See fig. 2	'RM \''	T _J = 125 °C		15	mA
Maximum junction capacitance per leg	C _T	V _R = 5 V _{DC} (test signal range 100 kHz to 1 MHz) 25 °C		450	pF
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body		8.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R 10 000 V _A		V/µs	

Note

 $^{^{(1)}\,}$ Pulse width < 300 µs, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range		T _J , T _{Stg}		- 55 to 175	°C	
Maximum thermal resistance, junction to case per leg		В	DC operation See fig. 4	1.5		
Maximum thermal resistance, junction to case per package		R_{thJC}	DC operation	0.75	°C/W	
Typical thermal resistance, case to heatsink		R _{thCS} Mounting surface, smooth and greased		0.5		
Approximate weight				2	g	
				0.07	OZ.	
Mounting torque ——	minimum		Niam lubricada di Mara a da	6 (5)	kgf ⋅ cm (lbf ⋅ in)	
	maximum		Non-lubricated threads	12 (10)		
			Case style D ² PAK	40CTC)150S	
Marking device			Case style TO-262	40CTC	150-1	



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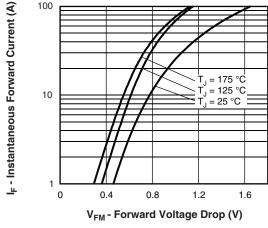


Fig. 1 - Maximum Forward Voltage Drop Characteristics

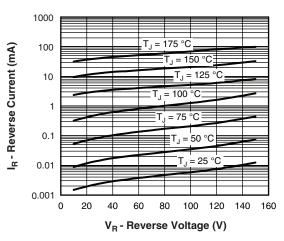


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

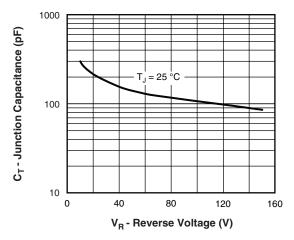


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

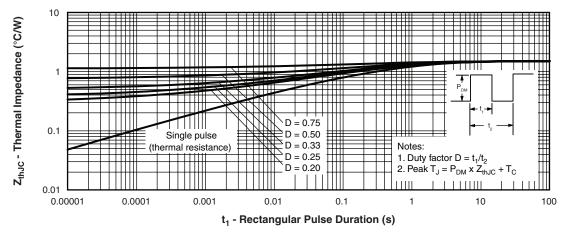


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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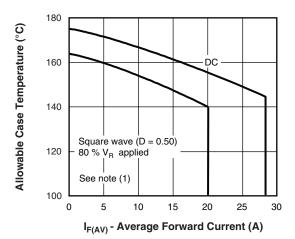


Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current

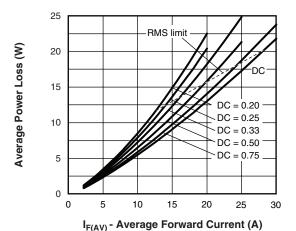


Fig. 6 - Forward Power Loss Characteristics

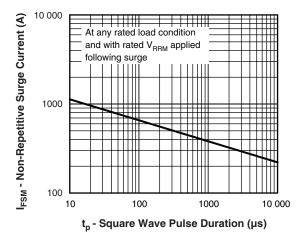


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

Note

(1) Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$; $Pd = Forward power loss = I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6); $Pd_{REV} = Inverse power loss = V_{R1} \times I_R$ (1 - D); I_R at $V_{R1} = 80 \% V_R$ applied

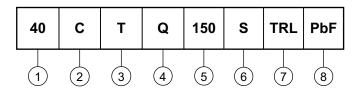


40CTQ150SPbF/40CTQ150-1PbF

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ORDERING INFORMATION TABLE

Device code



1 - Current rating (40 A)

2 - Circuit configuration:

C = Common cathode

3 - T = TO-220

4 - Schottky "Q" series

5 - Voltage rating (150 = 150 V)

6 - • S = D²PAK

• -1 = TO-262

7 - • None = Tube (50 pieces)

• TRL = Tape and reel (left oriented - for D²PAK only)

• TRR = Tape and reel (right oriented - for D2PAK only)

8 - • None = Standard production

• PbF = Lead (Pb)-free

LINKS TO RELATED DOCUMENTS			
Dimensions http://www.vishay.com/doc?95014			
Part marking information	http://www.vishay.com/doc?95008		
Packaging information	http://www.vishay.com/doc?95032		



Vishay

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