

# High Sensitivity, High Speed Photocoupler

## 6N139 Series

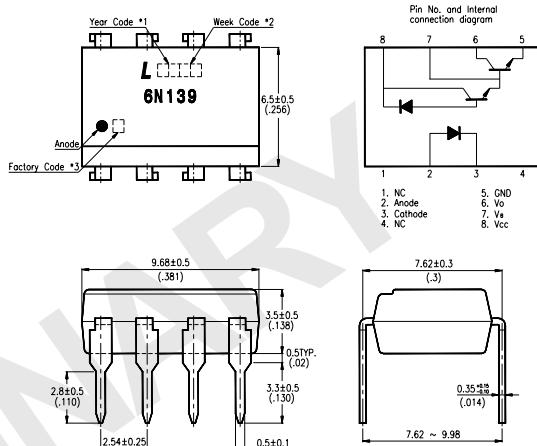
### Features

- High current transfer ratio  
(CTR : MIN. 500% at  $I_F = 1.6\text{mA}$ )
- High speed response  
( $t_{PHL}$  : TYP.  $0.2\ \mu\text{s}$  at  $R_L = 270\ \Omega$ )
- Instantaneous common mode rejection voltage  
( $CMH$  : TYP.  $500\text{V}/\mu\text{s}$ )
- TTL compatible output
- Options Available :
  - Leads with 0.4" (10.16mm) Spacing (M Type)
  - Lead Bends for Surface Mounting (S Type)
  - Tape and Reel of Type I for SMD (Add "-TA" Suffix)
  - Tape and Reel of Type II for SMD (Add "-TA1" Suffix)
  - VDE 0884 Approvals (Add "-V" Suffix)

### Applications

1. Interfaces for computer peripherals
2. Electronic calculators, measuring instruments, control equipment
3. Telephone sets
4. Signal transmission between circuits of different potentials and impedances

### Package Dimensions



#### NOTES :

1. Year date code.
2. 2-digit work week.
3. Factory code shall be marked (Z : Taiwan, Y : Thailand).
4. All dimensions are in millimeters (inches).
5. Tolerance is  $\pm 0.25\text{mm}$  (.010") unless otherwise noted.
6. Specifications are subject to change without notice.

### Ordering Information

Part Number	Package	Application part number
6N139 6N139M 6N139S 6N139S-TA 6N139S-TA1	8-pin DIP 8-pin (leads with 0.4" spacing) 8-pin (lead bends for surface mount) 8-pin (tape and reel packaging of type I) 8-pin (tape and reel packaging of type II)	6N139
6N139-V 6N139M-V 6N139S-V 6N139STA-V 6N139STA1-V	8-pin DIP 8-pin (leads with 0.4" spacing) 8-pin (lead bends for surface mount) 8-pin (tape and reel packaging of type I) 8-pin (tape and reel packaging of type II)	6N139

## Ratings and Characteristics

### Absolute Maximum Ratings

(Ta=25°C)

Parameter		Symbol	Rating	Unit
Input	Forward Current	I <sub>F</sub>	20	mA
	Reverse Voltage	V <sub>R</sub>	5	V
	Power Dissipation	P	35	mW
Output	Supply Voltage	V <sub>CC</sub>	-0.5~+18	V
	Output Voltage	V <sub>O</sub>	-0.5~+18	V
	Emitter-base Reverse Withstand Voltage (pin 5 to 7)	V <sub>EBO</sub>	0.5	V
	Average Output Current	I <sub>O</sub>	60	mA
	Power Dissipation	P <sub>O</sub>	100	mW
*1.Isolation Voltage		V <sub>ISO</sub>	2,500	Vrms
Operating Temperature		T <sub>opr</sub>	0~+70	°C
Storage Temperature		T <sub>stg</sub>	-55~+125	°C
*2.Soldering Temperature		T <sub>sol</sub>	260	°C

\*1. AC for 1 minute, R.H. = 40 ~ 60%

Isolation voltage shall be measured using the following method.

- (1) Short between anode and cathode on the primary side and between collector and emitter on the secondary side.
- (2) The isolation voltage tester with zero-cross circuit shall be used.
- (3) The waveform of applied voltage shall be a sine wave.

\*2. For 10 seconds

### Electrical / Optical Characteristics

( Ta = 0 ~ +70°C unless otherwise specified )

Parameter	Symbol	Min.	Typ.	Max.	unit	Conditions
Input Forward Voltage	V <sub>F</sub>	—	—	1.7	V	Ta=25°C, I <sub>F</sub> =1.6mA
Input Forward Voltage Temperature Coefficient	ΔV <sub>F</sub> / ΔT <sub>a</sub>	—	-1.9	—	mV/°C	I <sub>F</sub> =1.6mA
Input Reverse Voltage	B <sub>VR</sub>	5.0	—	—	V	Ta=25°C, I <sub>R</sub> =10 μA
Input Capacitance	C <sub>IN</sub>	—	60	—	pF	V <sub>F</sub> =0, f=1MHz
*1 Current Transfer Ratio	CTR	400	1800	—	%	I <sub>F</sub> =0.5mA, V <sub>O</sub> =0.4V V <sub>CC</sub> =4.5V
		500	1600	—	%	I <sub>F</sub> =1.6mA, V <sub>O</sub> =0.4V V <sub>CC</sub> =4.5V
Logic (0) Output Voltage	V <sub>O(L)</sub>	—	0.1	0.4	V	I <sub>F</sub> =1.6mA V <sub>CC</sub> =4.5V I <sub>O</sub> =6.4mA
		—	0.1	0.4	V	I <sub>F</sub> =5mA V <sub>CC</sub> =4.5V I <sub>O</sub> =15mA
		—	0.1	0.4	V	I <sub>F</sub> =12mA V <sub>CC</sub> =4.5V I <sub>O</sub> =24mA
Logic (1) Output Current	I <sub>O(H)</sub>	—	0.05	100	μ A	I <sub>F</sub> =0, V <sub>CC</sub> =V <sub>O</sub> =18V
Logic (0) Supply Current	I <sub>CC(L)</sub>	—	0.5	1.5	mA	I <sub>F</sub> =1.6mA, V <sub>CC</sub> =5V V <sub>O</sub> =open
Logic (1) Supply Current	I <sub>CC(H)</sub>	—	0.01	10	μ A	I <sub>F</sub> =0, V <sub>CC</sub> =5V V <sub>O</sub> =open
*2 Leak Current (Input-Output)	I <sub>l(O)</sub>	—	—	1.0	μ A	Ta=25°C, 45%RH, t=5s, V <sub>I-O</sub> =3kV DC
*2 Isolation Resistance (Input-Output)	R <sub>I-O</sub>	—	10 <sup>12</sup>	—	Ω	V <sub>I-O</sub> =500VDC
Capacitance (Input-Output)	C <sub>I-O</sub>	—	0.6	—	pF	f=1MHz

\*1. Current transfer ratio is the ratio of input current and output current expressed in %.

\*2. Measured as 2-pin element ( Short 1, 2, 3, 4 and 5, 6, 7, 8 )

## ● Electrical / Optical Characteristics

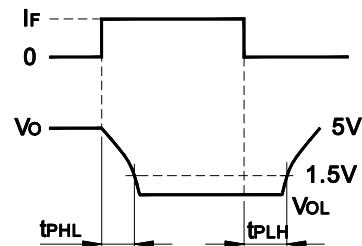
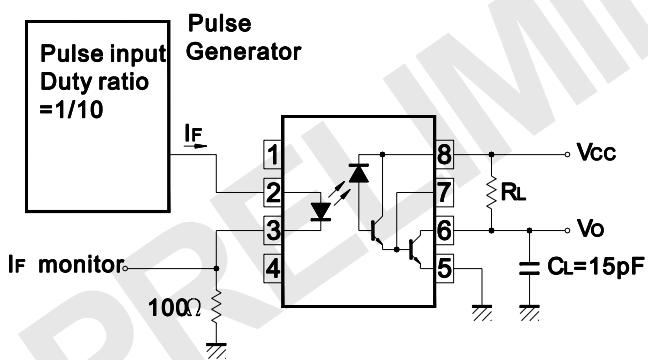
( Ta = 25°C V<sub>CC</sub> = 5V)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Propagation Delay time Output (1)→(0)	t <sub>PHL</sub>	—	5	25	μ s	I <sub>F</sub> =0.5mA, R <sub>L</sub> =4.7k Ω
		—	0.5	1	μ s	I <sub>F</sub> =12mA, R <sub>L</sub> =270 Ω
Propagation Delay time Output (0)→(1)	t <sub>PLH</sub>	—	10	60	μ s	I <sub>F</sub> =0.5mA, R <sub>L</sub> =4.7k Ω
		—	1.5	7	μ s	I <sub>F</sub> =12mA, R <sub>L</sub> =270 Ω
*3 Instantaneous common mode rejection voltage "output(1)"	CM <sub>H</sub>	—	500	—	V/ μ s	I <sub>F</sub> =0, V <sub>CM</sub> =10V <sub>P-P1</sub> R <sub>L</sub> =2.2k Ω
*3 Instantaneous common mode rejection voltage "output(0)"	CM <sub>L</sub>	—	-500	—	V/ μ s	V <sub>CM</sub> =10V <sub>P-P</sub> , I <sub>F</sub> =1.6mA R <sub>L</sub> =2.2k Ω

\*3 Instantaneous common mode rejection voltage "output (1)" represents a common mode voltage variation that can hold the output above (1) level (V<sub>O</sub>>2.0V).

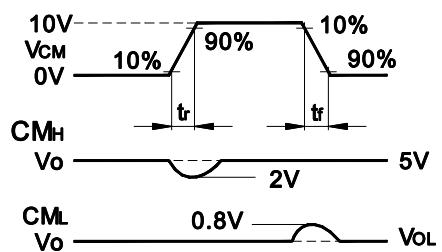
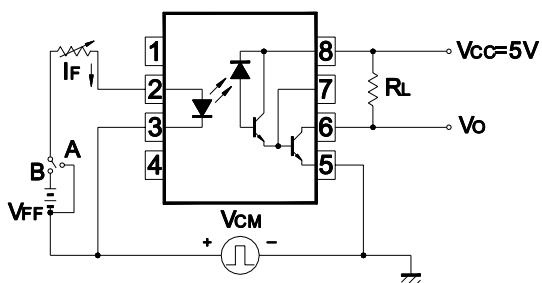
Instantaneous common mode rejection voltage "output (0)" represents a common mode voltage variation that can hold the output above (0) level (V<sub>O</sub><0.8V).

## Test Circuit for Propagation Delay Time



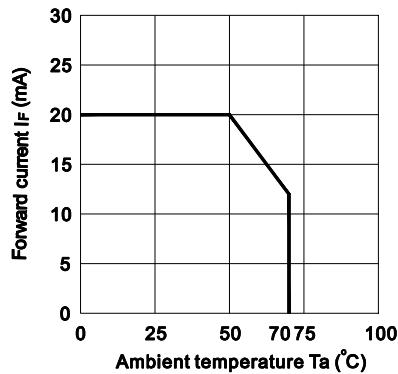
## Test Circuit for Instantaneous Common Mode Rejection Voltage

t<sub>r</sub>=t<sub>f</sub>=16ns

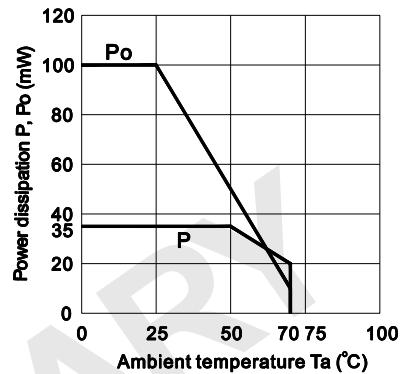


## Typical Electrical/Optical Characteristic Curves (25°C Ambient Temperature Unless Otherwise Noted)

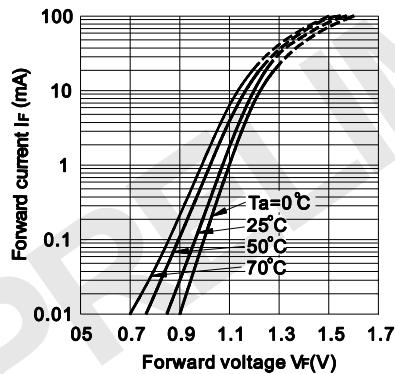
**Fig.1 Forward Current vs. Ambient Temperature**



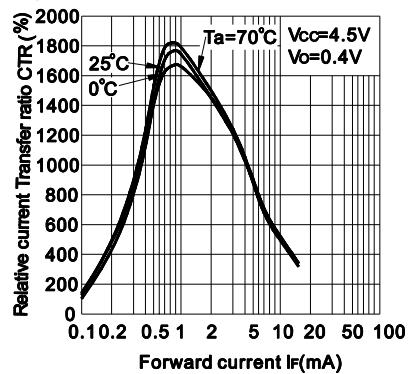
**Fig.2 Power Dissipation vs. Ambient Temperature**



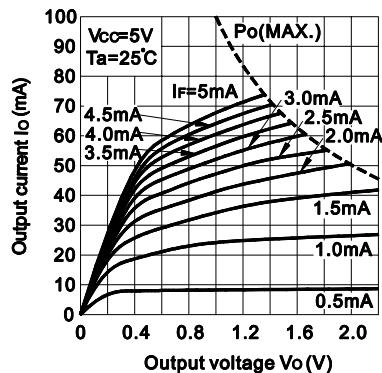
**Fig.3 Forward Current vs. Forward Voltage**



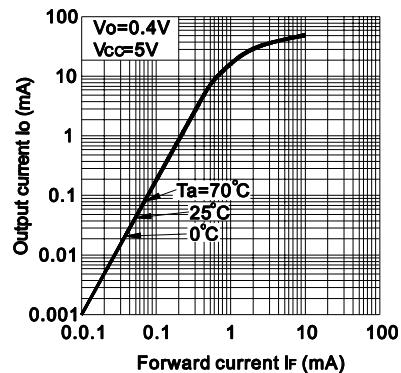
**Fig.4 Relative Current Transfer Ratio vs. Forward Current**



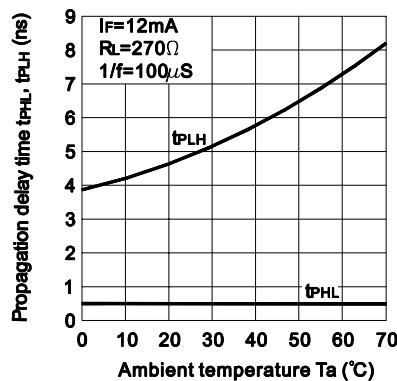
**Fig.5 Output Current vs. Output Voltage**



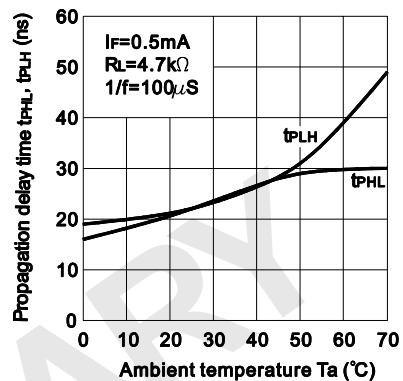
**Fig.6 Output Current vs. Forward Current**



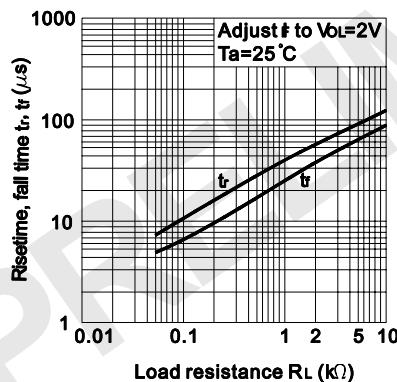
**Fig.7-a Propagation Delay Time vs. Ambient Temperature**



**Fig.7-b Propagation Delay Time vs. Ambient Temperature**



**Fig.8 Rise Time, Fall Time vs. Load Resistance**



**Fig.9 Logic (1) Supply Current vs. Ambient Temperature**

