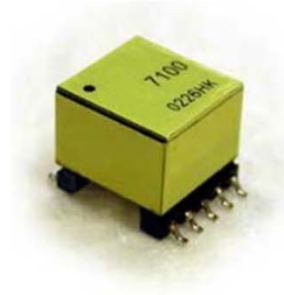


# HDSL

# Transformer

# 7100

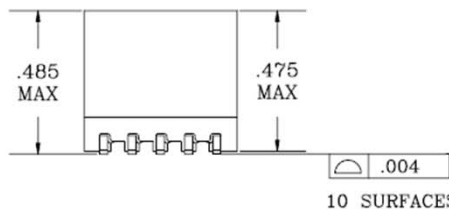
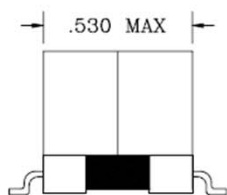
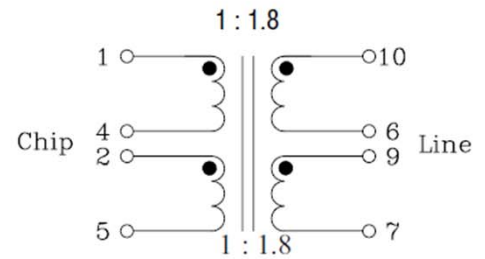
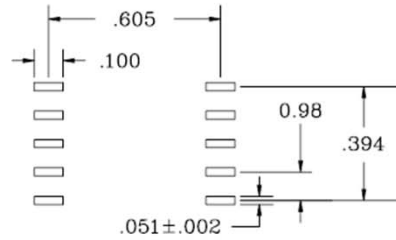
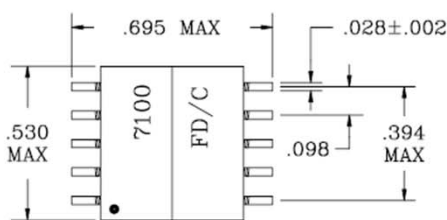
- Designed for use with Level One's ICs SK70721/725 and Zarlink's MH88435 and MH88612
- Excellent THD performance
- Designed to meet the supplementary insulation requirements of UL 1950, IEC950 and EN60950 for working voltages up to 250Vrms
- Extended temperature range: -40°C to +85°C



## MECHANICAL AND SCHEMATIC (All dimensions in inches)

SUGGESTED PAD LAYOUT

SCHEMATIC



10 SURFACES

UNLESS OTHERWISE SPECIFIED, ALL TOLERANCES ARE ±0.010

## ELECTRICAL SPECIFICATION @ 25°C

Turns Ratio (±2%)	Inductance (mH ± 10%)	Leakage Inductance (µH MAX)	Longitudinal Balance (dB MIN @ 135Ω) 5 – 196 kHz	Return Loss (dB MIN) 40 – 200 kHz	DC Resistance (Ω MAX)		Isolation Voltage (Vrms)
	Line Side (10-7)(w/ 6-9 shorted)	(10-7)(w/ 6-9, 1-4, 2-5 shorted)			Line Side (10-6)=(9-7)	Chip Side (1-5)(w/ 2-4 shorted)	
1:1.8	3.00	30	50	16.5	1.5	2.3	2000