Triple inverter with open-drain output Rev. 8 — 9 August 2010

Product data sheet

1. **General description**

The 74LVC3G06 provides three inverting buffers.

The output of this device is an open drain and can be connected to other open-drain outputs to implement active-LOW wired-OR or active-HIGH wired-AND functions.

Inputs can be driven from either 3.3 V or 5 V devices. This feature allows the use of this device in a mixed 3.3 V and 5 V environment.

Schmitt-trigger action at all inputs makes the circuit tolerant for slower input rise and fall time.

This device is fully specified for partial power-down applications using I_{OFF}. The I_{OFF} circuitry disables the output, preventing a damaging backflow current through the device when it is powered down.

Features and benefits 2.

- Wide supply voltage range from 1.65 V to 5.5 V
- 5 V tolerant input/output for interfacing with 5 V logic
- High noise immunity
- Complies with JEDEC standard:
 - ◆ JESD8-7 (1.65 V to 1.95 V)
 - ◆ JESD8-5 (2.3 V to 2.7 V)
 - ◆ JESD8-B/JESD36 (2.7 V to 3.6 V)
- ESD protection:
 - ◆ HBM JESD22-A114F exceeds 2000 V
 - MM JESD22-A115-A exceeds 200 V
- -24 mA output drive ($V_{CC} = 3.0 \text{ V}$)
- CMOS low power consumption
- Latch-up performance exceeds 250 mA
- Direct interface with TTL levels
- Inputs accept voltages up to 5 V
- Multiple package options
- Specified from -40 °C to +85 °C and -40 °C to +125 °C



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3. Ordering information

Table 1. Ordering information

Type number	Package							
	Temperature range	Name	Description	Version				
74LVC3G06DP	–40 °C to +125 °C	TSSOP8	plastic thin shrink small outline package; 8 leads; body width 3 mm; lead length 0.5 mm	SOT505-2				
74LVC3G06DC	–40 °C to +125 °C	VSSOP8	plastic very thin shrink small outline package; 8 leads; body width 2.3 mm	SOT765-1				
74LVC3G06GT	–40 °C to +125 °C	XSON8	plastic extremely thin small outline package; no leads; 8 terminals; body 1 \times 1.95 \times 0.5 mm	SOT833-1				
74LVC3G06GF	–40 °C to +125 °C	XSON8	extremely thin small outline package; no leads; 8 terminals; body $1.35 \times 1 \times 0.5$ mm	SOT1089				
74LVC3G06GD	–40 °C to +125 °C	XSON8U	plastic extremely thin small outline package; no leads; 8 terminals; UTLP based; body $3\times2\times0.5$ mm	SOT996-2				
74LVC3G06GM	–40 °C to +125 °C	XQFN8U	plastic extremely thin quad flat package; no leads; 8 terminals; UTLP based; body $1.6 \times 1.6 \times 0.5$ mm	SOT902-1				
74LVC3G06GN	–40 °C to +125 °C	XSON8	extremely thin small outline package; no leads; 8 terminals; body 1.2 \times 1.0 \times 0.35 mm	SOT1116				
74LVC3G06GS	–40 °C to +125 °C	XSON8	extremely thin small outline package; no leads; 8 terminals; body $1.35 \times 1.0 \times 0.35$ mm	SOT1203				

4. Marking

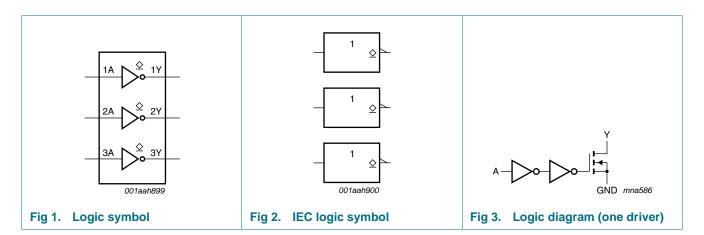
Table 2. Marking codes

Type number	Marking code ^[1]
74LVC3G06DP	V06
74LVC3G06DC	V06
74LVC3G06GT	V06
74LVC3G06GF	V6
74LVC3G06GD	V06
74LVC3G06GM	V06
74LVC3G06GN	V6
74LVC3G06GS	V6

^[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

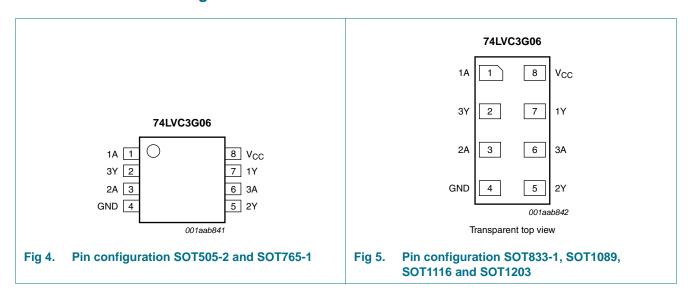
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5. Functional diagram

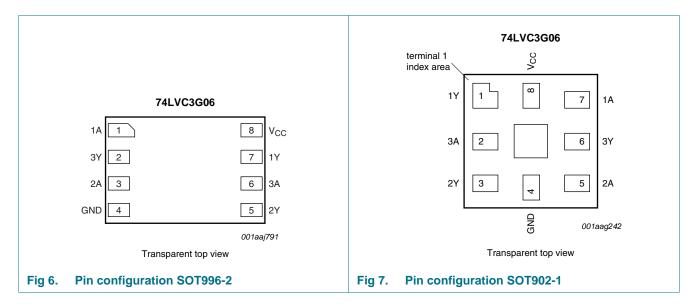


6. Pinning information

6.1 Pinning



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6.2 Pin description

Table 3. Pin description

Symbol	Pin	Description	
	SOT505-2, SOT765-1, SOT833-1, SOT1089, SOT996-2, SOT1116 and SOT1203	SOT902-1	
1A, 2A, 3A	1, 3, 6	7, 5, 2	data input
1Y, 2Y, 3Y	7, 5, 2	1, 3, 6	data output
GND	4	4	ground (0 V)
V _{CC}	8	8	supply voltage

7. Functional description

Table 4. Function table[1]

Input nA	Output nY
L	Z
Н	L

^[1] H = HIGH voltage level; L = LOW voltage level; Z = high-impedance OFF-state.

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8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{CC}	supply voltage		-0.5	+6.5	V
I _{IK}	input clamping current	$V_{l} < 0 V$	-50	-	mA
VI	input voltage		<u>[1]</u> –0.5	+6.5	V
I _{OK}	output clamping current	V _O < 0 V	-50	-	mA
Vo	output voltage	Active mode	<u>[1]</u> –0.5	+6.5	V
		Power-down mode	<u>[1][2]</u> –0.5	+6.5	V
Io	output current	$V_O = 0 V \text{ to } 6.5 V$	-	50	mA
I _{CC}	supply current		-	100	mA
I_{GND}	ground current		-100	-	mA
T _{stg}	storage temperature		-65	+150	°C
P _{tot}	total power dissipation	$T_{amb} = -40 ^{\circ}\text{C} \text{ to } +125 ^{\circ}\text{C}$	<u>[3]</u> _	250	mW

^[1] The minimum input and output voltage ratings may be exceeded if the input and output current ratings are observed.

9. Recommended operating conditions

Table 6. Operating conditions

Symbol	Parameter	Conditions	Min	Max	Unit
V_{CC}	supply voltage		1.65	5.5	V
V_{I}	input voltage		0	5.5	V
Vo	output voltage	Active mode	0	5.5	V
		Power-down mode; V _{CC} = 0 V	0	5.5	V
T _{amb}	ambient temperature		-40	+125	°C
$\Delta t/\Delta V$	input transition rise and fall rate	$V_{CC} = 1.65 \text{ V to } 2.7 \text{ V}$	-	20	ns/V
		V _{CC} = 2.7 V to 5.5 V	-	10	ns/V

^[2] When $V_{CC} = 0 \text{ V}$ (Power-down mode), the output voltage can be 5.5 V in normal operation.

^[3] For TSSOP8 and VSSOP8 packages: above 110 °C the value of P_{tot} derates linearly at 8.0 mW/K. For XSON8, XSON8U and XQFN8U packages: above 118 °C the value of P_{tot} derates linearly with 7.8 mW/K.

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10. Static characteristics

Table 7. Static characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

voltage $V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$ 0.7 $V_{CC} = 2.7 \text{ V to } 3.6 \text{ V}$ - 0.8		, ,		. •	,			
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$\begin{tabular}{l l l l l l l l l l l l l l l l l l l $	T _{amb} = -40	°C to +85 °C[1]						
$V_{CC} = 2.7 \ V \ to \ 3.6 \ V \\ V_{CC} = 2.7 \ V \ to \ 3.6 \ V \\ V_{CC} = 4.5 \ V \ to \ 5.5 \ V \\ V_{CC} = 4.5 \ V \ to \ 5.5 \ V \\ V_{CC} = 4.5 \ V \ to \ 5.5 \ V \\ V_{CC} = 4.5 \ V \ to \ 5.5 \ V \\ V_{CC} = 2.3 \ V \ to \ 1.95 \ V \\ V_{CC} = 2.3 \ V \ to \ 1.95 \ V \\ V_{CC} = 2.3 \ V \ to \ 1.95 \ V \\ V_{CC} = 2.3 \ V \ to \ 1.95 \ V \\ V_{CC} = 2.3 \ V \ to \ 1.95 \ V \\ V_{CC} = 2.3 \ V \ to \ 1.95 \ V \\ V_{CC} = 2.3 \ V \ to \ 1.95 \ V \\ V_{CC} = 2.3 \ V \ to \ 1.95 \ V \\ V_{CC} = 4.5 \ V \ to \ 5.5 \ V \\ V_{CC} = 4.5 \ V \ to \ 5.5 \ V \\ V_{CC} = 4.5 \ V \ to \ 5.5 \ V \\ V_{CC} = 4.5 \ V \ to \ 5.5 \ V \\ V_{CC} = 4.5 \ V \ to \ 5.5 \ V \\ V_{CC} = 4.5 \ V \ to \ 5.5 \ V \\ V_{CC} = 4.5 \ V \ to \ 5.5 \ V \\ V_{CC} = 4.5 \ V \ to \ 5.5 \ V \\ V_{CC} = 1.65 \ V \ to \ 5.5 \ V \\ V_{CC} = 1.65 \ V \ to \ 5.5 \ V \\ V_{CC} = 2.3 \ V \\ V_{CC} = 4.5 \ V \\ V_$	V _{IH}	HIGH-level input	V _{CC} = 1.65 V to 1.95 V		$0.65 \times V_{CC}$	-	-	V
$V_{CC} = 4.5 \ V \ to 5.5 \ V \\ V_{CC} = 4.5 \ V \ to 5.5 \ V \\ V_{CC} = 1.65 \ V \ to 1.95 \ V \\ V_{CC} = 2.3 \ V \ to 1.95 \ V \\ V_{CC} = 2.3 \ V \ to 1.95 \ V \\ V_{CC} = 2.3 \ V \ to 1.95 \ V \\ V_{CC} = 2.3 \ V \ to 1.95 \ V \\ V_{CC} = 2.3 \ V \ to 1.95 \ V \\ V_{CC} = 2.3 \ V \ to 1.95 \ V \\ V_{CC} = 2.3 \ V \ to 1.95 \ V \\ V_{CC} = 2.3 \ V \ to 1.95 \ V \\ V_{CC} = 2.3 \ V \ to 1.95 \ V \\ V_{CC} = 2.3 \ V \ to 1.95 \ V \\ V_{CC} = 2.3 \ V \ to 1.95 \ V \\ V_{CC} = 2.3 \ V \ to 1.95 \ V \\ V_{CC} = 1.65 \ V \ to 1.95 \ V \\ V_{CC} = 1.65 \ V \ to 1.95 \ V \\ V_{CC} = 1.65 \ V \\ V_$		voltage	V _{CC} = 2.3 V to 2.7 V		1.7	-	-	V
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$			V _{CC} = 2.7 V to 3.6 V		2.0	-	-	V
$\begin{tabular}{l l l l l l l l l l l l l l l l l l l $			V _{CC} = 4.5 V to 5.5 V		$0.7 \times V_{CC}$	-	-	V
$V_{CC} = 2.7 \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \$	V _{IL}		V _{CC} = 1.65 V to 1.95 V		-	-	$0.35 \times V_{CC}$	V
$V_{\text{CC}} = 4.5 \ \text{V to } 5.5 \ \text{V} \qquad - \qquad - \qquad 0.3 \times V_{\text{CC}}$ $V_{\text{OL}} \qquad V_{\text{OL}} \qquad V_{\text{I}} \qquad V_{\text{I}} = V_{\text{IH}} \text{ or } V_{\text{IL}}$ $V_{\text{I}} = V_{\text{IH}} \text{ or } V_{\text{IL}}$ $I_{\text{O}} = 100 \ \mu\text{A; } V_{\text{CC}} = 1.65 \ \text{V to } 5.5 \ \text{V} \qquad - \qquad - \qquad 0.10$ $I_{\text{O}} = 4 \ \text{mA; } V_{\text{CC}} = 1.65 \ \text{V} \qquad - \qquad - \qquad 0.45$ $I_{\text{O}} = 8 \ \text{mA; } V_{\text{CC}} = 2.3 \ \text{V} \qquad - \qquad - \qquad 0.30$ $I_{\text{O}} = 12 \ \text{mA; } V_{\text{CC}} = 2.7 \ \text{V} \qquad - \qquad - \qquad 0.40$ $I_{\text{O}} = 24 \ \text{mA; } V_{\text{CC}} = 2.7 \ \text{V} \qquad - \qquad - \qquad 0.55$ $I_{\text{O}} = 32 \ \text{mA; } V_{\text{CC}} = 3.0 \ \text{V} \qquad - \qquad - \qquad 0.55$ $I_{\text{I}} \qquad \text{input leakage current} \qquad V_{\text{I}} = 5.5 \ \text{V or GND; } \qquad - \qquad - \qquad 0.55$ $I_{\text{OZ}} \qquad \text{OFF-state output current} \qquad V_{\text{I}} = 5.5 \ \text{V or GND; } \qquad - \qquad \pm 0.1 \qquad \pm 10$ $V_{\text{CC}} = 0 \ \text{V to } 5.5 \ \text{V}$ $I_{\text{OFF}} \qquad \text{power-off leakage current} \qquad V_{\text{I}} = V_{\text{IH}} \text{ or } V_{\text{IL}}; V_{\text{O}} = V_{\text{CC}} \text{ or GND; } \qquad - \qquad \pm 0.1 \qquad \pm 10$ $I_{\text{CC}} \qquad \text{supply current} \qquad V_{\text{I}} = 5.5 \ \text{V or GND; } I_{\text{O}} = 0 \ \text{A; } \qquad - \qquad 0.1 \qquad 10$ $V_{\text{CC}} = 1.65 \ \text{V to } 5.5 \ \text{V}$ $\Delta I_{\text{CC}} \qquad \text{additional supply per pin; } V_{\text{I}} = V_{\text{CC}} - 0.6 \ \text{V; } I_{\text{O}} = 0 \ \text{A; } \qquad - \qquad 5 \qquad 500$		voltage	V _{CC} = 2.3 V to 2.7 V		-	-	0.7	V
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$			V _{CC} = 2.7 V to 3.6 V		-	-	0.8	V
$\label{eq:higher_relation} \begin{tabular}{llllllll} Voltage & $I_O = 100 \ \mu A; \ V_{CC} = 1.65 \ V \ to 5.5 \ V & - & - & 0.10 \\ \hline $I_O = 4 \ mA; \ V_{CC} = 2.3 \ V & - & - & 0.30 \\ \hline $I_O = 8 \ mA; \ V_{CC} = 2.3 \ V & - & - & 0.30 \\ \hline $I_O = 12 \ mA; \ V_{CC} = 2.7 \ V & - & - & 0.40 \\ \hline $I_O = 12 \ mA; \ V_{CC} = 3.0 \ V & - & - & 0.55 \\ \hline $I_O = 32 \ mA; \ V_{CC} = 3.0 \ V & - & - & 0.55 \\ \hline $I_O = 32 \ mA; \ V_{CC} = 4.5 \ V & - & - & 0.55 \\ \hline $I_O = 32 \ mA; \ V_{CC} = 4.5 \ V & - & - & 0.55 \\ \hline $I_O = 32 \ mA; \ V_{CC} = 4.5 \ V & - & - & 0.55 \\ \hline $I_O = 32 \ mA; \ V_{CC} = 4.5 \ V & - & - & 0.55 \\ \hline $I_O = 32 \ mA; \ V_{CC} = 0.5 \ V \\ \hline $I_O = 0 \ V \ to 5.5 \ V \\ \hline $I_O = 0 \ V \ to 5.5 \ V \\ \hline $I_O = 0 \ V \ to 5.5 \ V \\ \hline $I_O = 0 \ V_{CC} \ to \ S.5 \ V \\ \hline $I_O = 0 \ V_{CC} \ to \ S.5 \ V \\ \hline $I_O = 0 \ V_{CC} \ to \ S.5 \ V \\ \hline $I_O = 0 \ V_{CC} \ to \ S.5 \ V \\ \hline $I_O = 0 \ A; \\ \hline $V_{CC} = 1.65 \ V \ to \ 5.5 \ V \\ \hline $I_O = 0 \ A; \\ \hline $V_{CC} = 2.3 \ V \ to \ 5.5 \ V \\ \hline \end{tabular} $			V _{CC} = 4.5 V to 5.5 V		-	-	$0.3 \times V_{CC}$	V
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	V _{OL}		$V_I = V_{IH}$ or V_{IL}					
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$			$I_O = 100 \ \mu A; \ V_{CC} = 1.65 \ V \ to \ 5.5 \ V$		-	-	0.10	V
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$			$I_O = 4 \text{ mA}; V_{CC} = 1.65 \text{ V}$		-	-	0.45	V
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$			$I_{O} = 8 \text{ mA}; V_{CC} = 2.3 \text{ V}$		-	-	0.30	V
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$			$I_{O} = 12 \text{ mA}; V_{CC} = 2.7 \text{ V}$		-	-	0.40	V
$\begin{array}{cccccccccccccccccccccccccccccccccccc$			$I_{O} = 24 \text{ mA}; V_{CC} = 3.0 \text{ V}$		-	-	0.55	V
$V_{CC} = 0 \text{ V to } 5.5 \text{ V}$ $I_{OZ} \qquad \text{OFF-state output current} \qquad V_I = V_{IH} \text{ or } V_{IL}; \ V_O = V_{CC} \text{ or GND}; \qquad - \qquad \pm 0.1 \qquad \pm 10$ $V_{CC} = 5.5 \text{ V}$ $I_{OFF} \qquad \text{power-off leakage current} \qquad V_I \text{ or } V_O = 5.5 \text{ V}; \ V_{CC} = 0 \text{ V} \qquad - \qquad \pm 0.1 \qquad \pm 10$ $I_{CC} \qquad \text{supply current} \qquad V_I = 5.5 \text{ V or GND}; \ I_O = 0 \text{ A}; \qquad - \qquad 0.1 \qquad 10$ $V_{CC} = 1.65 \text{ V to } 5.5 \text{ V}$ $\Delta I_{CC} \qquad \text{additional supply current} \qquad \text{per pin; } V_I = V_{CC} - 0.6 \text{ V}; \ I_O = 0 \text{ A}; \qquad \frac{[2]}{2} - \qquad 5 \qquad 500$ $V_{CC} = 2.3 \text{ V to } 5.5 \text{ V}$			$I_{O} = 32 \text{ mA}; V_{CC} = 4.5 \text{ V}$		-	-	0.55	V
$ \begin{array}{cccccccccccccccccccccccccccccccccccc$	I _I	input leakage current		[2]	-	±0.1	±5	μΑ
$ \begin{array}{c} \text{current} \\ \text{I}_{CC} \\ \text{Supply current} \\ \text{V}_{I} = 5.5 \text{ V or GND; I}_{O} = 0 \text{ A;} \\ \text{V}_{CC} = 1.65 \text{ V to } 5.5 \text{ V} \\ \\ \Delta I_{CC} \\ \text{additional supply} \\ \text{current} \\ \text{V}_{CC} = 2.3 \text{ V to } 5.5 \text{ V} \\ \end{array} $	l _{OZ}	•			-	±0.1	±10	μΑ
$V_{CC} = 1.65 \text{ V to } 5.5 \text{ V}$ $\Delta I_{CC} \qquad \text{additional supply} \qquad \text{per pin; } V_I = V_{CC} - 0.6 \text{ V; } I_O = 0 \text{ A;} \qquad 5 \qquad 500$ $\text{current} \qquad V_{CC} = 2.3 \text{ V to } 5.5 \text{ V}$	I _{OFF}	•	V_{I} or $V_{O} = 5.5 \text{ V}; V_{CC} = 0 \text{ V}$		-	±0.1	±10	μΑ
current $V_{CC} = 2.3 \text{ V to } 5.5 \text{ V}$	I _{CC}	supply current			-	0.1	10	μΑ
C _I input capacitance - 2.5 -	Δl _{CC}			[2]	-	5	500	μΑ
	Cı	input capacitance			-	2.5	-	pF

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 Table 7.
 Static characteristics ...continued

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
T _{amb} = -40	°C to +125 °C					
V _{IH}	HIGH-level input	V _{CC} = 1.65 V to 1.95 V	$0.65 \times V_{CC}$	-	-	V
	voltage	V _{CC} = 2.3 V to 2.7 V	1.7	-	-	V
		V _{CC} = 2.7 V to 3.6 V	2.0	-	-	V
		V _{CC} = 4.5 V to 5.5 V	$0.7 \times V_{CC}$	-	-	V
V _{IL}	LOW-level input	V _{CC} = 1.65 V to 1.95 V	-	-	$0.35 \times V_{CC}$	V
	voltage	V _{CC} = 2.3 V to 2.7 V	-	-	0.7	V
		V _{CC} = 2.7 V to 3.6 V	-	-	0.8	V
		V _{CC} = 4.5 V to 5.5 V	-	-	$0.3 \times V_{CC}$	V
V _{OL}	LOW-level output voltage	$V_I = V_{IH}$ or V_{IL}				
		I_{O} = 100 μ A; V_{CC} = 1.65 V to 5.5 V	-	-	0.10	V
		$I_{O} = 4 \text{ mA}; V_{CC} = 1.65 \text{ V}$	-	-	0.70	V
		$I_{O} = 8 \text{ mA}; V_{CC} = 2.3 \text{ V}$	-	-	0.45	V
		$I_{O} = 12 \text{ mA}; V_{CC} = 2.7 \text{ V}$	-	-	0.60	V
		$I_{O} = 24 \text{ mA}; V_{CC} = 3.0 \text{ V}$	-	-	0.80	V
		$I_{O} = 32 \text{ mA}; V_{CC} = 4.5 \text{ V}$	-	-	0.80	V
l _l	input leakage current	V _I = 5.5 V or GND; V _{CC} = 0 V to 5.5 V	-	-	±20	μΑ
l _{OZ}	OFF-state output current	$V_I = V_{IH}$ or V_{IL} ; $V_O = V_{CC}$ or GND; $V_{CC} = 5.5 \text{ V}$	-	-	±10	μΑ
OFF	power-off leakage current	V_I or $V_O = 5.5$ V; $V_{CC} = 0$ V	-	-	±20	μΑ
lcc	supply current	$V_I = 5.5 \text{ V or GND; } I_O = 0 \text{ A;}$ $V_{CC} = 1.65 \text{ V to } 5.5 \text{ V}$	-	-	40	μА
Δl _{CC}	additional supply current	per pin; $V_1 = V_{CC} - 0.6 \text{ V}$; $I_O = 0 \text{ A}$; $V_{CC} = 2.3 \text{ V}$ to 5.5 V	-	-	5000	μΑ

^[1] All typical values are measured at T_{amb} = 25 °C.

^[2] These typical values are measured at $V_{CC} = 3.3 \text{ V}$.

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11. Dynamic characteristics

Table 8. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V). For test circuit see Figure 9.

Symbol	Parameter			–40 °C to +85 °C			-40 °C to +125 °C		Unit
				Min	Typ[1]	Max	Min	Max	
t_{pd}	propagation delay	nA to nY; see Figure 8	[2]						
		$V_{CC} = 1.65 \text{ V to } 1.95 \text{ V}$		1.0	2.6	6.5	1.0	8.2	ns
		$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$		0.5	1.6	3.9	0.5	4.9	ns
		V _{CC} = 2.7 V		1.0	2.2	4.2	1.0	5.3	ns
		$V_{CC} = 3.0 \text{ V to } 3.6 \text{ V}$		0.5	2.0	3.4	0.5	4.3	ns
		$V_{CC} = 4.5 \text{ V to } 5.5 \text{ V}$		0.5	1.4	2.9	0.5	3.7	ns
C_{PD}	power dissipation capacitance	$V_I = GND \text{ to } V_{CC}; V_{CC} = 3.3 \text{ V}$	[3]	-	5.9	-	-	-	pF

^[1] Typical values are measured at $T_{amb} = 25$ °C and $V_{CC} = 1.8$ V, 2.5 V, 2.7 V, 3.3 V and 5.0 V respectively.

[3] C_{PD} is used to determine the dynamic power dissipation (P_D in μW).

$$P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \Sigma (C_L \times V_{CC}^2 \times f_o) \text{ where:}$$

 f_i = input frequency in MHz;

f_o = output frequency in MHz;

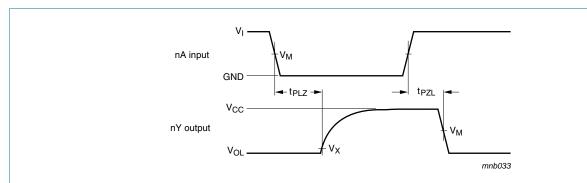
C_L = output load capacitance in pF;

V_{CC} = supply voltage in V;

N = number of inputs switching;

 $\Sigma (C_L \times V_{CC}{}^2 \times f_o) = sum \ of \ outputs.$

12. Waveforms



Measurement points are given in Table 9.

 $\ensuremath{\text{V}_{\text{OL}}}$ is the typical output voltage level that occurs with the output load.

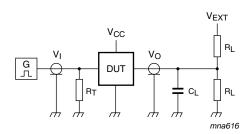
Fig 8. The input (nA) to output (nY) propagation delays

^[2] t_{pd} is the same as t_{PLZ} and t_{PZL} .

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Table 9. Measurement points

Supply voltage	Input	Output	Output		
V _{CC}	V _M	V _M	V _X		
1.65 V to 1.95 V	$0.5 \times V_{CC}$	$0.5 \times V_{CC}$	V _{OL} + 0.15 V		
2.3 V to 2.7 V	$0.5 \times V_{CC}$	$0.5 \times V_{CC}$	V _{OL} + 0.15 V		
2.7 V	1.5 V	1.5 V	V _{OL} + 0.3 V		
3.0 V to 3.6 V	1.5 V	1.5 V	V _{OL} + 0.3 V		
4.5 V to 5.5 V	$0.5 \times V_{CC}$	$0.5 \times V_{CC}$	V _{OL} + 0.3 V		



Test data is given in Table 10.

Definitions for test circuit:

R_L = Load resistance.

 C_L = Load capacitance including jig and probe capacitance.

 R_T = Termination resistance should be equal to the output impedance Z_0 of the pulse generator.

 V_{EXT} = External voltage for measuring switching times.

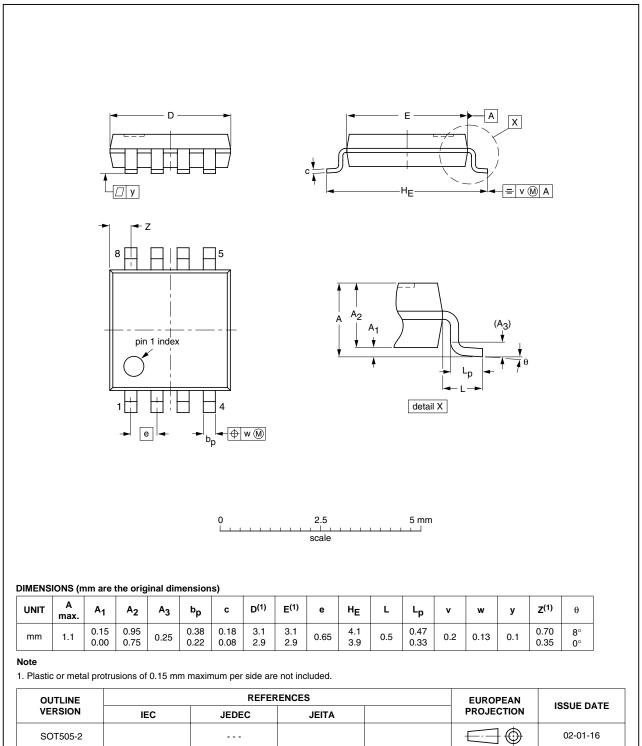
Fig 9. Test circuit for measuring switching times

Table 10. Test data

Supply voltage	Input	Input		Load		
V _{CC}	VI	t _r , t _f	CL	R_L	t_{PZL}, t_{PLZ}	
1.65 V to 1.95 V	V_{CC}	\leq 2.0 ns	30 pF	1 kΩ	$2 \times V_{CC}$	
2.3 V to 2.7 V	V _{CC}	≤ 2.0 ns	30 pF	500Ω	$2 \times V_{CC}$	
2.7 V	2.7 V	≤ 2.5 ns	50 pF	500Ω	6 V	
3.0 V to 3.6 V	2.7 V	≤ 2.5 ns	50 pF	500Ω	6 V	
4.5 V to 5.5 V	V _{CC}	≤ 2.5 ns	50 pF	500Ω	$2 \times V_{CC}$	

13. Package outline

TSSOP8: plastic thin shrink small outline package; 8 leads; body width 3 mm; lead length 0.5 mm

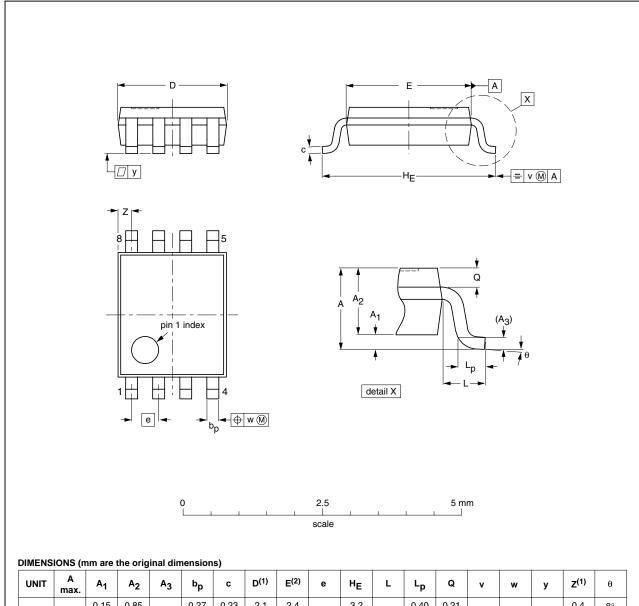


OUTLINE		REFERENCES			EUROPEAN	ISSUE DATE
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE
SOT505-2						02-01-16

Fig 10. Package outline SOT505-2 (TSSOP8)

VSSOP8: plastic very thin shrink small outline package; 8 leads; body width 2.3 mm

SOT765-1



UNIT	A max.	A ₁	A ₂	А3	bp	С	D ⁽¹⁾	E ⁽²⁾	е	HE	L	Lp	Q	v	w	у	Z ⁽¹⁾	θ
mm	1	0.15 0.00	0.85 0.60	0.12	0.27 0.17	0.23 0.08	2.1 1.9	2.4 2.2	0.5	3.2 3.0	0.4	0.40 0.15	0.21 0.19	0.2	0.13	0.1	0.4 0.1	8° 0°

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- 1. Plastic or metal protrusions of 0.15 mm maximum per side are not included.
- 2. Plastic or metal protrusions of 0.25 mm maximum per side are not included.

OUTLINE		REFER	EUROPEAN	ISSUE DATE			
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE	
SOT765-1		MO-187				02-06-07	

Fig 11. Package outline SOT765-1 (VSSOP8)

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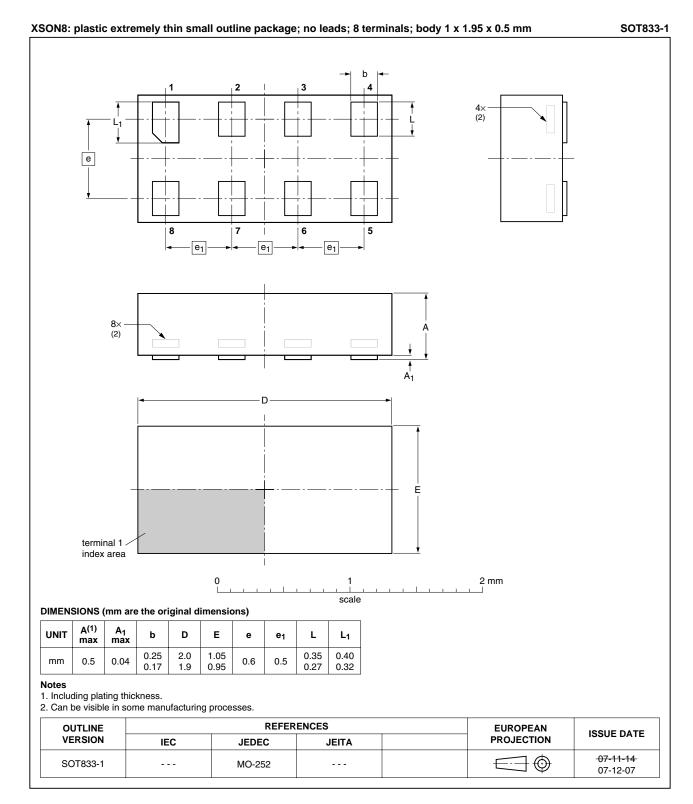


Fig 12. Package outline SOT833-1 (XSON8)

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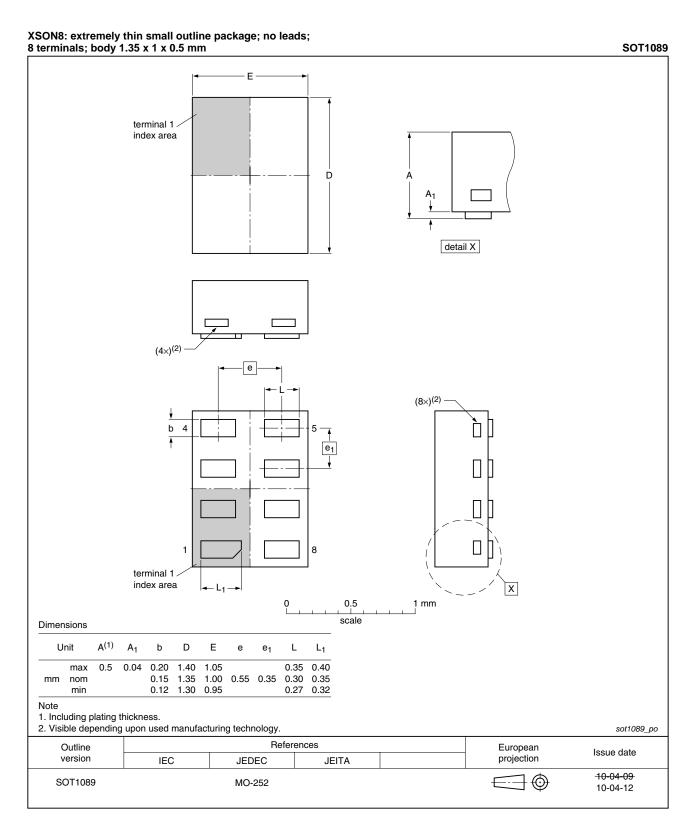


Fig 13. Package outline SOT1089 (XSON8)

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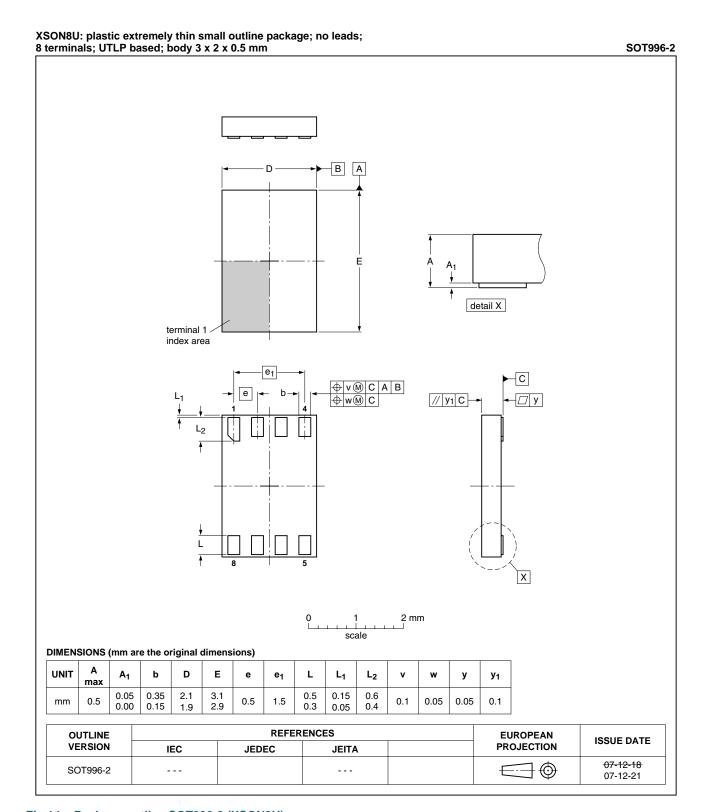


Fig 14. Package outline SOT996-2 (XSON8U)

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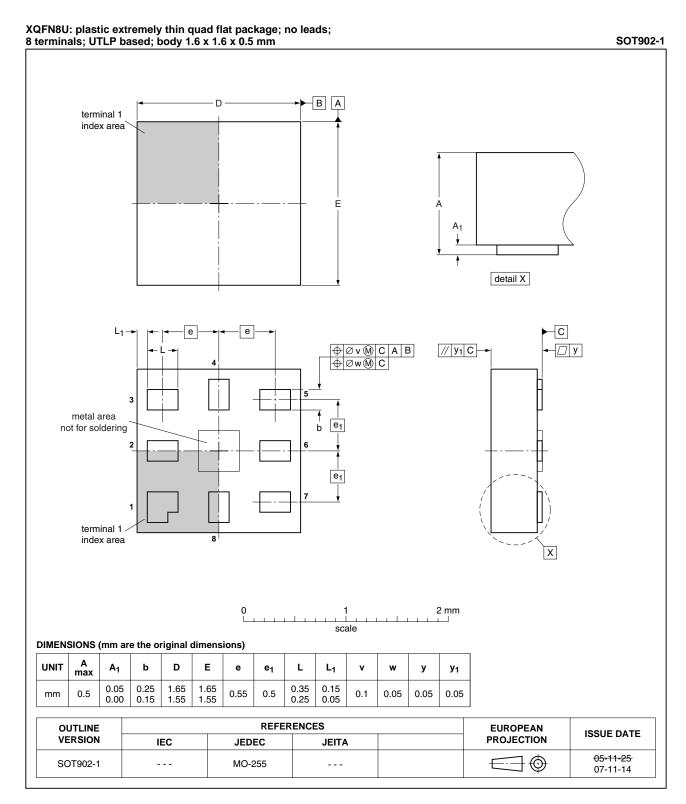


Fig 15. Package outline SOT902-1 (XQFN8U)

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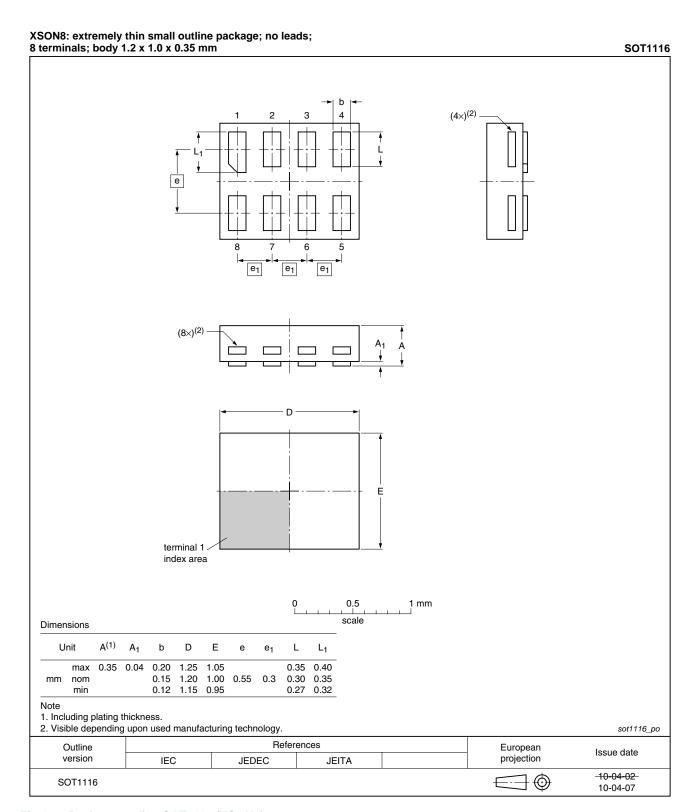


Fig 16. Package outline SOT1116 (XSON8)

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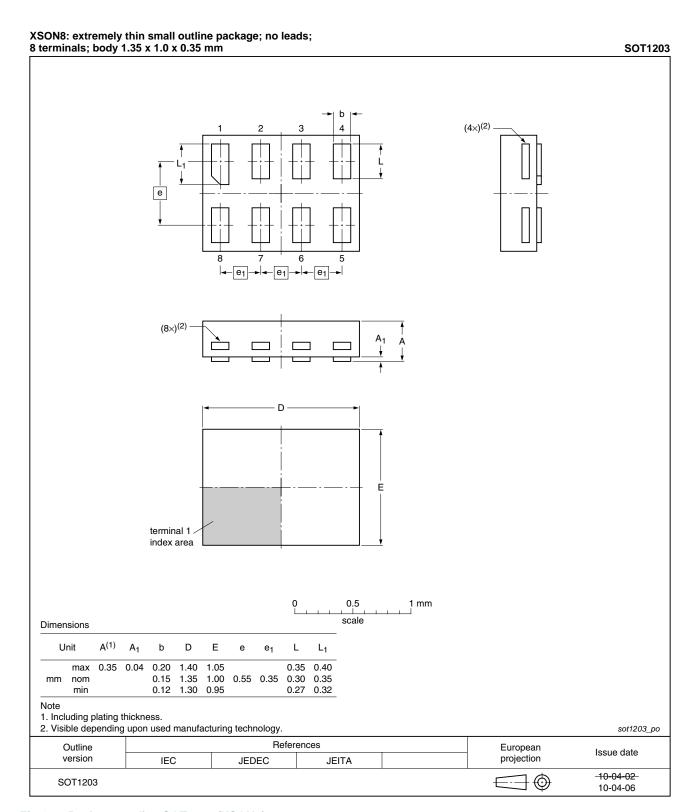


Fig 17. Package outline SOT1203 (XSON8)

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14. Abbreviations

Table 11. Abbreviations

Acronym	Description			
CMOS	Complementary Metal-Oxide Semiconductor			
DUT	Device Under Test			
ESD	ElectroStatic Discharge			
НВМ	Human Body Model			
MM	Machine Model			
TTL	Transistor-Transistor Logic			

15. Revision history

Table 12. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
74LVC3G06 v.8	20100809	Product data sheet	-	74LVC3G06 v.7
Modifications:	 Added type r 	number 74LVC3G06GF (SOT1	089/XSON8 packag	e).
	 Added type r 	number 74LVC3G06GN (SOT1	116/XSON8 package	e).
	 Added type r 	number 74LVC3G06GS (SOT1	203/XSON8 packag	e).
74LVC3G06 v.7	20090312	Product data sheet	-	74LVC3G06 v.6
74LVC3G06 v.6	20080403	Product data sheet	-	74LVC3G06 v.5
74LVC3G06 v.5	20070521	Product data sheet	-	74LVC3G06 v.4
74LVC3G06 v.4	20060302	Product data sheet	-	74LVC3G06 v.3
74LVC3G06 v.3	20050201	Product data sheet	-	74LVC3G06 v.2
74LVC3G06 v.2	20041021	Product data sheet	-	74LVC3G06 v.1
74LVC3G06 v.1	20040607	Product data sheet	-	-

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16. Legal information

16.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions"
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