Zibo Seno Electronic Engineering Co., Ltd.



ABS05 - ABS10

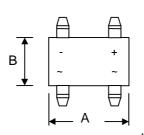


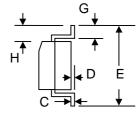


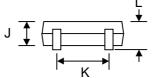
1.0A SURFACE MOUNT GLASS PASSIVATED BRIDGE RECTIFIER

Features

- Glass Passivated Die Construction
- Low Forward Voltage Drop
- High Current Capability
- High Surge Current Capability
- Designed for Surface Mount Application
- Plastic Material UL Flammability 94V-O







| ABS | | | | | | | | |
|----------------------|------|------|--|--|--|--|--|--|
| Dim | Min | Max | | | | | | |
| Α | 4.80 | 5.30 | | | | | | |
| В | 4.20 | 4.60 | | | | | | |
| С | 0.15 | 0.25 | | | | | | |
| D | _ | 0.20 | | | | | | |
| Е | 6.00 | 6.80 | | | | | | |
| G | 0.30 | 0.70 | | | | | | |
| H | 0.90 | 1.10 | | | | | | |
| J | 1 | 1.50 | | | | | | |
| K | 3.80 | 4.20 | | | | | | |
| L | 1.22 | 1.72 | | | | | | |
| All Dimensions in mm | | | | | | | | |

Mechanical Data

• Case: SOPA-4, ABS, Molded Plastic

 Terminals: Plated Leads Solderable per MIL-STD-202, Method 208

Polarity: As Marked on Case

Mounting Position: AnyMarking: Type Number

Lead Free: For RoHS / Lead Free Version

Maximum Ratings and Electrical Characteristics @TA=25°C unless otherwise specified

Single Phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

| Characteristic | Symbol | ABS05 | ABS1 | ABS2 | ABS4 | ABS6 | ABS8 | ABS10 | Unit |
|---|--------------------|-------------|------|------|------|------|------|-------|------------------|
| Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage | VRRM VRWM VR | 50 | 100 | 200 | 400 | 600 | 800 | 1000 | ٧ |
| RMS Reverse Voltage | VR(RMS) | 35 | 70 | 140 | 280 | 420 | 560 | 700 | V |
| Average Rectified Output Current (Note 1) @T _A = 30°C | lo | 1.0 | | | | | | | Α |
| Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method) | IFSM | 35 | | | | | | | А |
| I ² t Rating for Fusing (t < 8.3ms) | l ² t | 5.0 | | | | | | | A ² s |
| Forward Voltage per element @I _F = 1.0A | VFM | 1.1 | | | | | | | V |
| Peak Reverse Current $@T_A = 25^{\circ}C$ At Rated DC Blocking Voltage $@T_A = 125^{\circ}C$ | I IRM | 5.0 500 | | | | | | | μΑ |
| Typical Junction Capacitance per leg (Note 3) | Cj | 13 | | | | | | | pF |
| Typical Thermal Resistance per leg (Note 1) | R θ JA R θ JL | 62.5 25 | | | | | | | °C/W |
| Operating and Storage Temperature Range | Тj, Тsтg | -55 to +150 | | | | | | | °C |

Note: 1. Mounted on aluminum substrate PC board with 1.3mm² solder pad.

2. Measured at 1.0 MHz and applied reverse voltage of 4.0V D.C.

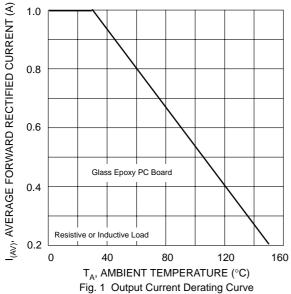
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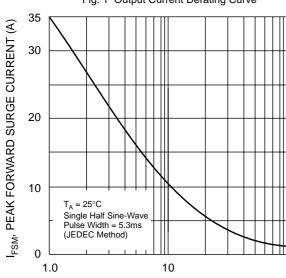


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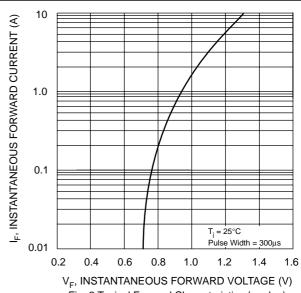








NUMBER OF CYCLES AT 60 Hz Fig. 3 Maximum Peak Forward Surge Current (per leg)



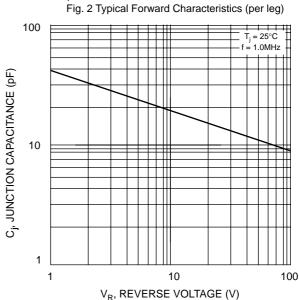


Fig. 4 Typical Junction Capacitance

