

Six Channel Integrated Power Management IC for Handheld Portable Equipment

FEATURES

- Multiple Patents Pending
- Six Integrated Regulators
 - 1.2A PWM Step-Down DC/DC
 - 1.2A PWM Step-Down DC/DC
 - 750mA PWM Step-Down DC/DC
 - 250mA Low Noise LDO
 - 250mA Low Noise LDO
 - 250mA Low Noise LDO
- I²C™ Compatible Serial Interface
 - Programmable Output Voltages
 - Configurable Operating Modes
- Minimal External Components
- 4x4mm, Thin-QFN (TQFN44-24) Package
 - Only 0.75mm Height
 - RoHS Compliant

APPLICATIONS

- Portable Devices and PDAs
- Wireless Handhelds
- DMB Enabled Devices
- GPS Receivers, etc.

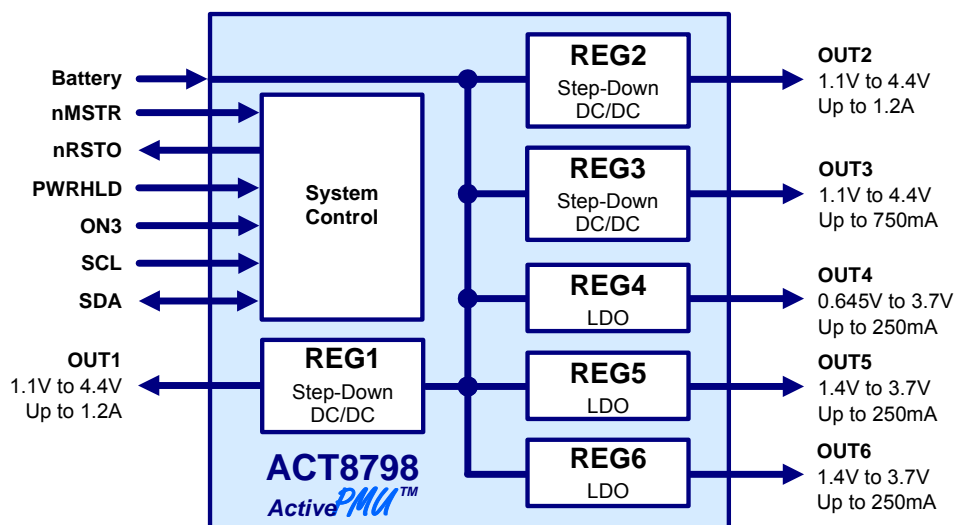
GENERAL DESCRIPTION

The patent-pending ACT8798 is a complete, cost-effective, highly-efficient *ActivePMU™* power management solution that is ideal for a wide range of portable handheld equipment. This device integrates three step-down DC/DC converters and three low dropout linear regulators (LDOs) into a single, thin, space-saving package. An I²C Serial Interface provides programmability for the DC/DC converters and LDOs.

REG1, REG2 and REG3 are fixed-frequency, current-mode PWM step-down DC/DC converters that are optimized for high efficiency and are capable of supplying up to 1.2A, 1.2A and 750mA, respectively. REG4, REG5 and REG6 are low noise, high PSRR linear regulators that are capable of supplying up to 250mA each.

The ACT8798 is available in a tiny 4mm x 4mm 24-pin Thin-QFN package that is just 0.75mm thin.

SYSTEM BLOCK DIAGRAM



PRODUCT OPTIONS

Block	Function	Output Voltage ^①	Capability ^②
REG1	Step-Down DC/DC	1.1V to 4.4V	1.2A
REG2	Step-Down DC/DC		1.2A
REG3	Step-Down DC/DC		750mA
REG4	LDO	0.645V to 3.7V	250mA
REG5	LDO	1.4V to 3.7V	250mA
REG6	LDO		250mA

①: Output voltage options detailed in this table represent standard voltage options, and are available for samples or production orders. Contact Active-Semi for more information regarding semi-custom output voltage combinations.

②: Contact factory for additional available products or custom requirements.

FUNCTIONAL BLOCK DIAGRAM

