#### **Dual N-Channel 30-V (D-S) MOSFET**

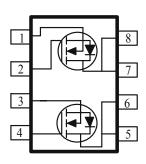
These miniature surface mount MOSFETs utilize High Cell Density process. Low  $r_{DS(on)}$  assures minimal power loss and conserves energy, making this device ideal for use in power management circuitry. Typical applications are PWMDC-DC converters, power management in portable and battery-powered products such as computers, printers, battery charger, telecommunication power system, and telephones power system.

•	Low $r_{DS(on)}$ Provides Higher Efficiency and
	Extends Battery Life

- Miniature SO-8 Surface Mount Package Saves Board Space
- High power and current handling capability
- Low side high current DC-DC Converter applications

PRODUCT SUMMARY				
V <sub>DS</sub> (V)	$r_{DS(on)} m(\Omega)$	$I_{D}(A)$		
30	$34 @ V_{GS} = 10V$	6.9		
30	$41 @ V_{GS} = 4.5V$	6.0		





ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25 °C UNLESS OTHERWISE NOTED)				
Parameter		Symbol	Limit	Units
Drain-Source Voltage			30	V
Gate-Source Voltage		$V_{GS}$	± 20	V
Continuo Dario Commut <sup>a</sup>	$T_A=25^{\circ}C$	ī	± 6.9	
Continuous Drain Current <sup>a</sup>	$T_A=25^{\circ}C$ $T_A=70^{\circ}C$	$^{1}D$	± 5.6	A
Pulsed Drain Current <sup>b</sup>		$I_{DM}$	± 40	
Continuous Source Current (Diode Conduction) <sup>a</sup>		$I_S$	1.7	A
D Dii4iâ	$T_A=25^{\circ}C$	D	2.1	W
Power Dissipation <sup>a</sup>	$T_A=25^{\circ}C$ $T_A=70^{\circ}C$	]¹ D	1.3	, ,,
Operating Junction and Storage Temperature Range		$T_J, T_{stg}$	-55 to 150	°C

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Maximum	Units		
Marina and I madisanda Ambisada	t <= 10 sec	D	62.5	°C/W		
Maximum Junction-to-Ambient <sup>a</sup>	Steady-State	$R_{\theta JA}$	110	°C/W		

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#### Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature

(C)

Downwoton	6 1 1	T C 1'4'	Limits			TT24	
Parameter	Symbol	<b>Test Conditions</b>	Min	Тур	Max	Unit	
Static							
Gate-Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = 250 \text{ uA}$	1				
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			±100	nA	
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}$			1	uA	
Zero Gate Voltage Drain Current	1DSS	$V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$			10	- uA	
On-State Drain Current <sup>A</sup>	$I_{D(on)}$	$V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$	20			A	
Drain-Source On-Resistance <sup>A</sup>		$V_{GS} = 10 \text{ V}, I_D = 6.9 \text{ A}$			34	mΩ	
Drain-Source On-Resistance	r <sub>DS(on)</sub>	$V_{GS} = 4.5 \text{ V}, I_D = 6.0 \text{ A}$			41		
Forward Tranconductance <sup>A</sup>	$g_{\mathrm{fs}}$	$V_{DS} = 15 \text{ V}, I_D = 6.9 \text{ A}$		25		S	
Diode Forward Voltage	$V_{\mathrm{SD}}$	$I_S = 1.7 \text{ A}, V_{GS} = 0 \text{ V}$		0.77		V	
Dynamic <sup>b</sup>							
Total Gate Charge	$Q_{g}$	$V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V},$ $I_{D} = 6.9 \text{ A}$		4.0			
Gate-Source Charge	$Q_{gs}$			1.1		nC	
Gate-Drain Charge	$Q_{gd}$			1.4			
Turn-On Delay Time	$t_{d(on)}$			12			
Rise Time	$t_{\rm r}$	$V_{DD} = 15 \text{ V}, R_L = 15 \Omega, I_D = 1 \text{ A},$		10			
Turn-Off Delay Time	$t_{d(off)}$	$V_{GEN} = 10 \text{ V}$		60		nS	
Fall-Time	$t_{\mathrm{f}}$			15			
Source-Ddrain Reverse Recovery Time	$t_{rr}$	$I_F = 1.7 \text{ A, Di/Dt} = 100 \text{ A/uS}$		50		1	

#### Notes

- a. Pulse test:  $PW \le 300us duty cycle \le 2\%$ .
- b. Guaranteed by design, not subject to production testing.

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## Typical Electrical Characteristics (N-Channel)

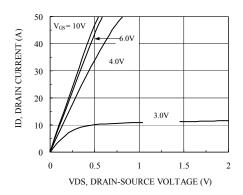


Figure 1. On-Region Characteristics

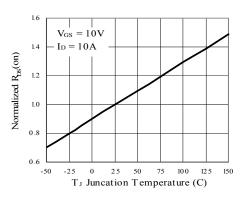


Figure 3. On-Resistance Variation with Temperature

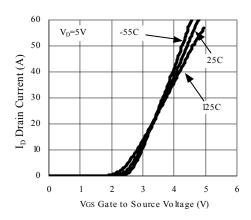


Figure 5. Transfer Characteristics

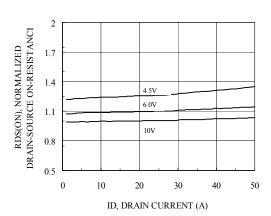


Figure 2. On-Resistance with Drain Current

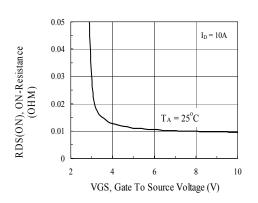


Figure 4. On-Resistance Variation with Gate to Source Voltage

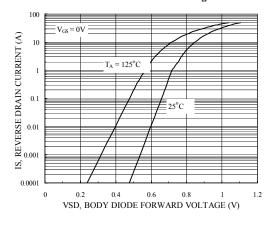


Figure 6. Body Diode Forward Voltage Variation with Source Current and Temperature

f=1MHz

### Typical Electrical Characteristics (N-Channel)

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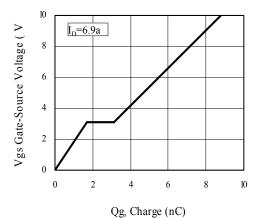
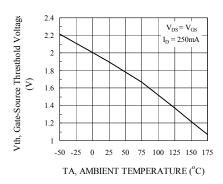


Figure 7. Gate Charge Characteristics



Ciss V<sub>cs</sub> = 0 V V

Figure 8. Capacitance Characteristics

VDS, DRAIN TO SOURCE VOLTAGE (V)

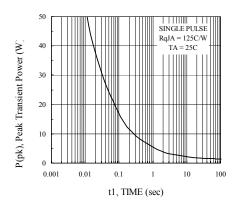


Figure 9. Threshold Vs Ambient Temperature

Figure 10. Single Pulse Maximum Power Dissipation

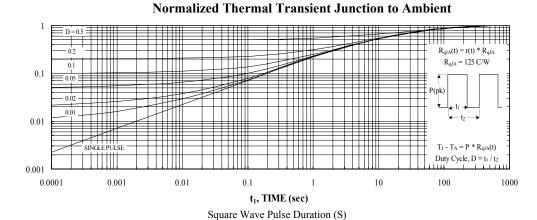
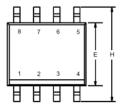
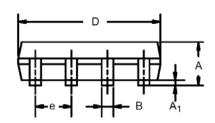


Figure 11. Transient Thermal Response Curve

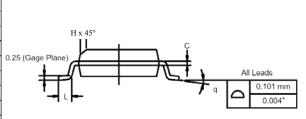
## Package Information

SO-8: 8LEAD





	MILLIN	IETERS	INC	HES
Dim	Min	Max	Min	Max
Α	1.35	1.75	0.053	0.069
A <sub>1</sub>	0.10	0.20	0.004	0.008
В	0.35	0.51	0.014	0.020
С	0.19	0.25	0.0075	0.010
D	4.80	5.00	0.189	0.196
E	3.80	4.00	0.150	0.157
е	1.27	BSC	0.050	BSC
Н	5.80	6.20	0.228	0.244
h	0.25	0.50	0.010	0.020
L	0.50	0.93	0.020	0.037
q	0°	8°	0°	8°



# Ordering information

• AM4920N-T1-XX

- A: Analog Power

- M: MOSFET

- 4920: Part number

- N: N-Channel

- T1: Tape & reel

- XX: Blank: Standard

PF: Leadfree