

# AZ10EL11 AZ100EL11

## ECL/PECL 1:2 Differential Fanout Buffer

### FEATURES

- 265ps Propagation Delay
- 5ps Skew Between Outputs
- High Bandwidth Output Transitions
- 75k $\Omega$  Internal Input Pulldown Resistors
- Direct Replacement for ON Semiconductor MC10EL11 & MC100EL11

### PACKAGE AVAILABILITY

PACKAGE	PART NO.	MARKING
SOIC 8	AZ10EL11D	AZM10EL11
SOIC 8 T&R	AZ10EL11DR1	AZM10EL11
SOIC 8 T&R	AZ10EL11DR2	AZM10EL11
SOIC 8	AZ100EL11D	AZM100EL11
SOIC 8 T&R	AZ100EL11DR1	AZM100EL11
SOIC 8 T&R	AZ100EL11DR2	AZM100EL11
TSSOP 8	AZ10EL11T	AZTEL11
TSSOP 8 T&R	AZ10EL11TR1	AZTEL11
TSSOP 8 T&R	AZ10EL11TR2	AZTEL11
TSSOP 8	AZ100EL11T	AZHEL11
TSSOP 8 T&R	AZ100EL11TR1	AZHEL11
TSSOP 8 T&R	AZ100EL11TR2	AZHEL11

### DESCRIPTION

The AZ10/100EL11 is a differential 1:2 fanout gate. The device is functionally similar to the E111 device but with higher performance capabilities. Having within-device skews and output transition times significantly improved over the E111, the EL11 is ideally suited for those applications that require the ultimate in AC performance.

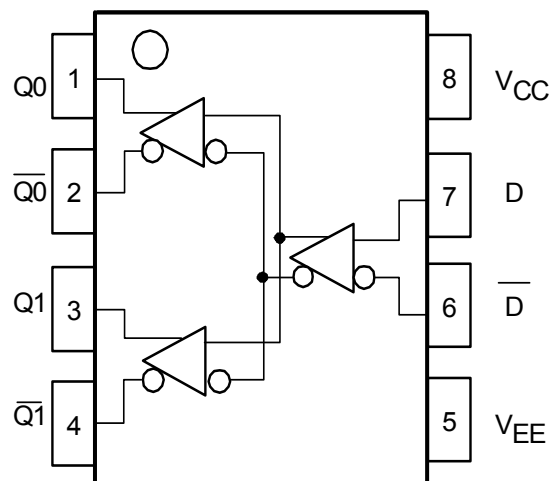
The differential inputs of the EL11 employ clamping circuitry to maintain stability under open input conditions. If the inputs are left open (pulled to  $V_{EE}$ ) the Q outputs will go LOW.

NOTE: Specifications in the ECL/PECL tables are valid when thermal equilibrium is established.

### PIN DESCRIPTION

PIN	FUNCTION
D, $\bar{D}$	Data Inputs
Q0, $\bar{Q}0$ , Q1, $\bar{Q}1$	Data Outputs
$V_{CC}$	Positive Supply
$V_{EE}$	Negative Supply

### LOGIC DIAGRAM AND PINOUT ASSIGNMENT



**AZ10EL11**  
**AZ100EL11**

**Absolute Maximum Ratings are those values beyond which device life may be impaired.**

Symbol	Characteristic	Rating	Unit
V <sub>CC</sub>	PECL Power Supply (V <sub>EE</sub> = 0V)	0 to +8.0	Vdc
V <sub>I</sub>	PECL Input Voltage (V <sub>EE</sub> = 0V)	0 to +6.0	Vdc
V <sub>EE</sub>	ECL Power Supply (V <sub>CC</sub> = 0V)	-8.0 to 0	Vdc
V <sub>I</sub>	ECL Input Voltage (V <sub>CC</sub> = 0V)	-6.0 to 0	Vdc
I <sub>OUT</sub>	Output Current --- Continuous --- Surge	50 100	mA
T <sub>A</sub>	Operating Temperature Range	-40 to +85	°C
T <sub>STG</sub>	Storage Temperature Range	-65 to +150	°C

**10K ECL DC Characteristics (V<sub>EE</sub> = -4.75V to -5.5V, V<sub>CC</sub> = GND)**

Symbol	Characteristic	-40°C			0°C			25°C			85°C			Unit
		Min	Typ	Max	Min	Typ	Max	Min	Typ	Max	Min	Typ	Max	
V <sub>OH</sub>	Output HIGH Voltage <sup>1</sup>	-1080		-890	-1020		-840	-980		-810	-910		-720	mV
V <sub>OL</sub>	Output LOW Voltage <sup>1</sup>	-1950		-1650	-1950		-1630	-1950		-1630	-1950		-1595	mV
V <sub>IH</sub>	Input HIGH Voltage	-1230		-890	-1170		-840	-1130		-810	-1060		-720	mV
V <sub>IL</sub>	Input LOW Voltage	-1950		-1500	-1950		-1480	-1950		-1480	-1950		-1445	mV
I <sub>IH</sub>	Input HIGH Current			150			150			150			150	µA
I <sub>IL</sub>	Input LOW Current	0.5			0.5			0.5			0.5			µA
I <sub>EE</sub>	Power Supply Current		26	31		26	31		26	31		26	31	mA

- Each output is terminated through a 50Ω resistor to V<sub>CC</sub> - 2V.

**10K PECL DC Characteristics (V<sub>EE</sub> = GND, V<sub>CC</sub> = +5.0V)**

Symbol	Characteristic	-40°C			0°C			25°C			85°C			Unit
		Min	Typ	Max	Min	Typ	Max	Min	Typ	Max	Min	Typ	Max	
V <sub>OH</sub>	Output HIGH Voltage <sup>1,2</sup>	3920		4110	3980		4160	4020		4190	4090		4280	mV
V <sub>OL</sub>	Output LOW Voltage <sup>1,2</sup>	3050		3350	3050		3370	3050		3370	3050		3405	mV
V <sub>IH</sub>	Input HIGH Voltage <sup>1</sup>	3770		4110	3830		4160	3870		4190	3940		4280	mV
V <sub>IL</sub>	Input LOW Voltage <sup>1</sup>	3050		3500	3050		3520	3050		3520	3050		3555	mV
I <sub>IH</sub>	Input HIGH Current			150			150			150			150	µA
I <sub>IL</sub>	Input LOW Current	0.5			0.5			0.5			0.5			µA
I <sub>EE</sub>	Power Supply Current		26	31		26	31		26	31		26	31	mA

- For supply voltages other than 5.0V, use the ECL table values and ADD supply voltage value.
- Each output is terminated through a 50Ω resistor to V<sub>CC</sub> - 2V.

**100K ECL DC Characteristics (V<sub>EE</sub> = -4.2V to -5.5V, V<sub>CC</sub> = GND)**

Symbol	Characteristic	-40°C			0°C			25°C			85°C			Unit
		Min	Typ	Max	Min	Typ	Max	Min	Typ	Max	Min	Typ	Max	
V <sub>OH</sub>	Output HIGH Voltage <sup>1</sup>	-1085	-1005	-880	-1025	-955	-880	-1025	-955	-880	-1025	-955	-880	mV
V <sub>OL</sub>	Output LOW Voltage <sup>1</sup>	-1830	-1695	-1555	-1810	-1705	-1620	-1810	-1705	-1620	-1810	-1705	-1620	mV
V <sub>IH</sub>	Input HIGH Voltage	-1165		-880	-1165		-880	-1165		-880	-1165		-880	mV
V <sub>IL</sub>	Input LOW Voltage	-1810		-1475	-1810		-1475	-1810		-1475	-1810		-1475	mV
I <sub>IH</sub>	Input HIGH Current			150			150			150			150	µA
I <sub>IL</sub>	Input LOW Current	0.5			0.5			0.5			0.5			µA
I <sub>EE</sub>	Power Supply Current		26	31		26	31		26	31		30	36	mA

- Each output is terminated through a 50Ω resistor to V<sub>CC</sub> - 2V.

**100K PECL DC Characteristics (V<sub>EE</sub> = GND, V<sub>CC</sub> = +5.0V)**

Symbol	Characteristic	-40°C			0°C			25°C			85°C			Unit
		Min	Typ	Max	Min	Typ	Max	Min	Typ	Max	Min	Typ	Max	
V <sub>OH</sub>	Output HIGH Voltage <sup>1,2</sup>	3915	3995	4120	3975	4045	4120	3975	4045	4120	3975	4045	4120	mV
V <sub>OL</sub>	Output LOW Voltage <sup>1,2</sup>	3170	3305	3445	3190	3295	3380	3190	3295	3380	3190	3295	3380	mV
V <sub>IH</sub>	Input HIGH Voltage <sup>1</sup>	3835		4120	3835		4120	3835		4120	3835		4120	mV
V <sub>IL</sub>	Input LOW Voltage <sup>1</sup>	3190		3525	3190		3525	3190		3525	3190		3525	mV
I <sub>IH</sub>	Input HIGH Current			150			150			150			150	µA
I <sub>IL</sub>	Input LOW Current	0.5			0.5			0.5			0.5			µA
I <sub>EE</sub>	Power Supply Current		26	31		26	31		26	31		30	36	mA

- For supply voltages other than 5.0V, use the ECL table values and ADD supply voltage value.
- Each output is terminated through a 50Ω resistor to V<sub>CC</sub> - 2V.

**AZ10EL11**  
**AZ100EL11**

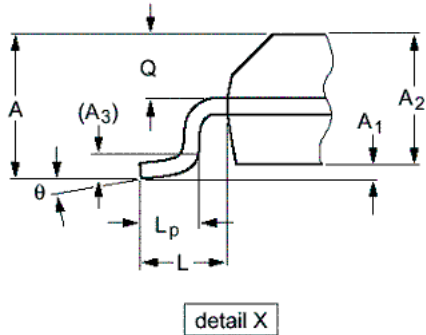
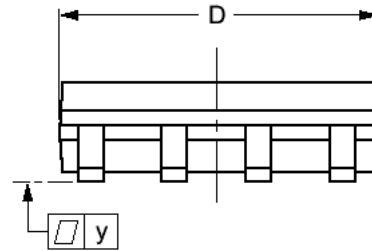
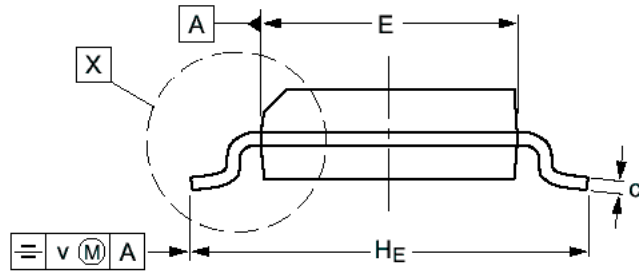
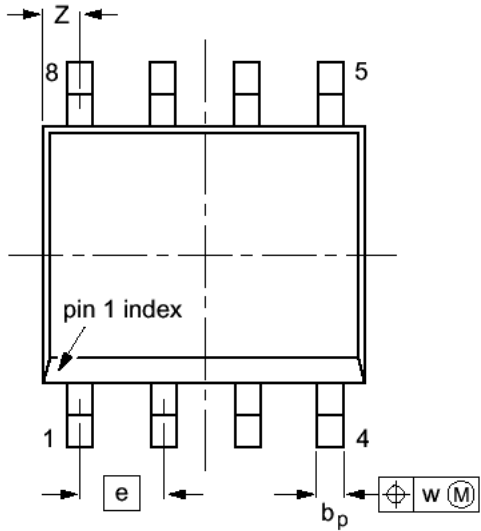
**AC Characteristics** ( $V_{EE} = 10E(-4.75V \text{ to } -5.5V)$ ,  $100E(-4.2V \text{ to } -5.5V)$ ;  $V_{CC} = GND$  or  $V_{EE} = GND$ ;  $V_{CC} = 10E(+4.75V \text{ to } +5.5V)$ ,  $100E(+4.2V \text{ to } +5.5V)$ )

Symbol	Characteristic	-40°C			0°C			25°C			85°C			Unit
		Min	Typ	Max	Min	Typ	Max	Min	Typ	Max	Min	Typ	Max	
$t_{PLH} / t_{PHL}$	Propagation Delay to Output	135	260	385	185	260	335	190	265	340	215	290	365	ps
$t_{SKEW}$	Within-Device Skew <sup>1</sup> Duty Cycle Skew <sup>2</sup>		5 5			5 5	20 20		5 5	20 20		5 5	20 20	ps
$V_{PP} (AC)$	Minimum Input Swing <sup>3</sup>	150			150			150			150			mV
$V_{CMR}$	Common Mode Range <sup>4</sup>	$V_{CC} - 2.0$		$V_{CC} - 0.4$	$V_{CC} - 2.0$		$V_{CC} - 0.4$	$V_{CC} - 2.0$		$V_{CC} - 0.4$	$V_{CC} - 2.0$		$V_{CC} - 0.4$	V
$t_r / t_f$	Rise/Fall Time 20 – 80%	100		350	100		350	100		350	100		350	ps

1. Within-device skew defined as identical transitions on similar paths through a device.
2. Duty cycle skew is the difference between a  $t_{PLH}$  and  $t_{PHL}$  propagation delay through a device.
3.  $V_{PP}$  is the minimum peak-to-peak differential input swing for which AC parameters are guaranteed. The device has a DC voltage gain of  $\approx 40$ .
4. The  $V_{CMR}$  range is referenced to the most positive side of the differential input signal. Normal operation is obtained if the HIGH level falls within the specified range and the peak-to-peak voltage lies between  $V_{PP}(\text{min})$  and  $1V$ .

AZ10EL11  
AZ100EL11

**PACKAGE DIAGRAM  
SOIC 8**



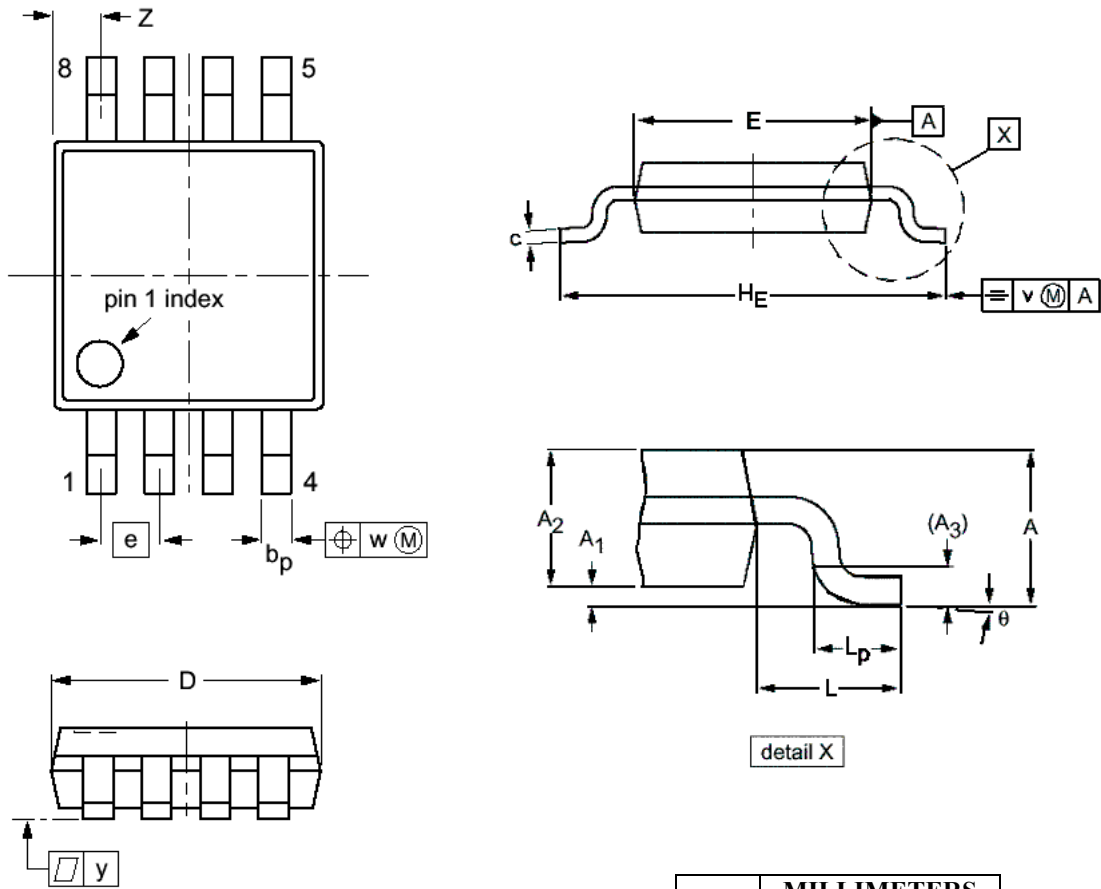
DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A		1.75		0.069
A <sub>1</sub>	0.10	0.25	0.004	0.010
A <sub>2</sub>	1.25	1.45	0.049	0.057
A <sub>3</sub>	0.25		0.01	
b <sub>p</sub>	0.36	0.49	0.014	0.019
c	0.19	0.25	0.0075	0.0100
D	4.8	5.0	0.19	0.20
E	3.8	4.0	0.15	0.16
e	1.27		0.050	
H <sub>E</sub>	5.80	6.20	0.228	0.244
L	1.05		0.041	
L <sub>p</sub>	0.40	1.00	0.016	0.039
Q	0.60	0.70	0.024	0.028
v	0.25		0.01	
w	0.25		0.01	
y	0.10		0.004	
Z	0.30	0.70	0.012	0.028
θ	0°	8°	0°	8°

NOTES:

1. DIMENSIONS D AND E DO NOT INCLUDE MOLD PROTRUSION.
2. MAXIMUM MOLD PROTRUSION FOR D IS 0.15mm.
3. MAXIMUM MOLD PROTRUSION FOR E IS 0.25mm.

AZ10EL11  
AZ100EL11

**PACKAGE DIAGRAM  
TSSOP 8**



- NOTES:
1. DIMENSIONS D AND E DO NOT INCLUDE MOLD PROTRUSION.
  2. MAXIMUM MOLD PROTRUSION FOR D IS 0.15mm.
  3. MAXIMUM MOLD PROTRUSION FOR E IS 0.25mm.

DIM	MILLIMETERS	
	MIN	MAX
A		1.10
A <sub>1</sub>	0.05	0.15
A <sub>2</sub>	0.80	0.95
A <sub>3</sub>	0.25	
b <sub>p</sub>	0.25	0.45
c	0.15	0.28
D	2.90	3.10
E	2.90	3.10
e	0.65	
H <sub>E</sub>	4.70	5.10
L	0.94	
L <sub>p</sub>	0.40	0.70
v	0.10	
w	0.10	
y	0.10	
Z	0.35	0.70
θ	0°	6°

**AZ10EL11**  
**AZ100EL11**

Arizona Microtek, Inc. reserves the right to change circuitry and specifications at any time without prior notice. Arizona Microtek, Inc. makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does Arizona Microtek, Inc. assume any liability arising out of the application or use of any product or circuit and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Arizona Microtek, Inc. does not convey any license rights nor the rights of others. Arizona Microtek, Inc. products are not designed, intended or authorized for use as components in systems intended to support or sustain life, or for any other application in which the failure of the Arizona Microtek, Inc. product could create a situation where personal injury or death may occur. Should Buyer purchase or use Arizona Microtek, Inc. products for any such unintended or unauthorized application, Buyer shall indemnify and hold Arizona Microtek, Inc. and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that Arizona Microtek, Inc. was negligent regarding the design or manufacture of the part.