

Continental Device India Limited An ISO/TS 16949, ISO 9001 and ISO 14001 Certified Company

# NPN SILICON PLANAR EPITAXIAL TRANSISTORS



# BC237,238, A,B,C BC239, B,C

TO-92

Plastic Package For Lead Free Parts, Device Part # will be Prefixed with "T"

### **Amplifier Transistors**

#### ABSOLUTE MAXIMUM RATINGS (T<sub>a</sub>=25°C)

DESCRIPTION	SYMBOL	BC237	BC238	BC239	UNITS
Collector Emitter Voltage	V <sub>CEO</sub>	45	25	25	V
Collector Emitter Voltage	V <sub>CES</sub>	50	30	30	V
Emitter Base Voltage	V <sub>EBO</sub>	6.0	5.0	5.0	V
Collector Current Continuous	Ι <sub>C</sub>	,	00		mA
Power Dissipation at T <sub>a</sub> =25 <sup>o</sup> C	PD	3	mW		
Derate Above 25ºC		:	mW/⁰C		
Power Dissipation at T <sub>c</sub> =25 <sup>o</sup> C	PD		W		
Derate Above 25ºC		8.0			
Operating And Storage Junction Temperature Range	T <sub>j</sub> , T <sub>stg</sub>	- 55 1	°C		

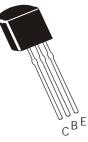
#### THERMAL RESISTANCE

Junction to Ambient in free air	R <sub>th (j-a)</sub>	357	°C/W
Junction to Case	R <sub>th (j-c)</sub>	125	°C/W

### ELECTRICAL CHARACTERISTICS (T<sub>a</sub>=25°C unless specified otherwise)

DESCRIPTION	SYMBOL	TEST CONDITION	MIN	MAX	UNITS
Collector Emitter Voltage	V <sub>CEO</sub>	I <sub>C</sub> =2mA, I <sub>B</sub> =0			
		BC237	45		V
		BC238/BC239	25		V
Emitter Base Voltage	V <sub>EBO</sub>	I <sub>E</sub> =10μΑ, I <sub>C</sub> =0			
		BC237	6.0		V
		BC238/BC239	5.0		V
Collector Cut Off Current	I <sub>CES</sub>	BC238/BC239		15	nA
		$V_{CE}$ =30V, $V_{BE}$ =0			
		BC237		15	nA
		$V_{CE}$ =50V, $V_{BE}$ =0			
		BC238/BC239		4.0	μA
		V <sub>CE</sub> =30V, V <sub>BE</sub> =0, Ta=125°C			
		BC237		4.0	μA
		$V_{CE}$ =50V, $V_{BE}$ =0, Ta=125°C			

BC237\_239Rev\_1 201205E



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## ELECTRICAL CHARACTERISTICS (T<sub>a</sub>=25°C unless specified otherwise)

DESCRIPTION	SYMBOL	TEST CONDITION	MIN	MAX	UNITS
DC Current Gain	h <sub>FE</sub>	$I_C=10\mu A, V_{CE}=5V$	V		
		Α	TYP90		
		В	TYF	TYP150	
		С	TYF	270	
		I <sub>C</sub> =2mA, V <sub>CE</sub> =5V			
		BC237/238/239	120	800	
		Α	120	220	
		В	200	460	
		С	380	800	
		*I <sub>C</sub> =100mA, V <sub>CE</sub> =5V			
		Α	TYP	120	
		В	TYF	180	
		С	TYP300		
Collector Emitter Saturation Voltage	V <sub>CE (sat)</sub>	I <sub>C</sub> =10mA, I <sub>B</sub> =0.5mA		0.20	V
		*I <sub>C</sub> =100mA, I <sub>B</sub> =5mA			
		BC237/239		0.60	V
		BC238		0.80	V
Base Emitter Saturation Voltage	V <sub>BE (sat)</sub>	I <sub>C</sub> =10mA, I <sub>B</sub> =0.5mA		0.83	V
		*I <sub>C</sub> =100mA, I <sub>B</sub> =5mA		1.05	V
Base Emitter On Voltage	V <sub>BE (on)</sub>	I <sub>C</sub> =2mA, V <sub>CE</sub> =5V	0.55	0.70	V

#### SMALL SIGNAL CHARACTERISTICS

DESCRIPTION	SYMBOL	TEST CONDITION	TEST CONDITION MIN MAX			
Transistors Frequency	f <sub>T</sub>	$I_C=0.5mA$ , $V_{CE}=3V$ , f=100MHz				
		BC237	TYP	100	MHz	
		BC238	TYP	120	MHz	
		BC239	TYP	TYP140		
		I <sub>C</sub> =10mA, V <sub>CE</sub> =5V, f=100MHz	150	MHz		
Collector Output Capacitance	C <sub>ob</sub>	V <sub>CB</sub> =10V, I <sub>E</sub> =0, f=1MHz	4.5		pF	
Emitter Input Capacitance	C <sub>ib</sub>	V <sub>EB</sub> =0.5V, f=1MHz	TYP8 pF		pF	
Noise Figure	NF	V <sub>CE</sub> =5V, I <sub>C</sub> =0.2mA, R <sub>S</sub> =2KΩ, f=1KHz, B=200Hz	,			
		BC237/238	10		dB	
		BC239		4.0	dB	

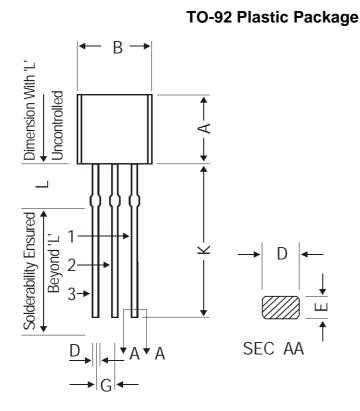
## \*Pulse Test: Pulse Width $\leq$ 300ms, Duty Cycle $\leq$ 2%

BC237\_239Rev\_1 201205E

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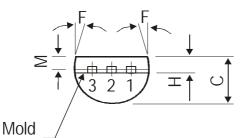
## TO-92

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DIM	MIN.	MAX.			
А	4.32	5.33			
В	4.45	5.20			
С	3.18	4.19			
D	0.41	0.55			
E	0.35	0.50			
F	5 DEG				
G	1.14	1.40			
Н	1.20	1.40			
К	12.70				
L	1.982	2.082			
М	1.03	1.20			

All dimensions are in mm





The TO-92 Package , Tape and Ammo Pack drawings are correct as on the date of issue/revision of this Data Sheet. The currently valild dimensions and information, may please be confirmed from the TO-92 Drawing in the Packages and Packing Section of the Product Catalogue.

# **Packing Details**

PACKAGE	STAND	ARDPACK	INNER CARTON BOX		OUTER CARTON BOX		
	Details	Net Weight/Qty	Size Qty		Size	Qty	Gr Wt
TO-92 Bulk	1K/polybag	200 gm/1K pcs	3" x 7.5" x 7.5"	5K	17" x 15" x 13.5"	80K	23 kgs
TO-92 T&A	2K/ammo box	645 gm/2K pcs	12.5" x 8" x 1.8"	2K	17" x 15" x 13.5"	32K	12.5 kgs

BC237\_239Rev\_1 201205E

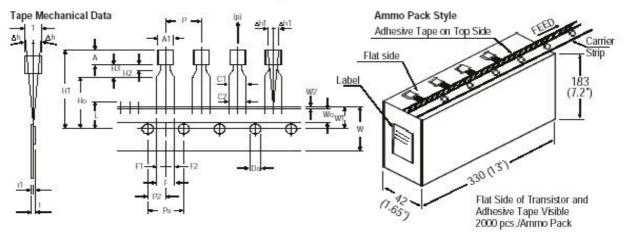
Parting Line

21

# TO-92 Plastic Package

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### TO-92 Tape and Ammo Pack



#### All dimensions are in mm

		SPECIFICATION			N			
ITEM	SYMBOL	MIN.	NOM.	MAX.	TOL.			
BODY WIDTH	A1	4.45		5.20	(	NOTES		
BODY HEIGHT	Α	4.32		5.33		1. Maximum alignment deviation between		
BODY THICKNESS	Т	3.18		4.19		leads will not to be greater than 0.2mm.		
PITCH OF COMPONENT	Р		12.7		± 1.0	2. Maximum non-cumulative variation		
*1FEED HOLE PITCH	Po		12.7		± 0.3	between tape feed holes shall not		
*2 FEED HOLE CENTRE TO	10255		1112220061			exceed 1 mm in 20 pitches.		
COMPONENT CENTRE	P2		6.35		± 0.4	3. Holddown tape will not exceed beyond		
DISTANCE BETWEEN OUTER LEADS	E		5.08		+ 0.6 - 0.2	the edge(s) of carrier tape and there shall be no exposure of adhesive.		
*3 COMPONENT ALIGNMENT SIDE VIEW	Δh		0	1.0		4. There will be no more than three (3)		
*4 COMPONENT ALIGNMENT FRONT VIEW	Δh1		0	1.3		consecutive missing components in a		
TAPE WIDTH	w		18	00025	± 0.5	tape.		
HOLD-DOWN TAPE WIDTH	Wo		6		± 0.2	5. A tape trailer, having at least three feed		
HOLE POSITION	W1		9		+ 0.7	holes are provided after the last component in a tape.		
HOLD-DOWN TAPE POSITION	W2	0.0		0.7		6. Splices should not interfere with the		
LEAD WIRE CLINCH HEIGHT	Ho	V 00 202575	16	11212223	± 0.5	sprocket feed holes.		
COMPONENT HEIGHT	H1		19250	24.0				
LENGTH OF SNIPPED LEADS	L			11.0				
FEED HOLE DIAMETER	Do		4		± 0.2	REMARKS		
*5 TOTAL TAPE THICKNESS	t			1.2				
LEAD - TO - LEAD DISTANCE	F1, F2	2.40		2.70		*1 Cumulative pitch error 1.0 mm/20 pitch		
STAND OFF	H2	0.45		1.45	- 0.1	*2 To be measured at bottom of clinch		
CLINCH HEIGHT	H3	0.10		3.0		*3 At top of body		
LEAD PARALLELISM	C1 - C2			0.22		*4 At top of body		
PULL - OUT FORCE	(p)	6N				*5 t1 0.3 – 0.6 mm		

## **Component Disposal Instructions**

- 1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.
- 2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

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## Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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BC237\_239Rev\_1 201205E