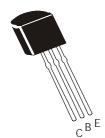


#### Continental Device India Limited

An ISO/TS 16949, ISO 9001 and ISO 14001 Certified Company



## NPN SILICON PLANAR EPITAXIAL TRANSISTORS



BC485, A, B, L BC487, A, B, L BC489, A, B, L

TO-92 Plastic Package

## **High Current Transistors**

# ABSOLUTE MAXIMUM RATINGS (T<sub>a</sub>=25°C)

DESCRIPTION	SYMBOL	BC485	BC487	BC489	UNITS
Collector Emitter Voltage	V <sub>CEO</sub>	45	60	80	V
Collector Base Voltage	V <sub>CBO</sub>	45	60	80	V
Emitter Base Voltage	V <sub>EBO</sub>	5.0			
Collector Current Continuous	I <sub>C</sub>	1.0			
Power Dissipation at T <sub>a</sub> =25°C	P <sub>D</sub>	625			
Derate Above 25°C		5.0			
Power Dissipation at T <sub>c</sub> =25°C	P <sub>D</sub>	1,5			
Derate Above 25°C		12			
Operating and Storage Junction Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	- 55 to +150			

#### THERMAL RESISTANCE

Junction to Case	R <sub>th (j-c)</sub>	83.3	°C/W
Junction to Ambient in free air	R <sub>th (j-a)</sub>	200	°C/W

## ELECTRICAL CHARACTERISTICS (T<sub>a</sub>=25°C unless specified otherwise)

DESCRIPTION	SYMBOL	TEST CONDITION		BC485	BC487	BC489	UNITS
Collector Emitter Voltage	$V_{CEO}$	$I_C=1$ mA, $I_B=0$		>45	>60	>80	V
Collector Base Voltage	$V_{CBO}$	I <sub>C</sub> =100μΑ,	I <sub>E</sub> =0	>45	>60	>80	V
Emitter Base Voltage	$V_{EBO}$	I <sub>E</sub> =10μΑ,	I <sub>C</sub> =0	>5.0			V
Collector Cut Off Current	I <sub>CBO</sub>	$V_{CB}=30V$ , $I_{E}=0$	BC485	<100			nA
		$V_{CB}$ =40V, $I_{E}$ =0	BC487	<100			nA
		$V_{CB}$ =60V, $I_E$ =0,	BC489		<100		nA
DC Current Gain	h <sub>FE</sub>	I <sub>C</sub> =10mA, V <sub>CE</sub> =2V		>40			
		$I_C=100$ mA, $V_{CE}=2$ V					
		BC485/487/489		60 - 400			
		BC485L/487I	L/489L		60 - 150		
		BC485A/487/	4/489A	100 - 250			
		BC485B/487B/489B		160 - 400			
		*I <sub>C</sub> =1A, V <sub>CE</sub> =5V		>15			
<b>Collector Emitter Saturation Voltage</b>	V <sub>CE (sat)</sub>	I <sub>C</sub> =500mA, I <sub>B</sub> =50mA		<0.50		V	
		*I <sub>C</sub> =1A, I <sub>B</sub> =100mA		TYP 0.30		V	
Base Emitter Saturation Voltage	V <sub>BE (sat)</sub>	I <sub>C</sub> =500mA, I <sub>B</sub> =50mA		<1.2		V	
		*I <sub>C</sub> =1A, I <sub>B</sub> =100mA			TYP 0.9		V

<sup>\*</sup>Pulse test:- Pulse Width =300ms, Duty Cycle 2%

# **NPN SILICON PLANAR EPITAXIAL TRANSISTORS**



BC485, A, B, L BC487, A, B, L BC489, A, B, L

TO-92 Plastic Package

# ELECTRICAL CHARACTERISTICS (T<sub>a</sub>=25°C unless specified otherwise)

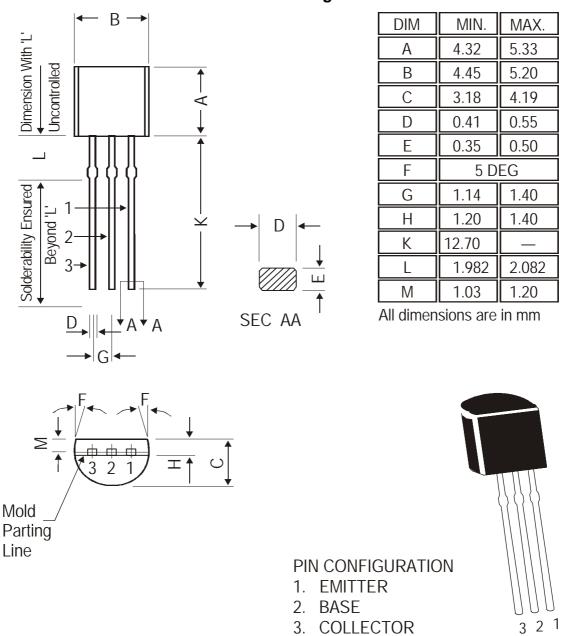
## **SMALL SIGNAL CHARACTERISTICS**

DESCRIPTION	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNITS
Transistors Frequency	$f_T$	$I_C$ =50mA, $V_{CE}$ =2V, f=100MHz		200		MHz
Output Capacitance	C <sub>ob</sub>	V <sub>CB</sub> =10V, f=1MHz		7.0		рF
Input Capacitance	$C_{ib}$	V <sub>EB</sub> =0.5V, f=1MHz		50		рF

BC485, A, B, L BC487, A, B, L BC489, A, B, L

TO-92 Plastic Package





The TO-92 Package, Tape and Ammo Pack drawings are correct as on the date of issue/revision of this Data Sheet.

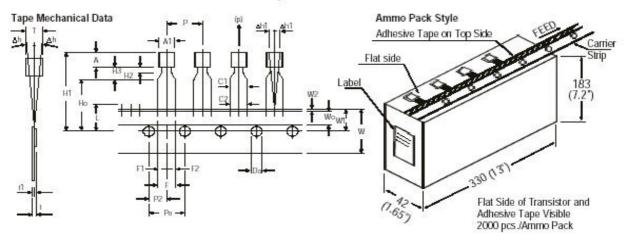
The currently valid dimensions and information, may please be confirmed from the TO-92 Drawing in the Packages and Packing Section of the Product Catalogue.

#### **Packing Details**

PACKAGE	STANDARD PACK		INNER CARTO	N BOX	OUTER CARTON BOX		
	Details	Net Weight/Oty	Size	Qty	Size	Qty	Gr Wt
TO-92 Bulk	1K/polybag	200 gm/1K pcs	3" x 7.5" x 7.5"	5K	17" x 15" x 13.5"	80K	23 kgs
TO-92 T&A	2K/ammo box	645 gm/2K pcs	12.5" x 8" x 1.8"	2K	17" x 15" x 13.5"	32K	12.5 kgs

TO-92 Plastic Package

## TO-92 Tape and Ammo Pack



#### All dimensions are in mm

		SPECIFICATION				
ITEM	SYMBOL	MIN.	NOM.	MAX.	TOL.	
BODY WIDTH	A1	4.45		5.20		NOTES
BODY HEIGHT	Α	4.32		5.33		Maximum alignment deviation between
BODY THICKNESS	T	3.18		4.19		leads will not to be greater than 0.2mm.
PITCH OF COMPONENT	P		12.7		± 1.0	Maximum non-cumulative variation
*1FEED HOLE PITCH	Po		12.7		± 0.3	between tape feed holes shall not
*2 FEED HOLE CENTRE TO			11-000000000			exceed 1 mm in 20 pitches.
COMPONENT CENTRE	P2		6.35		$\pm 0.4$	3. Holddown tape will not exceed beyond
DISTANCE BETWEEN OUTER LEADS	E		5.08		+ 0.6	the edge(s) of carrier tape and there shall be no exposure of adhesive.
*3 COMPONENT ALIGNMENT SIDE VIEW	Δh		0	1.0		4. There will be no more than three (3)
*4 COMPONENT ALIGNMENT FRONT VIEW	Δh1		0	1.3		consecutive missing components in a
TAPE WIDTH	W		18	66000	± 0.5	tape.
HOLD-DOWN TAPE WIDTH	Wo		6		± 0.2	5. A tape trailer, having at least three feed
HOLE POSITION	W1		9		+ 0.7	holes are provided after the last component in a tape.
HOLD-DOWN TAPE POSITION	W2	0.0		0.7		6. Splices should not interfere with the
LEAD WIRE CLINCH HEIGHT	Ho	10000	16	1100000	± 0.5	sprocket feed holes.
COMPONENT HEIGHT	H1		1000	24.0	2.0.0	
LENGTH OF SNIPPED LEADS	1			11.0		
FEED HOLE DIAMETER	Do		4		± 0.2	DEMARKS
*5 TOTAL TAPE THICKNESS	t		5.00	1.2		REMARKS
LEAD - TO - LEAD DISTANCE	F1, F2	2.40		2.70		*1 Cumulative pitch error 1.0 mm/20 pitch
STAND OFF	ua	0.45		1.45	- 0.1	*2 To be measured at bottom of clinch
	H2 H3	0.45		1.45		*3 At top of body
CLINCH HEIGHT	1000			3.0 0.22		*4 At top of body
LEAD PARALLELISM	C1 - C2	CNC		0.22		
PULL - OUT FORCE	(p)	6N		L la		*5 t1 0.3 – 0.6 mm

#### **Component Disposal Instructions**

- 1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.
- 2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

**Customer Notes** 

BC485, A, B, L BC487, A, B, L BC489, A, B, L

TO-92 Plastic Package

#### **Disclaimer**

The product information and the selection guides facilitate selection of the CDIL's Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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