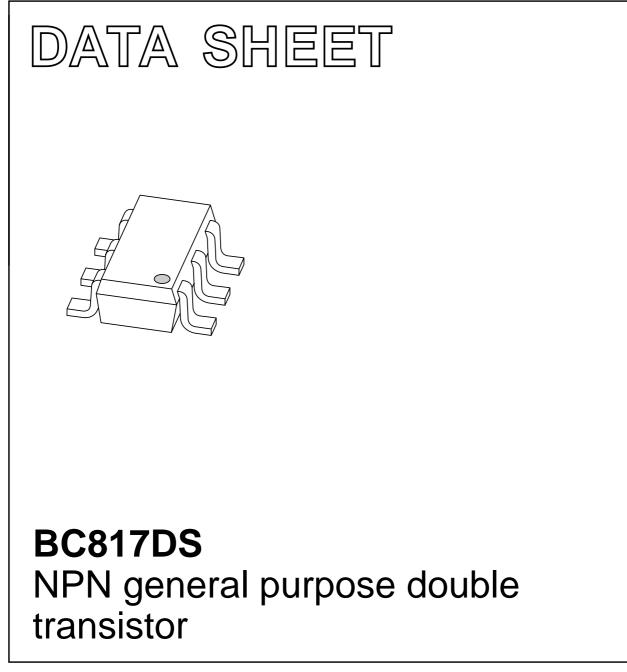
# DISCRETE SEMICONDUCTORS



Product specification Supersedes data of 2002 Aug 09 2002 Nov 22



## BC817DS

## FEATURES

- High current (500 mA)
- 600 mW total power dissipation
- Replaces two SOT23 packaged transistors on same PCB area.

## APPLICATIONS

- · General purpose switching and amplification
- Push-pull amplifiers
- Multi-phase stepper motor drivers.

## DESCRIPTION

NPN transistor pair in a SOT457 (SC-74) plastic package.

## MARKING

| TYPE NUMBER | MARKING CODE |  |  |
|-------------|--------------|--|--|
| BC817DS     | N3           |  |  |

## QUICK REFERENCE DATA

| SYMBOL                                 | SYMBOL PARAMETER                           |   | UNIT |
|--|--|---|------|
| V <sub>CEO</sub>                       | V <sub>CEO</sub> collector-emitter voltage |   |      |
| I <sub>C</sub>                         | I <sub>C</sub> collector current (DC)      |   | mA   |
| I <sub>CM</sub> peak collector current |  | 1 | A    |

## PINNING

| PIN  | DESCRIPTION |          |
|------|-------------|----------|
| 1, 4 | emitter     | TR1; TR2 |
| 2, 5 | base        | TR1; TR2 |
| 6, 3 | collector   | TR1; TR2 |

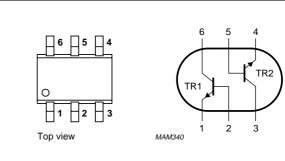


Fig.1 Simplified outline (SOT457) and symbol.

### LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

| SYMBOL           | PARAMETER CONDITIONS   |                                  | MIN. | MAX. |    |  |
|------------------|--|----------------------------------|------|------|----|--|
| Per transi       | Per transistor unless otherwise specified                    |                                  |      |      |    |  |
| V <sub>CBO</sub> | collector-base voltage                                       | open emitter                     | _    | 50   | V  |  |
| V <sub>CEO</sub> | collector-emitter voltage                                    | open base                        | _    | 45   | V  |  |
| V <sub>EBO</sub> | emitter-base voltage   | open collector                   | _    | 5    | V  |  |
| I <sub>C</sub>   | collector current (DC)                                       |                                  | -    | 500  | mA |  |
| I <sub>CM</sub>  | peak collector current                                       |                                  | -    | 1    | A  |  |
| I <sub>BM</sub>  | peak base current  |                                  | -    | 200  | mA |  |
| P <sub>tot</sub> | total power dissipation $T_{amb} \le 25 \text{ °C}$ ; note 1 |                                  | _    | 370  | mW |  |
| T <sub>stg</sub> | storage temperature  |                                  | -65  | +150 | °C |  |
| Tj               | junction temperature   |                                  | -    | 150  | °C |  |
| T <sub>amb</sub> | operating ambient temperature                                |                                  | -65  | +150 | °C |  |
| Per device       | 9  |                                  | L    |      |    |  |
| P <sub>tot</sub> | total power dissipation                                      | T <sub>amb</sub> ≤ 25 °C; note 1 | _    | 600  | mW |  |

Note

1. Device mounted on a printed-circuit board; single sided copper; tinplated; mounting pad for collector 1 cm<sup>2</sup>.

## THERMAL CHARACTERISTICS

| SYMBOL              | PARAMETER                                   | CONDITIONS | VALUE | UNIT |
|---------------------|---|------------|-------|------|
| R <sub>th j-a</sub> | thermal resistance from junction to ambient | note 1     | 208   | K/W  |

#### Note

1. Device mounted on a printed-circuit board; single sided copper; tinplated; mounting pad for collector 1 cm<sup>2</sup>.

### CHARACTERISTICS

 $T_{amb}$  = 25 °C unless otherwise specified.

| SYMBOL             | PARAMETER                            | CONDITIONS   | MIN. | TYP. | MAX. | UNIT |  |
|--------------------|--------------------------------------|--|------|------|------|------|--|
| Per transis        | Per transistor                       |  |      |      |      |      |  |
| I <sub>CBO</sub>   | collector-base cut-off current       | $V_{CB} = 20 \text{ V}; I_E = 0$   | -    | -    | 100  | nA   |  |
|                    |                                      | $V_{CB} = 20 \text{ V}; \text{ I}_{E} = 0; \text{ T}_{j} = 150 ^{\circ}\text{C}$ | -    | -    | 5    | μA   |  |
| I <sub>EBO</sub>   | emitter-base cut-off current         | $V_{EB} = 5 V; I_{C} = 0$  | -    | -    | 100  | nA   |  |
| h <sub>FE</sub>    | DC current gain                      | V <sub>CE</sub> = 1 V; I <sub>C</sub> = 100 mA; note 1                           | 160  | -    | 400  |      |  |
|                    |                                      | V <sub>CE</sub> = 1 V; I <sub>C</sub> = 500 mA; note 1                           | 40   | -    | -    |      |  |
| V <sub>CEsat</sub> | collector-emitter saturation voltage | I <sub>C</sub> = 500 mA; I <sub>B</sub> = 50 mA; note 1                          | -    | -    | 700  | mV   |  |
| V <sub>BE</sub>    | base-emitter voltage                 | $V_{CE} = 1 V; I_C = 500 mA;$<br>notes 1 and 2                                   | -    | -    | 1.2  | V    |  |
| C <sub>c</sub>     | collector capacitance                | V <sub>CB</sub> = 10 V; I <sub>E</sub> = I <sub>e</sub> = 0; f = 1 MHz           | _    | 5    | _    | pF   |  |
| f <sub>T</sub>     | transition frequency                 | $V_{CE} = 5 \text{ V}; I_{C} = 10 \text{ mA};$<br>f = 100 MHz                    | 100  | -    | -    | MHz  |  |

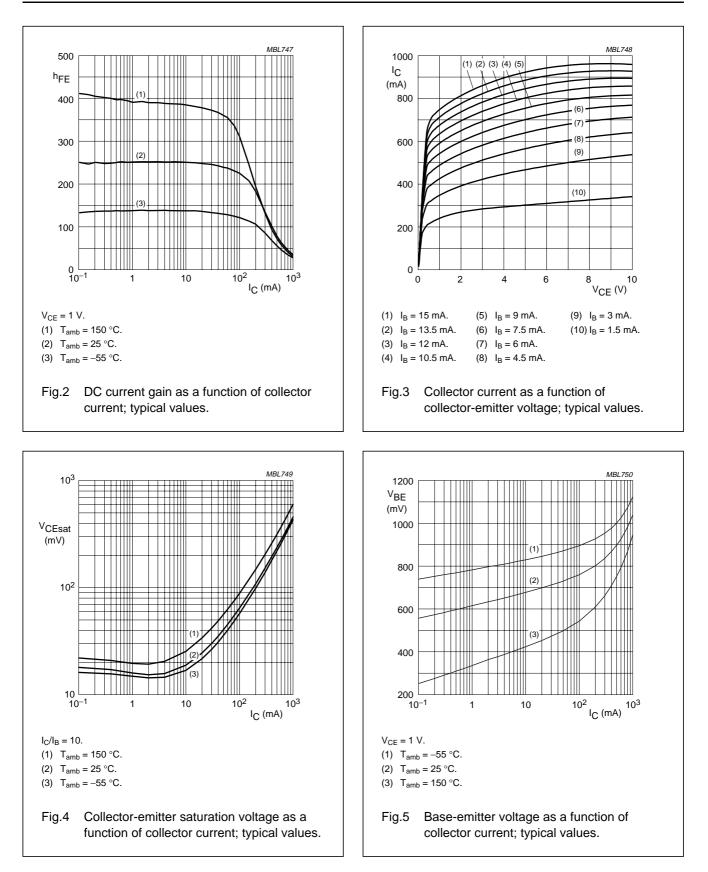
## Notes

1. Pulse test:  $t_p \leq 300 \ \mu s; \ \delta \leq 0.02.$ 

2.  $V_{BE}$  decreases by approximately -2 mV/K with increasing temperature.

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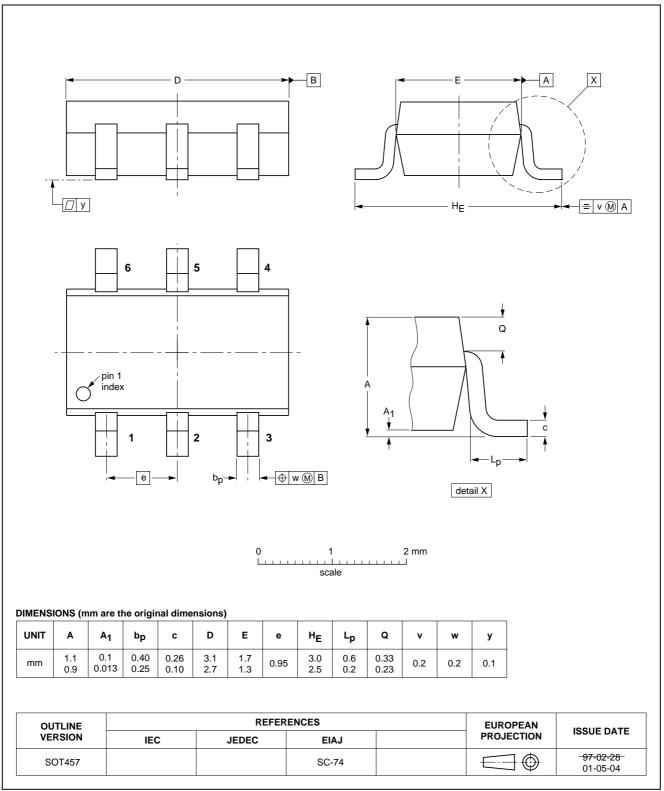


**BC817DS** 

# NPN general purpose double transistor

## PACKAGE OUTLINE

## Plastic surface mounted package; 6 leads



SOT457

BC817DS

## DATA SHEET STATUS

| LEVEL | DATA SHEET<br>STATUS <sup>(1)</sup> | PRODUCT<br>STATUS <sup>(2)(3)</sup> | DEFINITION   |
|-------|-------------------------------------|-------------------------------------|--|
| 1     | Objective data                      | Development                         | This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.  |
| 11    | Preliminary data                    | Qualification                       | This data sheet contains data from the preliminary specification.<br>Supplementary data will be published at a later date. Philips<br>Semiconductors reserves the right to change the specification without<br>notice, in order to improve the design and supply the best possible<br>product.             |
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NOTES

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