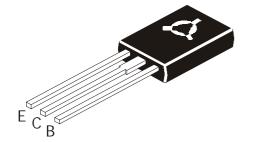


PNP DARLIGNTON POWER SILICON TRANSISTORS



BD676, 676A BD678, 678A BD680, 680A BD682, 684

TO126 Plastic Package

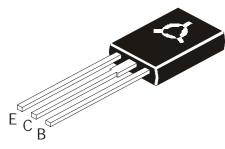
For Use As Output Devices In Complementary General Purpose Amplifier Applications.

COMPLEMENTARY TO BD675, 675A, 677, 677A, 679, 679A, 681 & 683 BD678, 678A, 680, 680A ARE EQUIVALENT TO MJE700, 702, 703.

ABSOLUTE MAXIMUM RATINGS (Ta=25°C unless specified otherwise)

DESCRIPTION	SYMBOL	TEST CONDITION	BD676	BD678	BD680	BD682	BD684	UNIT
			BD676/	BD678A	BD680A	1		
Collector -Base Voltage	V _{CBO}		45	60	80	100	120	V
Collector -Emitter Voltage	V_{CEO}		45	60	80	100	120	V
Emitter Base Voltage	V_{EBO}				5.0			V
Collector Current	Ι _C				4.0			А
Base Current	I _B				0.1			Α
Collector Power Dissipation @ Tc=25°C	P_{D}				40			W
Derate above 25°C					0.32			W/ºC
Operation and Storage Junction	T _i ,T _{stg}			-55 to +150			°C	
Temperature Range								
THERMAL RESISTANCE	_							
Junction to Case	R _{th(j-c)}				3.13			°C/W
ELECTRICAL CHARACTERISTICS (Ta=2	5⁰C unles	s specified otherwise	e)					
DESCRIPTION	SYMBOL	TEST CONDITION	BD676	BD678	BD680	BD682	BD684	UNIT
			BD676A	BD678A	BD680A	۱		
Collector Emitter Voltage	BV_{CEO}^*	I _C =50mA, I _B =0	>45	>60	>80	>100	>120	V
Collector Cut off Current	I _{CEO}	V_{CE} =Half Rated V_{CEO} , I _B =0	<500	<500	<500	<500	>120	μA
Collector Cut off Current	I_{CBO}	V_{CB} =Rated V_{CBO} ,I _{E=} 0	<0.2	<0.2	<0.2	<0.2	<0.2	mA
		V _{CB} =Rated V _{CBO} ,I _{E=} 0 Ta=100°C	<2	<2	<2	<2	<2	mA
Emitter Cut off Current DC Current Gain	I _{EBO} h _{FE} *	V_{EB} =5V,I _C =0	<2	<2	<2	<2	<2	mA
NON A		I _C =1.5A, V _{CF} =3V	<-		>750-		>	
4		$I_{C}=2A, V_{CE}=3V$	<		>750		>	

PNP DARLIGNTON POWER SILICON TRANSISTORS



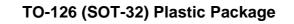
BD676, 676A BD678, 678A BD680, 680A BD682, 684

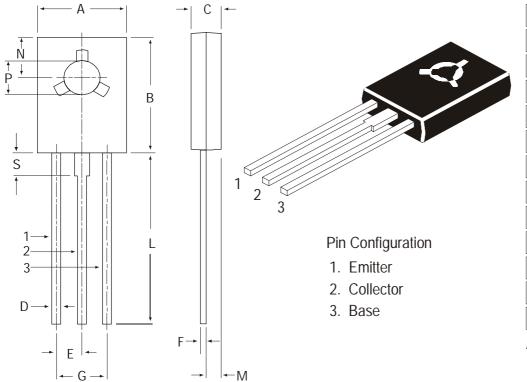
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SYMBO	L TEST CONDITION	BD676 BD678 BD680 BD682 BD684 UNIT			
		BD676A BD678A BD680A			
oltage V _{CE(sat)} *					
NON A	I _C =1.5A, I _B =30mA	<> V			
Α	I_{C} =2.0A, I_{B} =40mA	<> V			
V _{BE (on)} *					
NON A	I _C =1.5A, V _{CE} =3V	<> V			
Α	$I_{C}=2A, V_{CE}=3V$	<> V			
lhfel	I _C =1.5A, V _{CE} =3V f=1MHz	<>			
	Ditage V _{CE(sat)} * NON A A V _{BE (on)} * NON A A	NON A $I_{C}=1.5A, I_{B}=30mA$ A $I_{C}=2.0A, I_{B}=40mA$ $V_{BE (on)}^{*}$ NON A $I_{C}=1.5A, V_{CE}=3V$ A $I_{C}=2A, V_{CE}=3V$			

BD676, 676A BD678, 678A BD680, 680A BD682, 684

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DIM	MIN	MAX			
А	7.4	7.8			
В	10.5	10.8			
С	2.4	2.7			
D	0.7	0.9			
E	2.25 TYP.				
F	0.49	0.75			
G	4.5 TYP.				
L	15.7 TYP.				
М	1.27 TYP.				
Ν	3.75 TYP.				
Р	3.0	3.2			
S	2.5 TYP.				

All diminsions in mm.

Packing Detail

PACKAGE	STANDARD PACK		INNER CARTON BOX		OUTER CARTON BOX		
	Details	Net Weight/Qty	Size	Qty	Size	Qty	Gr Wt
TO-126 Bulk	500 pcs/polybag	340 gm/500 pcs	3" x 7.5" x 7.5"	2K	17" x 15" x 13.5"	32K	31 kgs
TO-126 Tube	50 pcs/tube	73 gm/50 pcs	3" x 3.7" x 21.5"	1K	19" x 19" x 19"	10K	15 kgs

Notes

BD676, 676A BD678, 678A BD680, 680A BD682, 684

TO126 Plastic Package

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD is believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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Data Sheet