# **Darlington Complementary Silicon Power Transistors**

This series of plastic, medium–power silicon NPN and PNP Darlington transistors are designed for general purpose and low speed switching applications.

#### Features

- High DC Current Gain  $-h_{FE} = 2500$  (typ) @  $I_C = 5.0$  Adc.
- Collector Emitter Sustaining Voltage @ 30 mAdc:

 $V_{CEO(sus)} = 80 \text{ Vdc (min)} - BDW46$ 100 Vdc (min) - BDW42/BDW47

Low Collector Emitter Saturation Voltage

 $V_{CE(sat)} = 2.0 \text{ Vdc} (max) @ I_C = 5.0 \text{ Adc}$ 3.0 Vdc (max) @ I\_C = 10.0 Adc

- Monolithic Construction with Built-In Base Emitter Shunt resistors
- TO-220 Compact Package
- These Devices are Pb-Free and are RoHS Compliant\*

#### MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-Emitter Voltage BDW46 BDW42, BDW47	V <sub>CEO</sub>	80 100	Vdc
Collector-Base Voltage BDW46 BDW42, BDW47	V <sub>CB</sub>	80 100	Vdc
Emitter-Base Voltage	V <sub>EB</sub>	5.0	Vdc
Collector Current	Ι <sub>C</sub>	15	Adc
Base Current	Ι <sub>Β</sub>	0.5	Adc
Total Device Dissipation @ T <sub>C</sub> = 25°C Derate above 25°C	PD	85 0.68	W W/∘C
Operating and Storage Junction Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

#### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction–to–Case	$R_{ extsf{ heta}JC}$	1.47	°C/W

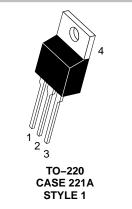
\*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.



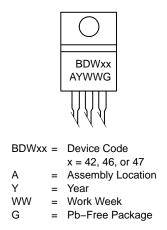
## **ON Semiconductor®**

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# 15 AMP DARLINGTON COMPLEMENTARY SILICON POWER TRANSISTORS 80–100 VOLT, 85 WATT



#### MARKING DIAGRAM



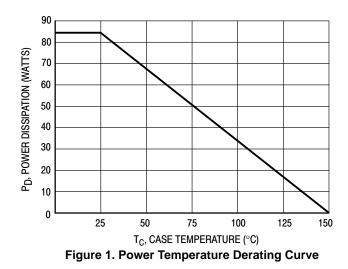
#### ORDERING INFORMATION

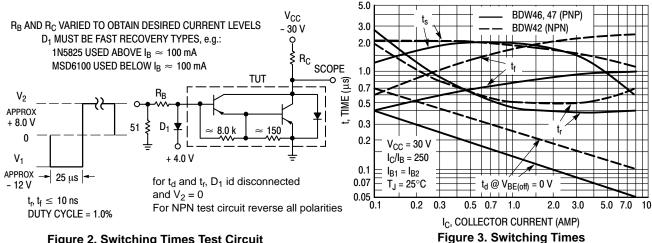
Device	Package	Shipping
BDW42G	TO–220 (Pb–Free)	50 Units/Rail
BDW46G	TO–220 (Pb–Free)	50 Units/Rail
BDW47G	TO–220 (Pb–Free)	50 Units/Rail

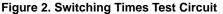
#### ELECTRICAL CHARACTERISTICS (T<sub>C</sub> = 25°C unless otherwise noted)

Characteristic			Min	Max	Unit
OFF CHARACTERISTICS					
Collector Emitter Sustaining Voltage (Note 1) ( $I_C = 30$ mAdc, $I_B = 0$ )	BDW46 BDW42/BDW47	V <sub>CEO(sus)</sub>	80 100		Vdc
Collector Cutoff Current $(V_{CE} = 40 \text{ Vdc}, I_B = 0)$ $(V_{CE} = 50 \text{ Vdc}, I_B = 0)$	BDW46 BDW42/BDW47	I <sub>CEO</sub>		2.0 2.0	mAdc
Collector Cutoff Current ( $V_{CB} = 80 \text{ Vdc}, I_E = 0$ ) ( $V_{CB} = 100 \text{ Vdc}, I_E = 0$ )	BDW46 BDW42/BDW47	I <sub>CBO</sub>	-	1.0 1.0	mAdc
Emitter Cutoff Current ( $V_{BE} = 5.0 \text{ Vdc}, I_C = 0$ )		I <sub>EBO</sub>	-	2.0	mAdc
ON CHARACTERISTICS (Note 1)					
DC Current Gain $(I_C = 5.0 \text{ Adc}, V_{CE} = 4.0 \text{ Vdc})$ $(I_C = 10 \text{ Adc}, V_{CE} = 4.0 \text{ Vdc})$		h <sub>FE</sub>	1000 250		
Collector–Emitter Saturation Voltage $(I_C = 5.0 \text{ Adc}, I_B = 10 \text{ mAdc})$ $(I_C = 10 \text{ Adc}, I_B = 50 \text{ mAdc})$		V <sub>CE(sat)</sub>	-	2.0 3.0	Vdc
Base–Emitter On Voltage (I <sub>C</sub> = 10 Adc, V <sub>CE</sub> = 4.0 Vdc)		V <sub>BE(on)</sub>	-	3.0	Vdc
SECOND BREAKDOWN (Note 2)					
Second Breakdown Collector Current with Base Forward Biased BDW42	V <sub>CE</sub> = 28.4 Vdc	I <sub>S/b</sub>	3.0	_	Adc
BDW46/BDW47	$V_{CE} = 40 \text{ Vdc}$ $V_{CE} = 22.5 \text{ Vdc}$ $V_{CE} = 36 \text{ Vdc}$		1.2 3.8 1.2	- - -	
DYNAMIC CHARACTERISTICS					
Magnitude of common emitter small signal short circuit curre ( $I_C = 3.0$ Adc, $V_{CE} = 3.0$ Vdc, f = 1.0 MHz)	nt transfer ratio	f <sub>T</sub>	4.0	-	MHz
Output Capacitance ( $V_{CB}$ = 10 Vdc, I <sub>E</sub> = 0, f = 0.1 MHz)	BDW42 BDW46/BDW47	C <sub>ob</sub>	-	200 300	pF
Small–Signal Current Gain (I <sub>C</sub> = 3.0 Adc, V <sub>CE</sub> = 3.0 Vdc, f = 1.0 kHz)		h <sub>fe</sub>	300	_	

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.
Pulse Test: Pulse Width = 300 μs, Duty Cycle = 2.0%.
Pulse Test non repetitive: Pulse Width = 250 ms.







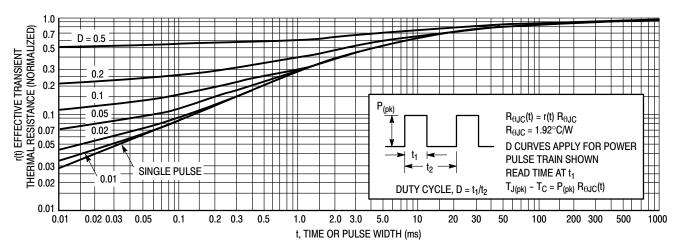
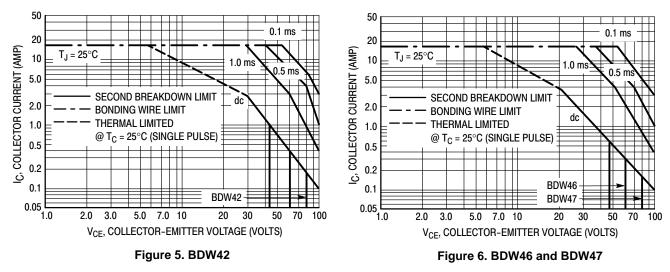


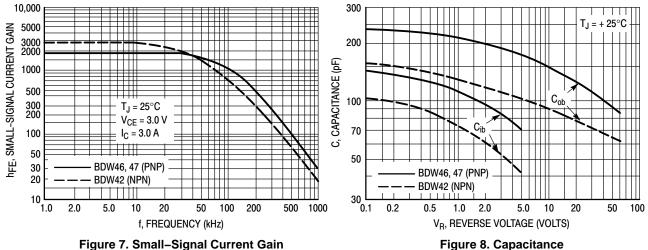
Figure 4. Thermal Response



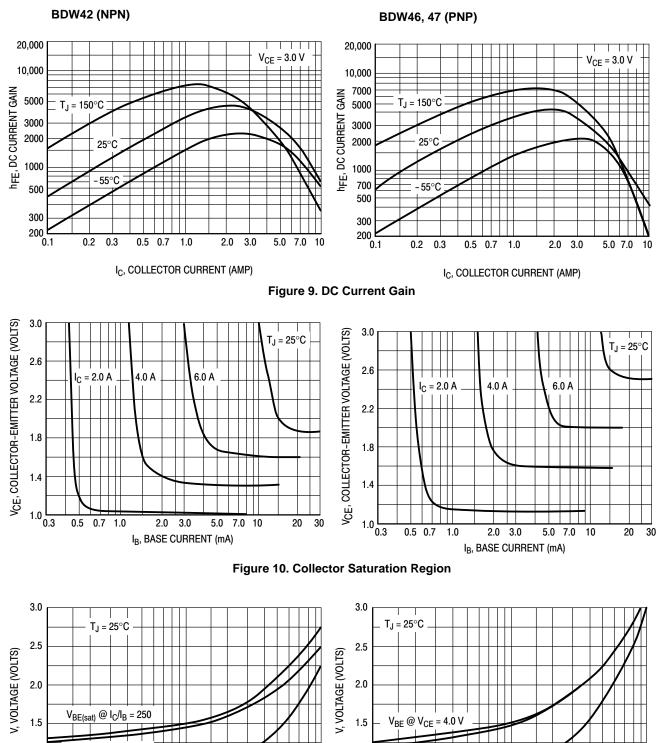
#### **ACTIVE-REGION SAFE OPERATING AREA**

There are two limitations on the power handling ability of a transistor: average junction temperature and second breakdown. Safe operating area curves indicate  $I_C - V_{CE}$  limits of the transistor that must be observed for reliable operation; i.e., the transistor must not be subjected to greater dissipation than the curves indicate. The data of Figure 5 and 6 is based on  $T_{J(pk)} = 200^{\circ}C$ ;  $T_C$  is variable depending on conditions.

Second breakdown pulse limits are valid for duty cycles to 10% provided  $T_{J(pk)} \leq 200^{\circ}C$ .  $T_{J(pk)}$  may be calculated from the data in Figure 4. At high case temperatures, thermal limitations will reduce the power that can be handled to values less than the limitations imposed by second breakdown. \*Linear extrapolation







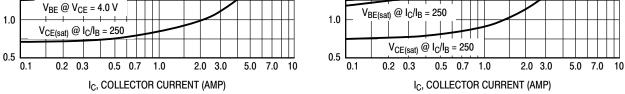
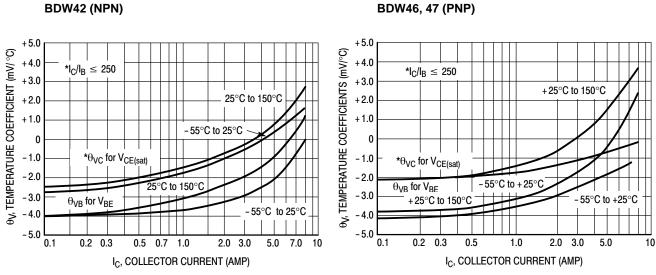
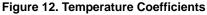
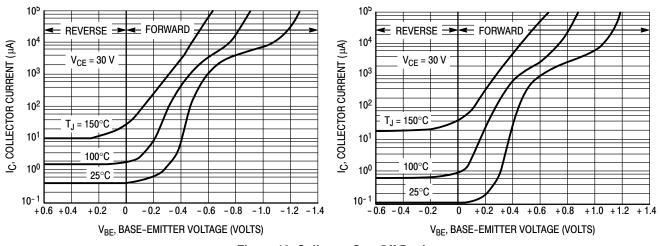


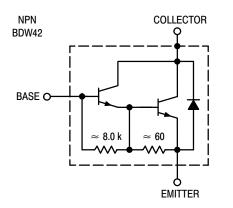
Figure 11. "On" Voltages











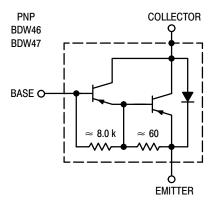
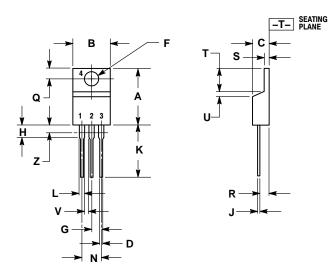


Figure 14. Darlington Schematic

#### PACKAGE DIMENSIONS

**TO-220** CASE 221A-09 ISSUE AH



NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.

2. CONTROLLING DIMENSION: INCH.

 DIMENSION Z DEFINES A ZONE WHERE ALL BODY AND LEAD IRREGULARITIES ARE ALLOWED.

	INCHES		MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.570	0.620	14.48	15.75
В	0.380	0.415	9.66	10.53
С	0.160	0.190	4.07	4.83
D	0.025	0.038	0.64	0.96
F	0.142	0.161	3.61	4.09
G	0.095	0.105	2.42	2.66
Η	0.110	0.161	2.80	4.10
ſ	0.014	0.024	0.36	0.61
Κ	0.500	0.562	12.70	14.27
L	0.045	0.060	1.15	1.52
Ν	0.190	0.210	4.83	5.33
Q	0.100	0.120	2.54	3.04
R	0.080	0.110	2.04	2.79
S	0.045	0.055	1.15	1.39
Т	0.235	0.255	5.97	6.47
U	0.000	0.050	0.00	1.27
۷	0.045		1.15	
Ζ		0.080		2.04

STYLE 1: PIN 1. BASE 2. COLLECTOR 3. EMITTER 4. COLLECTOR

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