



# BGY885A

860 MHz, 18.5 dB push-pull amplifier

Rev. 7 — 19 September 2011

Product data sheet

## 1. Product profile

### 1.1 General description

Hybrid amplifier module for CATV systems operating over a frequency range of 40 MHz to 860 MHz with a supply voltage of 24 V (DC).

#### CAUTION



This device is sensitive to ElectroStatic Discharge (ESD). Therefore care should be taken during transport and handling.

### 1.2 Features and benefits

- Excellent linearity
- Extremely low noise
- Silicon nitride passivation
- Rugged construction
- Gold metallization ensures excellent reliability

### 1.3 Quick reference data

Table 1. Quick reference data

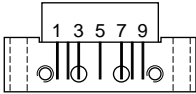
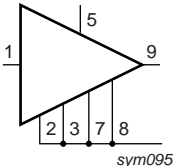
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$G_p$	power gain	$f = 50$ MHz	18	18.5	19	dB
		$f = 860$ MHz	18.5	19.5	-	dB
$I_{tot}$	total current consumption (DC)	$V_B = 24$ V	[1] -	225	240	mA

[1] The module normally operates at  $V_B = 24$  V, but is able to withstand supply transients up to 30 V.



## 2. Pinning information

Table 2. Pinning

Pin	Description	Simplified outline	Symbol
1	input		
2	common		
3	common		
5	+V <sub>B</sub>		
7	common		
8	common		
9	output		

## 3. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
BGY885A	-	rectangular single-ended package; aluminium flange; 2 vertical mounting holes; 2 × 6-32 UNC and 2 extra horizontal mounting holes; 7 gold-plated in-line leads	SOT115J

## 4. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>i</sub>	RF input voltage		-	65	dBmV
T <sub>stg</sub>	storage temperature		-40	+100	°C
T <sub>mb</sub>	mounting base temperature		-20	+100	°C

## 5. Characteristics

**Table 5. Characteristics**

Bandwidth 40 MHz to 860 MHz;  $V_B = 24\text{ V}$ ;  $T_{mb} = 30\text{ °C}$ ;  $Z_S = Z_L = 75\ \Omega$ ; unless otherwise specified.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$G_p$	power gain	$f = 50\text{ MHz}$	18	18.5	19	dB
		$f = 860\text{ MHz}$	18.5	19.5	-	dB
SL	slope cable equivalent	$f = 40\text{ MHz to }860\text{ MHz}$	0	0.8	2	dB
FL	flatness of frequency response	$f = 40\text{ MHz to }860\text{ MHz}$	-	$\pm 0.2$	$\pm 0.3$	dB
$S_{11}$	input return losses	$f = 40\text{ MHz to }80\text{ MHz}$	20	31	-	dB
		$f = 80\text{ MHz to }160\text{ MHz}$	18.5	30	-	dB
		$f = 160\text{ MHz to }320\text{ MHz}$	17	27.5	-	dB
		$f = 320\text{ MHz to }640\text{ MHz}$	15.5	25	-	dB
		$f = 640\text{ MHz to }860\text{ MHz}$	14	20.5	-	dB
$S_{22}$	output return losses	$f = 40\text{ MHz to }80\text{ MHz}$	20	29	-	dB
		$f = 80\text{ MHz to }160\text{ MHz}$	18.5	27.5	-	dB
		$f = 160\text{ MHz to }320\text{ MHz}$	17	24	-	dB
		$f = 320\text{ MHz to }640\text{ MHz}$	15.5	21	-	dB
		$f = 640\text{ MHz to }860\text{ MHz}$	14	21	-	dB
$\varphi_{s21}$	phase response	$f = 50\text{ MHz}$	-45	-	+45	deg
CTB	composite triple beat	49 channels flat; $V_o = 44\text{ dBmV}$ ; measured at 859.25 MHz	-	-65	-61	dB
$X_{mod}$	cross modulation	49 channels flat; $V_o = 44\text{ dBmV}$ ; measured at 55.25 MHz	-	-65	-61	dB
CSO	composite second order distortion	49 channels flat; $V_o = 44\text{ dBmV}$ ; measured at 860.5 MHz	-	-67	-61	dB
$d_2$	second order distortion		[1] -	-78	-70	dB
$V_o$	output voltage	$d_{im} = -60\text{ dB}$	[2] 58	60	-	dBmV
F	noise figure	$f = 50\text{ MHz}$	-	4.5	5	dB
		$f = 450\text{ MHz}$	-	-	5.5	dB
		$f = 550\text{ MHz}$	-	-	5.5	dB
		$f = 600\text{ MHz}$	-	-	6	dB
		$f = 650\text{ MHz}$	-	-	6	dB
		$f = 750\text{ MHz}$	-	-	7	dB
		$f = 860\text{ MHz}$	-	6	8	dB
$I_{tot}$	total current consumption (DC)		[3] -	225	240	mA

[1]  $f_p = 55.25\text{ MHz}$ ;  $V_p = 44\text{ dBmV}$ ;  $f_q = 805.25\text{ MHz}$ ;  $V_q = 44\text{ dBmV}$ ; measured at  $f_p + f_q = 860.5\text{ MHz}$ .

[2] Measured according to DIN45004B:  $f_p = 851.25\text{ MHz}$ ;  $V_p = V_o$ ;  $f_q = 858.25\text{ MHz}$ ;  $V_q = V_o - 6\text{ dB}$ ;  $f_r = 860.25\text{ MHz}$ ;  $V_r = V_o - 6\text{ dB}$ ; measured at  $f_p + f_q - f_r = 849.25\text{ MHz}$ .

[3] The module normally operates at  $V_B = 24\text{ V}$ , but is able to withstand supply transients up to 30 V.

**Table 6. Characteristics**Bandwidth 40 MHz to 750 MHz;  $V_B = 24$  V;  $T_{mb} = 30$  °C;  $Z_S = Z_L = 75$   $\Omega$ ; unless otherwise specified.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit	
G <sub>p</sub>	power gain	f = 50 MHz	18	18.5	19	dB	
		f = 750 MHz	18.5	-	-	dB	
SL	slope cable equivalent	f = 40 MHz to 750 MHz	0	-	1.5	dB	
FL	flatness of frequency response	f = 40 MHz to 750 MHz	-	-	±0.3	dB	
S <sub>11</sub>	input return losses	f = 40 MHz to 80 MHz	20	31	-	dB	
		f = 80 MHz to 160 MHz	18.5	30	-	dB	
		f = 160 MHz to 320 MHz	17	27.5	-	dB	
		f = 320 MHz to 640 MHz	15.5	25	-	dB	
		f = 640 MHz to 750 MHz	14	20.5	-	dB	
S <sub>22</sub>	output return losses	f = 40 MHz to 80 MHz	20	29	-	dB	
		f = 80 MHz to 160 MHz	18.5	27.5	-	dB	
		f = 160 MHz to 320 MHz	17	24	-	dB	
		f = 320 MHz to 640 MHz	15.5	21	-	dB	
		f = 640 MHz to 750 MHz	14	21	-	dB	
φ <sub>s21</sub>	phase response	f = 50 MHz	-45	-	+45	deg	
CTB	composite triple beat	110 channels flat; V <sub>o</sub> = 44 dBmV; measured at 745.25 MHz	-	-55	-53	dB	
X <sub>mod</sub>	cross modulation	110 channels flat; V <sub>o</sub> = 44 dBmV; measured at 55.25 MHz	-	-58	-57	dB	
CSO	composite second order distortion	110 channels flat; V <sub>o</sub> = 44 dBmV; measured at 746.5 MHz	-	-65	-53	dB	
d <sub>2</sub>	second order distortion		[1]	-	-65	dB	
V <sub>o</sub>	output voltage	d <sub>im</sub> = -60 dB	[2]	59	-	dBmV	
F	noise figure	see <a href="#">Table 5</a>	-	-	-	dB	
I <sub>tot</sub>	total current consumption (DC)		[3]	-	225	240	mA

[1] f<sub>p</sub> = 55.25 MHz; V<sub>p</sub> = 44 dBmV; f<sub>q</sub> = 691.25 MHz; V<sub>q</sub> = 44 dBmV; measured at f<sub>p</sub> + f<sub>q</sub> = 746.5 MHz.[2] Measured according to DIN45004B: f<sub>p</sub> = 740.25 MHz; V<sub>p</sub> = V<sub>o</sub>; f<sub>q</sub> = 747.25 MHz; V<sub>q</sub> = V<sub>o</sub> - 6 dB; f<sub>r</sub> = 749.25 MHz; V<sub>r</sub> = V<sub>o</sub> - 6 dB; measured at f<sub>p</sub> + f<sub>q</sub> - f<sub>r</sub> = 738.25 MHz.[3] The module normally operates at V<sub>B</sub> = 24 V, but is able to withstand supply transients up to 30 V.**Table 7. Characteristics**Bandwidth 40 MHz to 600 MHz; V<sub>B</sub> = 24 V; T<sub>mb</sub> = 30 °C; Z<sub>S</sub> = Z<sub>L</sub> = 75  $\Omega$ ; unless otherwise specified.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
G <sub>p</sub>	power gain	f = 50 MHz	18	18.5	19	dB
		f = 600 MHz	18.5	-	-	dB
SL	slope cable equivalent	f = 40 MHz to 600 MHz	0	-	1.5	dB
FL	flatness of frequency response	f = 40 MHz to 600 MHz	-	-	±0.3	dB
S <sub>11</sub>	input return losses	f = 40 MHz to 80 MHz	20	31	-	dB
		f = 80 MHz to 160 MHz	18.5	30	-	dB
		f = 160 MHz to 320 MHz	17	27.5	-	dB
		f = 320 MHz to 600 MHz	16	25	-	dB

**Table 7. Characteristics ...continued**Bandwidth 40 MHz to 600 MHz;  $V_B = 24\text{ V}$ ;  $T_{mb} = 30\text{ °C}$ ;  $Z_S = Z_L = 75\ \Omega$ ; unless otherwise specified.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit	
S <sub>22</sub>	output return losses	f = 40 MHz to 80 MHz	20	29	-	dB	
		f = 80 MHz to 160 MHz	18.5	27.5	-	dB	
		f = 160 MHz to 320 MHz	17	24	-	dB	
		f = 320 MHz to 600 MHz	16	21	-	dB	
φ <sub>s21</sub>	phase response	f = 50 MHz	-45	-	+45	deg	
CTB	composite triple beat	85 channels flat; V <sub>o</sub> = 44 dBmV; measured at 595.25 MHz	-	-60	-57	dB	
X <sub>mod</sub>	cross modulation	85 channels flat; V <sub>o</sub> = 44 dBmV; measured at 55.25 MHz	-	-60.5	-59	dB	
CSO	composite second order distortion	85 channels flat; V <sub>o</sub> = 44 dBmV; measured at 596.5 MHz	-	-64.5	-58	dB	
d <sub>2</sub>	second order distortion		[1]	-	-79	-70	dB
V <sub>o</sub>	output voltage	d <sub>im</sub> = -60 dB	[2]	61	64.5	-	dBmV
F	noise figure	see <a href="#">Table 5</a>	-	-	-	dB	
I <sub>tot</sub>	total current consumption (DC)		[3]	-	225	240	mA

[1] f<sub>p</sub> = 55.25 MHz; V<sub>p</sub> = 44 dBmV; f<sub>q</sub> = 541.25 MHz; V<sub>q</sub> = 44 dBmV; measured at f<sub>p</sub> + f<sub>q</sub> = 596.5 MHz.[2] Measured according to DIN45004B: f<sub>p</sub> = 590.25 MHz; V<sub>p</sub> = V<sub>o</sub>; f<sub>q</sub> = 597.25 MHz; V<sub>q</sub> = V<sub>o</sub> - 6 dB; f<sub>r</sub> = 599.25 MHz; V<sub>r</sub> = V<sub>o</sub> - 6 dB; measured at f<sub>p</sub> + f<sub>q</sub> - f<sub>r</sub> = 588.25 MHz.[3] The module normally operates at V<sub>B</sub> = 24 V, but is able to withstand supply transients up to 30 V.**Table 8. Characteristics**Bandwidth 40 MHz to 550 MHz;  $V_B = 24\text{ V}$ ;  $T_{mb} = 30\text{ °C}$ ;  $Z_S = Z_L = 75\ \Omega$ ; unless otherwise specified.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
G <sub>p</sub>	power gain	f = 50 MHz	18	18.5	19	dB
		f = 550 MHz	18.5	-	-	dB
SL	slope cable equivalent	f = 40 MHz to 550 MHz	0	-	1.5	dB
FL	flatness of frequency response	f = 40 MHz to 550 MHz	-	-	±0.3	dB
S <sub>11</sub>	input return losses	f = 40 MHz to 80 MHz	20	31	-	dB
		f = 80 MHz to 160 MHz	18.5	30	-	dB
		f = 160 MHz to 320 MHz	17	27.5	-	dB
		f = 320 MHz to 550 MHz	16	25	-	dB
S <sub>22</sub>	output return losses	f = 40 MHz to 80 MHz	20	29	-	dB
		f = 80 MHz to 160 MHz	18.5	27.5	-	dB
		f = 160 MHz to 320 MHz	17	24	-	dB
		f = 320 MHz to 550 MHz	16	21	-	dB
φ <sub>s21</sub>	phase response	f = 50 MHz	-45	-	+45	deg
CTB	composite triple beat	77 channels flat; V <sub>o</sub> = 44 dBmV; measured at 547.25 MHz	-	-61	-60	dB
X <sub>mod</sub>	cross modulation	77 channels flat; V <sub>o</sub> = 44 dBmV; measured at 55.25 MHz	-	-61	-60	dB
CSO	composite second order distortion	77 channels flat; V <sub>o</sub> = 44 dBmV; measured at 548.5 MHz	-	-69	-60	dB

**Table 8. Characteristics ...continued**

Bandwidth 40 MHz to 550 MHz;  $V_B = 24\text{ V}$ ;  $T_{mb} = 30\text{ °C}$ ;  $Z_S = Z_L = 75\ \Omega$ ; unless otherwise specified.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$d_2$	second order distortion		[1]	-	-72	dB
$V_o$	output voltage	$d_{im} = -60\text{ dB}$	[2]	62	-	dBmV
F	noise figure	see <a href="#">Table 5</a>	-	-	-	dB
$I_{tot}$	total current consumption (DC)		[3]	-	225	240 mA

[1]  $f_p = 55.25\text{ MHz}$ ;  $V_p = 44\text{ dBmV}$ ;  $f_q = 493.25\text{ MHz}$ ;  $V_q = 44\text{ dBmV}$ ; measured at  $f_p + f_q = 548.5\text{ MHz}$ .

[2] Measured according to DIN45004B:  $f_p = 540.25\text{ MHz}$ ;  $V_p = V_o$ ;  $f_q = 547.25\text{ MHz}$ ;  $V_q = V_o - 6\text{ dB}$ ;  $f_r = 549.25\text{ MHz}$ ;  $V_r = V_o - 6\text{ dB}$ ; measured at  $f_p + f_q - f_r = 538.25\text{ MHz}$ .

[3] The module normally operates at  $V_B = 24\text{ V}$ , but is able to withstand supply transients up to 30 V.

**Table 9. Characteristics**

Bandwidth 40 MHz to 450 MHz;  $V_B = 24\text{ V}$ ;  $T_{mb} = 30\text{ °C}$ ;  $Z_S = Z_L = 75\ \Omega$ ; unless otherwise specified.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$G_p$	power gain	$f = 50\text{ MHz}$	18	18.5	19	dB
		$f = 450\text{ MHz}$	18.5	-	-	dB
SL	slope cable equivalent	$f = 40\text{ MHz to }450\text{ MHz}$	0	-	1.5	dB
FL	flatness of frequency response	$f = 40\text{ MHz to }450\text{ MHz}$	-	-	$\pm 0.3$	dB
$s_{11}$	input return losses	$f = 40\text{ MHz to }80\text{ MHz}$	20	31	-	dB
		$f = 80\text{ MHz to }160\text{ MHz}$	18.5	30	-	dB
		$f = 160\text{ MHz to }320\text{ MHz}$	17	27.5	-	dB
		$f = 320\text{ MHz to }450\text{ MHz}$	16	25	-	dB
$s_{22}$	output return losses	$f = 40\text{ MHz to }80\text{ MHz}$	20	29	-	dB
		$f = 80\text{ MHz to }160\text{ MHz}$	18.5	27.5	-	dB
		$f = 160\text{ MHz to }320\text{ MHz}$	17	24	-	dB
		$f = 320\text{ MHz to }450\text{ MHz}$	16	21	-	dB
$\varphi_{s21}$	phase response	$f = 50\text{ MHz}$	-45	-	+45	deg
CTB	composite triple beat	60 channels flat; $V_o = 46\text{ dBmV}$ ; measured at 445.25 MHz	-	-	-61	dB
$X_{mod}$	cross modulation	60 channels flat; $V_o = 46\text{ dBmV}$ ; measured at 55.25 MHz	-	-	-60	dB
CSO	composite second order distortion	60 channels flat; $V_o = 46\text{ dBmV}$ ; measured at 446.5 MHz	-	-	-61	dB
$d_2$	second order distortion		[1]	-	-75	dB
$V_o$	output voltage	$d_{im} = -60\text{ dB}$	[2]	64	-	dBmV
F	noise figure	see <a href="#">Table 5</a>	-	-	-	dB
$I_{tot}$	total current consumption (DC)		[3]	-	225	240 mA

[1]  $f_p = 55.25\text{ MHz}$ ;  $V_p = 46\text{ dBmV}$ ;  $f_q = 391.25\text{ MHz}$ ;  $V_q = 46\text{ dBmV}$ ; measured at  $f_p + f_q = 446.5\text{ MHz}$ .

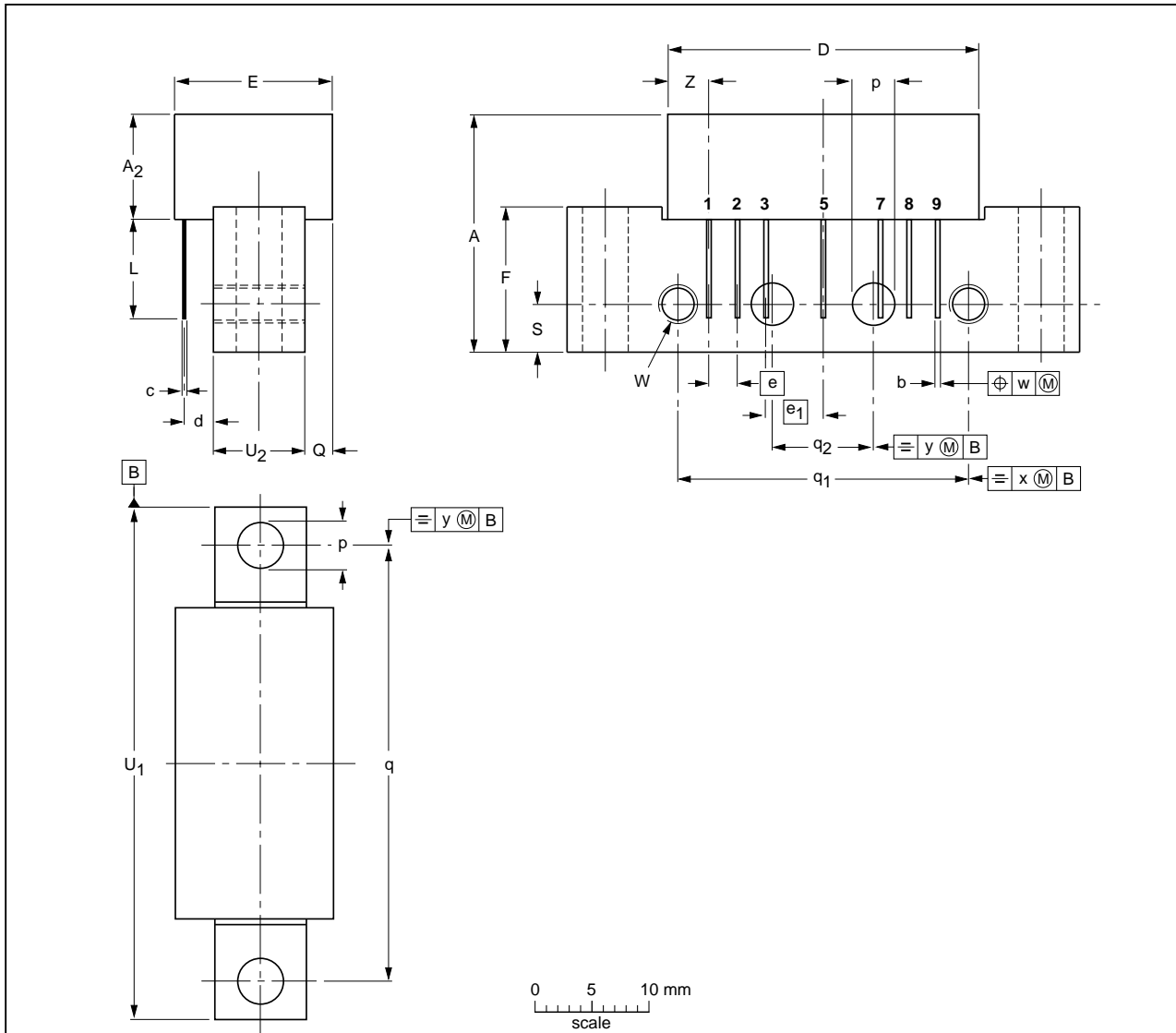
[2] Measured according to DIN45004B:  $f_p = 440.25\text{ MHz}$ ;  $V_p = V_o$ ;  $f_q = 447.25\text{ MHz}$ ;  $V_q = V_o - 6\text{ dB}$ ;  $f_r = 449.25\text{ MHz}$ ;  $V_r = V_o - 6\text{ dB}$ ; measured at  $f_p + f_q - f_r = 438.25\text{ MHz}$ .

[3] The module normally operates at  $V_B = 24\text{ V}$ , but is able to withstand supply transients up to 30 V.

## 6. Package outline

Rectangular single-ended package; aluminium flange; 2 vertical mounting holes; 2 x 6-32 UNC and 2 extra horizontal mounting holes; 7 gold-plated in-line leads

SOT115J



**DIMENSIONS (mm are the original dimensions)**

UNIT	A max.	A <sub>2</sub> max.	b	c	D max.	d	E max.	e	e <sub>1</sub>	F	L min.	p	Q max.	q	q <sub>1</sub>	q <sub>2</sub>	S	U <sub>1</sub>	U <sub>2</sub>	W	w	x	y	Z max.
mm	20.8	9.5	0.51 0.38	0.25	27.2	2.04 2.54	13.75	2.54	5.08	12.7	8.8	4.15 3.85	2.4	38.1	25.4	10.2	4.2	44.75 44.25	8.2 7.8	6-32 UNC	0.25	0.7	0.1	3.8

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	JEITA			
SOT115J						-04-02-04- 10-06-18

**Fig 1. Package outline SOT115J**

## 7. Revision history

**Table 10. Revision history**

Document ID	Release date	Data sheet status	Change notice	Supersedes
BGY885A v.7	20110919	Product data sheet	-	BGY885A v.6
Modifications:		<ul style="list-style-type: none"><li>• The format of this data sheet has been redesigned to comply with the new identity guidelines of NXP Semiconductors.</li><li>• Legal texts have been adapted to the new company name where appropriate.</li><li>• Package outline drawings have been updated to the latest version.</li></ul>		
BGY885A v.6 (9397 750 14434)	20050322	Product data sheet	-	BGY885A v.5
BGY885A v.5 (9397 750 08818)	20011022	Product specification	-	BGY885A v.4
BGY885A v.4 (9397 750 05444)	19990330	Product specification	-	BGY885A v.3
BGY885A v.3 (9397 750 02093)	19970407	Product specification	-	BGY885A v.2
BGY885A v.2	19950201	Product specification	-	n.a.



## 8. Legal information

### 8.1 Data sheet status

Document status <sup>[1][2]</sup>	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL <http://www.nxp.com>.

### 8.2 Definitions

**Draft** — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. NXP Semiconductors does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

**Short data sheet** — A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local NXP Semiconductors sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

**Product specification** — The information and data provided in a Product data sheet shall define the specification of the product as agreed between NXP Semiconductors and its customer, unless NXP Semiconductors and customer have explicitly agreed otherwise in writing. In no event however, shall an agreement be valid in which the NXP Semiconductors product is deemed to offer functions and qualities beyond those described in the Product data sheet.

### 8.3 Disclaimers

**Limited warranty and liability** — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information.

In no event shall NXP Semiconductors be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, NXP Semiconductors' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the *Terms and conditions of commercial sale* of NXP Semiconductors.

**Right to make changes** — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

**Suitability for use** — NXP Semiconductors products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or

malfunction of an NXP Semiconductors product can reasonably be expected to result in personal injury, death or severe property or environmental damage. NXP Semiconductors accepts no liability for inclusion and/or use of NXP Semiconductors products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

**Applications** — Applications that are described herein for any of these products are for illustrative purposes only. NXP Semiconductors makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using NXP Semiconductors products, and NXP Semiconductors accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the NXP Semiconductors product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

NXP Semiconductors does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using NXP Semiconductors products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). NXP does not accept any liability in this respect.

**Limiting values** — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) will cause permanent damage to the device. Limiting values are stress ratings only and (proper) operation of the device at these or any other conditions above those given in the Recommended operating conditions section (if present) or the Characteristics sections of this document is not warranted. Constant or repeated exposure to limiting values will permanently and irreversibly affect the quality and reliability of the device.

**Terms and conditions of commercial sale** — NXP Semiconductors products are sold subject to the general terms and conditions of commercial sale, as published at <http://www.nxp.com/profile/terms>, unless otherwise agreed in a valid written individual agreement. In case an individual agreement is concluded only the terms and conditions of the respective agreement shall apply. NXP Semiconductors hereby expressly objects to applying the customer's general terms and conditions with regard to the purchase of NXP Semiconductors products by customer.

**No offer to sell or license** — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

**Export control** — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from national authorities.

**Quick reference data** — The Quick reference data is an extract of the product data given in the Limiting values and Characteristics sections of this document, and as such is not complete, exhaustive or legally binding.

**Non-automotive qualified products** — Unless this data sheet expressly states that this specific NXP Semiconductors product is automotive qualified, the product is not suitable for automotive use. It is neither qualified nor tested in accordance with automotive testing or application requirements. NXP Semiconductors accepts no liability for inclusion and/or use of non-automotive qualified products in automotive equipment or applications.

In the event that customer uses the product for design-in and use in automotive applications to automotive specifications and standards, customer (a) shall use the product without NXP Semiconductors' warranty of the

product for such automotive applications, use and specifications, and (b) whenever customer uses the product for automotive applications beyond NXP Semiconductors' specifications such use shall be solely at customer's own risk, and (c) customer fully indemnifies NXP Semiconductors for any liability, damages or failed product claims resulting from customer design and use of the product for automotive applications beyond NXP Semiconductors' standard warranty and NXP Semiconductors' product specifications.

## 8.4 Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

## 9. Contact information

For more information, please visit: <http://www.nxp.com>

For sales office addresses, please send an email to: [salesaddresses@nxp.com](mailto:salesaddresses@nxp.com)

## 10. Contents

---

<b>1</b>	<b>Product profile</b> . . . . .	<b>1</b>
1.1	General description . . . . .	1
1.2	Features and benefits . . . . .	1
1.3	Quick reference data . . . . .	1
<b>2</b>	<b>Pinning information</b> . . . . .	<b>2</b>
<b>3</b>	<b>Ordering information</b> . . . . .	<b>2</b>
<b>4</b>	<b>Limiting values</b> . . . . .	<b>2</b>
<b>5</b>	<b>Characteristics</b> . . . . .	<b>3</b>
<b>6</b>	<b>Package outline</b> . . . . .	<b>7</b>
<b>7</b>	<b>Revision history</b> . . . . .	<b>8</b>
<b>8</b>	<b>Legal information</b> . . . . .	<b>9</b>
8.1	Data sheet status . . . . .	9
8.2	Definitions . . . . .	9
8.3	Disclaimers . . . . .	9
8.4	Trademarks . . . . .	10
<b>9</b>	<b>Contact information</b> . . . . .	<b>10</b>
<b>10</b>	<b>Contents</b> . . . . .	<b>11</b>

---

Please be aware that important notices concerning this document and the product(s) described herein, have been included in section 'Legal information'.

---

© NXP B.V. 2011.

All rights reserved.

For more information, please visit: <http://www.nxp.com>

For sales office addresses, please send an email to: [salesaddresses@nxp.com](mailto:salesaddresses@nxp.com)

Date of release: 19 September 2011

Document identifier: BGY885A