

### LVDS Interface ICs



# 4bit LVDS Receiver

BU90LV048 No.12057EAT03

### Description

LVDS Interface IC of ROHM "Serializer" "Deserializer" operate from 8MHz to 150MHz wide clock range, and number of bits range is from 35 to 70. Data is transmitted seven times (7X) stream and reduce cable number by 3(1/3) or less. The ROHM's LVDS has low swing mode to be able to expect further low EMI.

Driver and Receiver of 4 bits operate to 250MHz. It can be used for a variety of purposes, home appliances such as LCD-TV, business machines such as decoders, instruments, and medical equipment.

## Features

- 1) >500 Mbps (250 MHz) switching rates
- 2) Flow-through pinout simplifies PCB layout
- 3) 150 ps channel-to-channel skew (typical)
- 4) 100 ps differential skew (typical)
- 5) 3.7 ns maximum propagation delay
- 6) 3.3V power supply design
- 7) 6mA and 8mA selectable output drive strength
- 8) Accepts small swing (200 mV typical) differential signal levels
- 9) Supports open, short and terminated input fail-safe
- 10) Conforms to ANSI/TIA/EIA-644 Standard
- 11) Industrial temperature operating range (-40°C to +85°C)

#### Applications

Car Navigation System
Copier
Digital TV (Signal System)
FA equipment
Medical equipment
Vending machine, Ticket vending machine

#### Precaution

- ■This chip is not designed to protect from radioactivity.
- This document may be used as strategic technical data which subjects to COCOM regulations.

BU90LV048 Technical Note

# ●Block Diagram

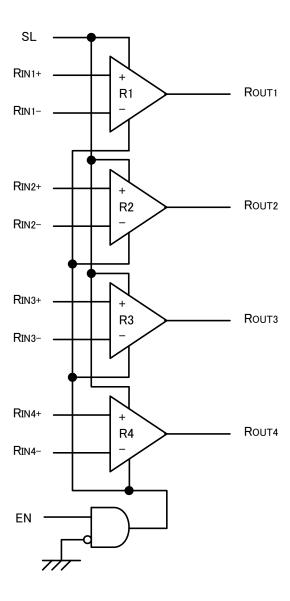


Fig.1. Block Diagram

# ●SSOP-B16 Package Outline and Specification

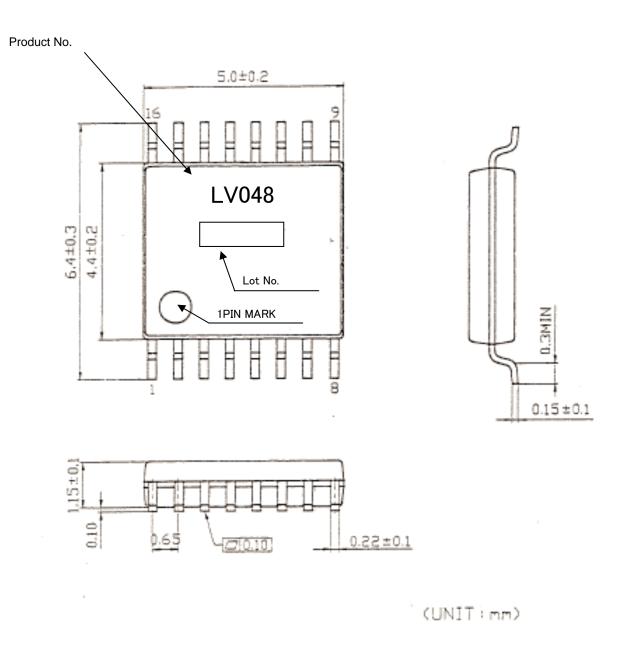


Fig.2. SSOP-B16 Package Outline and Specification

BU90LV048 Technical Note

# ●Pin Configuration

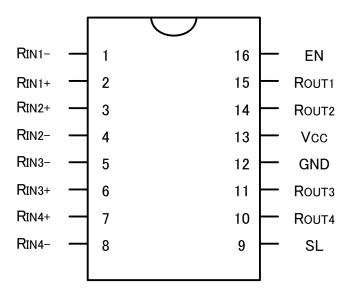


Fig.3. Pin Diagram (Top View)

# ●Pin Description

Table 1 : Pin Description

Pin Name	Pin No.	Туре	Descriptions
RIN+	2, 3, 6, 7	LVDS In	Non-inverting receiver input pin
RIN-	1, 4, 5, 8	LVDS In	Inverting receiver input pin
ROUT	10, 11, 14, 15	LVCMOS Out	Receiver output pin
SL	9	LVCMOS In	Drive strength select pin: When SL is low or open, Rout set 8mA mode. When SL is high, Rout set 6mA mode.
EN	16	LVCMOS In	Receiver enable pin: When EN is Low or open, the receiver is disabled. When EN is high, the receiver is enabled.
VCC	13	Power	Power supply pin, +3.3V±0.3V
GND	12	GND	Ground pin

Function Description

Inction Description		INPUT	OUTPUTS	5 · 0 · · · · ·
EN	SL	R <sub>IN+</sub> - R <sub>IN-</sub>	R <sub>OUT</sub>	Drive Strength
		VID . 0V	Н	
н	L or Open	VID0.1V	L	8mA
		Full Fail-safe OPEN/SHORT or Terminated	Н	
		VID . 0V	Н	
н	Н	VID0.1V	L	6mA
		Full Fail-safe OPEN/SHORT or Terminated	Н	
All other combinations of EN, SL inputs		X	Z	

●Absolute Maximum Ratings

Item	Symbol	Va	Unit	
петі	Symbol	Min.	Max.	Offic
Supply voltage	VCC	-0.3	4.0	V
Input voltage	VIN	-0.3	VCC+0.3	V
Output voltage	VOUT	-0.3	VCC+0.3	V
Storage temperature range	Tstg	-55	125	°C

### ●Package Power

Package	PD(mW)	DERATING(mW/°C) *1
SSOP-B16	400	4.0
330F-B10	450 <sup>*2</sup>	4.5 <sup>**2</sup>

**%1** At temperature Ta > 25°C

**X2** Package power when mounting on the PCB board.

The size of PCB board  $:70 \times 70 \times 1.6 \text{ (mm}^3)$ 

The material of PCB board :The FR4 glass epoxy board.(3% or less copper foil area)

## Recommended Operating Conditions

Item	Symbol	Value			Unit	Condition	
nem	Symbol	Min.	Тур.	Max.	Offic	Condition	
Supply voltage	Vcc	3.0	3.3	3.6	V		
Operating temperature range	Topr	-40	-	85	°C		

## **DC** Characteristics

Parameter	Symbol	Conditions	Pin	Min	Тур	Max	Units
Differential Input High Threshold	V <sub>TH</sub>	V <sub>CM</sub> = +1.2V, 0.05V,	R <sub>IN+</sub> ,	-	-	100	mV
Differential Input Low Threshold	V <sub>TL</sub>	2.95V	R <sub>IN-</sub>	-100	-	-	mV
Common-Mode Voltage Range	VCMR	VID = 200mV pk to pk		0.1	-	2.3	V
Input Current	I <sub>IN</sub>	V <sub>IN</sub> = 0 or Vcc		-20	-	+20	μΑ
Output High Voltage	V <sub>OH1</sub>	$I_{OH} = -8 \text{ mA}, V_{ID} =$ +200 mV, SL=low	R <sub>OUT</sub>	V <sub>CC</sub> - 0.4	-	-	V
Output High Voltage	V <sub>OH2</sub>	$I_{OH} = -6 \text{ mA}, V_{ID} =$ +200 mV, SL= high		V <sub>CC</sub> - 0.4	-	-	V
Output Low Voltage	V <sub>OL1</sub>	$I_{OL} = 8 \text{ mA}, V_{ID} = -200 \text{ mV}, SL=low$		-	-	0.4	V
Output Low Voltage	V <sub>OL2</sub>	$I_{OL} = 6 \text{ mA}, V_{ID} = -200 \text{ mV}, SL= \text{high}$		-	-	0.4	V
Output Short Circuit Current	I <sub>OS</sub>	Enabled, V <sub>OUT</sub> = 0V		-15	-80	-	mA
Output 3-STATE Current	I <sub>OZ</sub>	Disabled, V <sub>OUT</sub> = 0V or V <sub>CC</sub>		-10	±1	+10	uA
Input High Voltage	V <sub>IH</sub>		SL	V <sub>CC</sub> × 0.8	-	V <sub>cc</sub>	V
Input Low Voltage	V <sub>IL</sub>		EN	GND	-	V <sub>CC</sub> × 0.2	V
Input Current	I <sub>1</sub>	$V_{IN} = 0V \text{ or } V_{CC},$ Other Input = $V_{CC}$ or GND		-10	-	+10	μΑ
Input Clamp Voltage	V <sub>CL</sub>	I <sub>CL</sub> = −18 mA		-1.5	-0.8	-	V
No Load Supply Current Receivers Enabled	I <sub>CC</sub>	EN = V <sub>CC</sub> , Inputs Open	$V_{CC}$	-	1	-	mA
No Load Supply Current Receivers Disabled	I <sub>CCZ</sub>	EN= GND, SL = GND, Inputs Open		-	0.5	-	mA

## Switching Characteristics

 $V_{CC}$  = +3.3V ±0.3V,  $T_{opr}$  = -40°C to +85°C.

Parameter	Symbol	Conditions	Min	Тур	Max	Units
Differential Propagation Delay High to Low	t <sub>PHLD</sub>		1.2	2.0	3.7	ns
Differential Propagation Delay Low to High	t <sub>PLHD</sub>		1.2	1.9	3.7	ns
Differential Pulse Skew  t <sub>PHLD</sub> - t <sub>PLHD</sub>	t <sub>SKD1</sub>		0	0.1	0.4	ns
Differential Channel-to-Channel Skew; same device	t <sub>SKD2</sub>	$C_L = 15pF$ $V_{ID} = 200mV$	0	0.15	0.5	ns
Differential Part to Part Skew	t <sub>SKD3</sub>	(Fig.4 and Fig.5)	-	-	1.0	ns
Differential Part to Part Skew	t <sub>SKD4</sub>		-	-	1.5	ns
Rise Time	t <sub>TLH</sub>		-	0.5	1.5	ns
Fall Time	t <sub>THL</sub>		-	0.5	1.5	ns
Disable Time High to Z	t <sub>PHZ</sub>		-	8	14	ns
Disable Time Low to Z	t <sub>PLZ</sub>	$R_L = 2k\Omega$ $C_L = 15pF$	-	8	14	ns
Enable Time Z to High	t <sub>PZH</sub>	(Fig.6 and Fig.7)	-	3	14	ns
Enable Time Z to Low	t <sub>PZL</sub>		-	9	14	ns
Maximum Operating Frequency	f <sub>Max</sub>	All Channels Switching	250	-	-	MHz

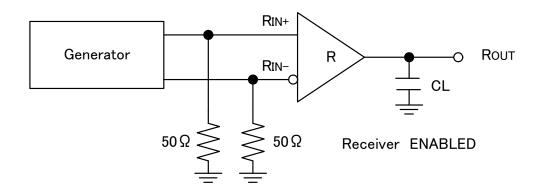


Fig.4. Receiver Propagation Delay and Transition Time Test Circuit

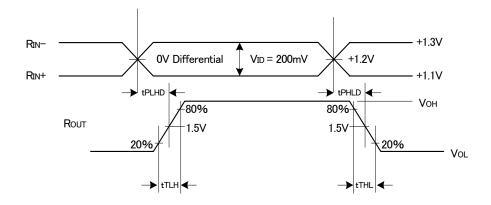


Fig.5. Receiver Propagation Delay and Transition Time Waveforms

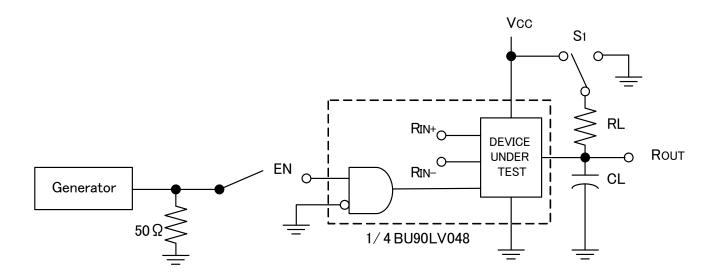


Fig.6. Receiver 3-STATE Delay Test Circuit

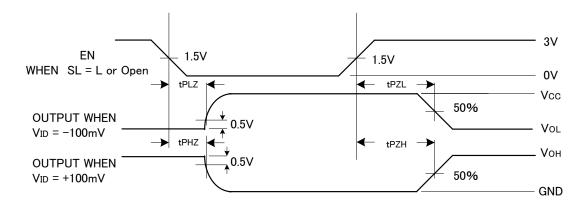


Figure 7. Receiver 3-STATE Delay Waveforms

BU90LV048 Technical Note

# Typical Application

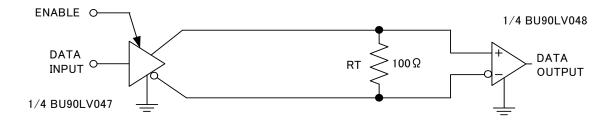
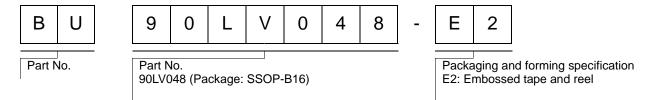
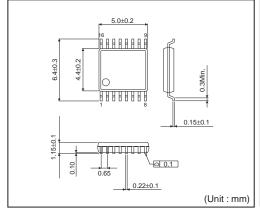


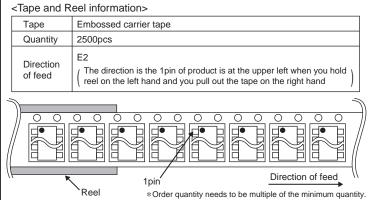
Fig.8. Point-to-Point Application

## Ordering part number



### SSOP-B16





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(Note1) Medical Equipment Classification of the Specific Applications

JAPAN	USA	EU	CHINA
CLASSⅢ	CI ACCIII	CLASS II b	СГУССШ
CLASSIV	CLASSII	CLASSⅢ	CLASSIII

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  - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
  - [f] Sealing or coating our Products with resin or other coating materials
  - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
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- 7. De-rate Power Dissipation (Pd) depending on Ambient temperature (Ta). When used in sealed area, confirm the actual ambient temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

#### Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used; if flow soldering method is preferred, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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#### **Precaution for Electrostatic**

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
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  - [b] the temperature or humidity exceeds those recommended by ROHM
  - the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
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- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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Rev.001



# BU90LV048 - Web Page

**Distribution Inventory** 

Part Number	BU90LV048
Package	SSOP-B16
Unit Quantity	2500
Minimum Package Quantity	2500
Packing Type	Taping
Constitution Materials List	inquiry
RoHS	Yes