## BUJ105AB

### **GENERAL DESCRIPTION**

High-voltage, high-speed planar-passivated npn power switching transistor in SOT404 (D<sup>2</sup>-PAK) surface-mount package intended for use in high frequency electronic lighting ballast applications, converters, inverters, switching regulators, motor control systems, etc.

## QUICK REFERENCE DATA

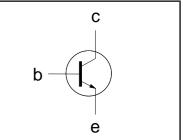
SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
V <sub>CESM</sub>	Collector-emitter voltage peak value	$V_{BE} = 0 V$	-	700	V
V <sub>CBO</sub>	Collector-Base voltage (open emitter)		-	700	V
V <sub>CEO</sub>	Collector-emitter voltage (open base)		-	400	V
I <sub>C</sub>	Collector current (DC)		-	8	А
	Collector current peak value		-	16	Α
I <sub>CM</sub> P <sub>tot</sub>	Total power dissipation	$T_{mb} \leq 25 \degree C$	-	125	W
V <sub>CEsat</sub>	Collector-emitter saturation voltage	$T_{mb} \le 25 \degree C$ $I_{C} = 4.0 \text{ A}; I_{B} = 0.8 \text{ A}$	0.3	1.0	V
h <sub>FEsat</sub>		$I_{c} = 4.0 \text{ A}$ : $V_{c_{c}} = 5 \text{ V}$	11	15	
t <sub>f</sub>	Fall time	$I_{\rm C} = 5 \text{ A}; I_{\rm B1} = 1 \text{ A}$	20	50	ns

**PIN CONFIGURATION** 

#### PINNING - SOT404

PIN	DESCRIPTION		
1	base		
2	collector		
3	emitter		
mb	collector		
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## LIMITING VALUES8

Limiting values in accordance with the Absolute Maximum Rating System (IEC 134)

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V <sub>CESM</sub>	Collector to emitter voltage	$V_{BE} = 0 V$	-	700	V
V <sub>CEO</sub>	Collector to emitter voltage (open base)		-	400	V
V <sub>CBO</sub>	Collector to base voltage (open emitter)		-	700	V
	Collector current (DC)		-	8	Α
I I <sub>CM</sub>	Collector current peak value		-	16	А
I I <sub>B</sub>	Base current (DC)		-	4	А
1 1	Base current peak value		-	8	Α
I <sub>BM</sub> P <sub>tot</sub>	Total power dissipation	T <sub>mb</sub> ≤ 25 °C	-	125	W
	Storage temperature		-65	150	°C
T	Junction temperature		-	150	°C

### THERMAL RESISTANCES

SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
R <sub>th j-mb</sub>	Thermal resistance junction to mounting base		-	1.0	K/W
R <sub>th j-a</sub>	Thermal resistance junction to ambient	minimum footprint, FR4 board	55	-	K/W

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# STATIC CHARACTERISTICS

 $T_{mb}$  = 25 °C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I <sub>CES</sub> ,I <sub>CBO</sub> I <sub>CES</sub>	Collector cut-off current <sup>1</sup>		-	-	0.2 0.5	mA mA
I <sub>CEO</sub> I <sub>EBO</sub> V <sub>CEOsust</sub>	Collector cut-off current Emitter cut-off current Collector-emitter sustaining voltage	$V_{CEO} = V_{CEOMmax} (400V) V_{EB} = 9 V; I_{C} = 0 A I_{B} = 0 A; I_{C} = 10 mA; L = 25 mH$	- - 400	- - -	0.1 1 -	mA mA V
$\begin{array}{l} V_{\text{CEsat}} \\ V_{\text{BEsat}} \\ h_{\text{FE}} \\ h_{\text{FE}} \\ h_{\text{FEsat}} \end{array}$	Collector-emitter saturation voltage Base-emitter saturation voltage DC current gain	$I_{c} = 4.0 \text{ A;} I_{B} = 0.8 \text{ A}$ $I_{c} = 4.0 \text{ A;} I_{B} = 0.8 \text{ A}$ $I_{c} = 1 \text{ mA;} \text{ V}_{CE} = 5 \text{ V}$ $I_{c} = 500 \text{ mA;} \text{ V}_{CE} = 5 \text{ V}$ $I_{c} = 4.0 \text{ A;} \text{ V}_{CE} = 5 \text{ V}$	- - 10 13 8	0.3 1.0 14 23 11	1.0 1.5 34 36 15	V V

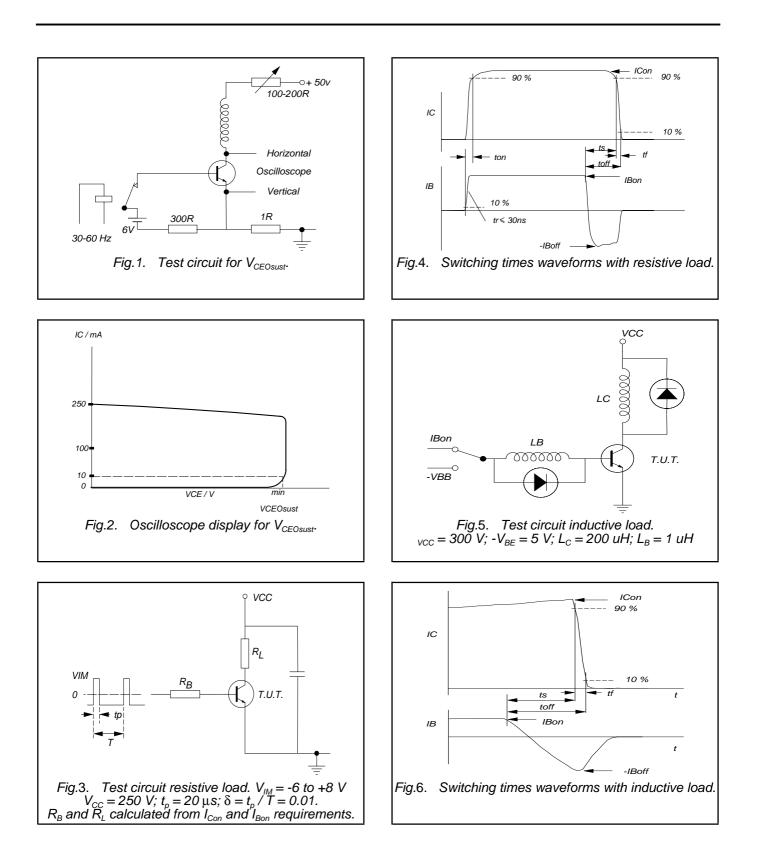
## **DYNAMIC CHARACTERISTICS**

 $T_{mb}$  = 25 °C unless otherwise specified

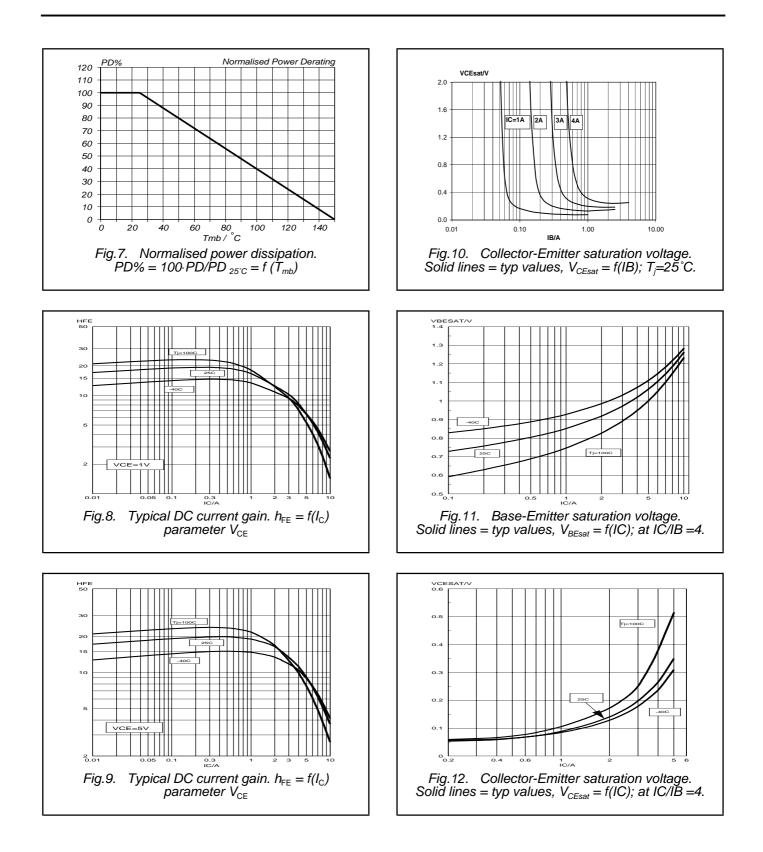
SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
	Switching times (resistive load)	$I_{Con} = 5 \text{ A}; I_{Bon} = -I_{Boff} = 1 \text{ A};$ R <sub>L</sub> = 75 ohms; V <sub>BB2</sub> = 4 V;			
t <sub>on</sub>	Turn-on time		0.65	1	μs
t <sub>s</sub>	Turn-off storage time		1.8	2.5	μs
t <sub>f</sub>	Turn-off fall time		0.3	0.5	μs
	Switching times (inductive load)	$I_{Con} = 5 \text{ A}; I_{Bon} = 1 \text{ A}; L_{B} = 1 \mu\text{H};$ -V <sub>BB</sub> = 5 V			
t <sub>s</sub>	Turn-off storage time		1.2	1.7	μs
t <sub>r</sub>	Turn-off fall time		20	50	ns
	Switching times (inductive load)	$I_{Con} = 5 \text{ A}; I_{Bon} = 1 \text{ A}; L_B = 1 \mu\text{H}; -V_{BB} = 5 \text{ V}; T_i = 100 \ ^{\circ}\text{C}$			
t <sub>s</sub>	Turn-off storage time		1.4	1.9	μs
t <sub>f</sub>	Turn-off fall time		25	100	ns

<sup>1</sup> Measured with half sine-wave voltage (curve tracer).

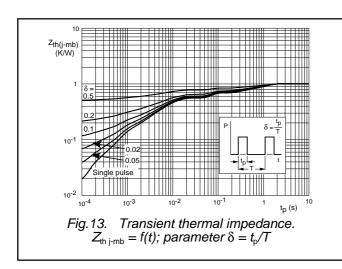
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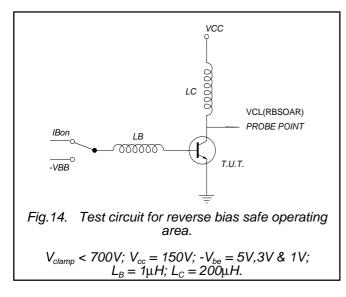


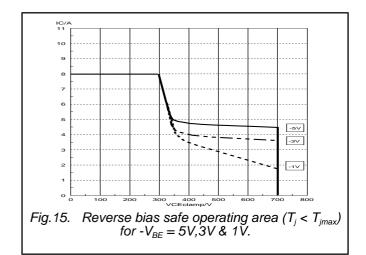
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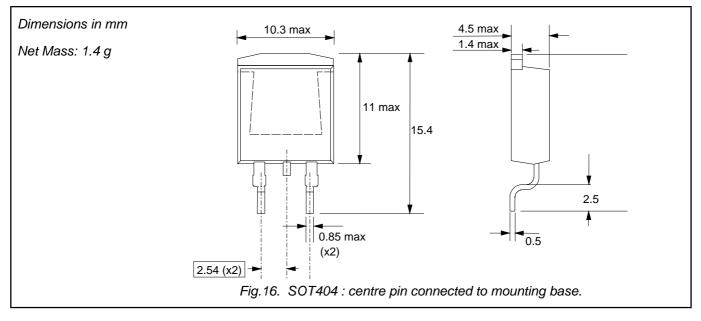




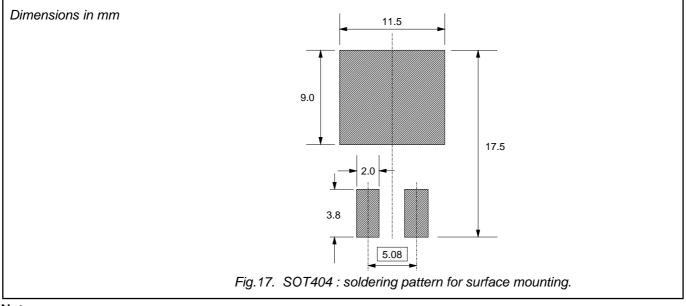


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### **MECHANICAL DATA**



### **MOUNTING INSTRUCTIONS**



#### Notes

1. Plastic meets UL94 V0 at 1/8".

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#### DEFINITIONS

DATA SHEET STATUS				
DATA SHEET STATUS <sup>2</sup>	PRODUCT STATUS <sup>3</sup>	DEFINITIONS		
Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice		
Preliminary data	Qualification	This data sheet contains data from the preliminary specification. Supplementary data will be published at a later date. Philips Semiconductors reserves the right to change the specification without notice, in ordere to improve the design and supply the best possible product		
Product data	Production	This data sheet contains data from the product specification. Philips Semiconductors reserves the right to make changes at any time in order to improve the design, manufacturing and supply. Changes will be communicated according to the Customer Product/Process Change Notification (CPCN) procedure SNW-SQ-650A		

#### Limiting values

Limiting values are given in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of this specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

#### Application information

Where application information is given, it is advisory and does not form part of the specification.

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