

# **CMOS LDO Regulator Series for Automotive**

# Ultra-Small Package FULL CMOS LDO Regulator

# **BUxxJA2MNVX-C series**

#### **General Description**

BUxxJA2MNVX-C series are high-performance FULL CMOS regulators with 200mA output, which are mounted on versatile package SSON004R1010 (1.00mm x 1.00 mm x 0.60mm). These device have excellent noise characteristics and load responsiveness characteristics despite its low circuit current consumption of 35µA. They are most appropriate for various applications such as power supplies for radar and camera of the automotive.

#### **Features**

- AEC-Q100 Qualified<sup>(NoteE1)</sup>
- High Accuracy Output
- Low Current Consumption
- Compatible With Small Ceramic Capacitor(C<sub>IN</sub>=C<sub>O</sub>=0.47µF)
- With Built-in Output Discharge Circuit
- High Ripple Rejection
- ON/OFF Control of Output Voltage
- Built-in Over Current Protection Circuit
- Built-in Thermal Shutdown Circuit (Note1) Grade1

#### **Key Specifications**

Input Voltage Range: 1.7V to 6.0V
 Output Voltage: 1.0V to 3.4V
 Output Voltage Accuracy: ±2.0%(Ta=-40°C to 125°C)
 Output Current: 200mA(Max)
 Standby Current: 35µA (Typ)
 Operating Temperature Range: -40°C to +125°C

 Package
 W(Typ) x D(Typ) x H(Max)

 SSON004R1010 :
 1.00mm x 1.00mm x 0.60mm



# **Applications**

Radar and camera for automotive, etc.

#### **Typical Application Circuit**

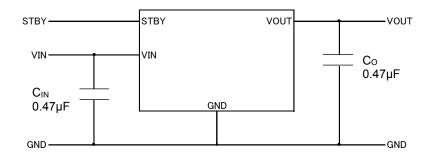
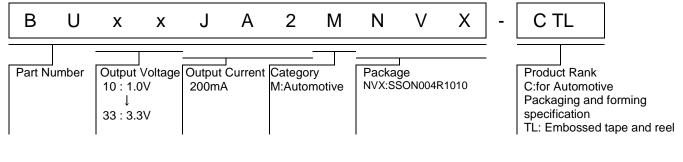


Figure 1. Application Circuit

# **Ordering Information**



Output Voltage	1.0V	1.2V	1.25V	1.5V	1.8V	2.5V	2.8V	2.85V	3.0V	3.3V
Part Number	BU10	BU12	BU1C	BU15	BU18	BU25	BU28	BU2J	BU30	BU33

# **Block Diagram**

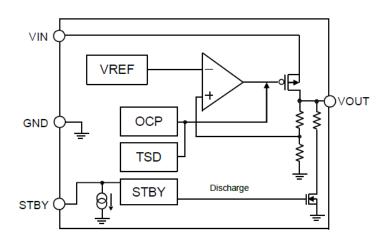
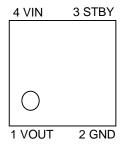


Figure 2. Block Diagram

# **Pin Configurations**



SSON004R1010 (TOP VIEW)

# **Pin Descriptions**

Pin No.	Pin name	Pin Function
1	VOUT	Output Voltage
2	GND	Ground
3	STBY	ON/OFF control of output voltage (High: ON, Low: OFF)
4	VIN	Power Supply Voltage
Back side	FIN	Connect to GND

**Absolute Maximum Ratings** (Ta=25°C)

Para	meter	Symbol	Rating	Unit
Power Supply Voltag	je	V <sub>IN</sub>	-0.3 to +6.5	V
STBY Voltage		V <sub>STBY</sub>	-0.3 to +6.5	V
Power Dissipation	SSON004R1010	Pd	0.69 (Note1)	W
Operating Temperatu	ure Range	Topr	-40 to +125	°C
Storage Temperature	e Range	Tstg	-55 to +150	°C
Maximum junction te	mperature	Tjmax	+150	°C

(Note1) Mounted on 70mm x 70mm x 1.6mm glass epoxy board. Reduce 5.52mW per 1°C above 25°C

**Recommended Operating Range** 

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Parameter	Symbol	Min	Max	Unit
Power Supply Voltage	V <sub>IN</sub>	1.7	6.0	V
STBY Voltage	Vstby	0.0	6.0	V
Maximum Output Current	Іоит	-	200	mA

**Recommended Operating Conditions** 

Parameter	Symbol	Min	Тур	Max	Unit	Conditions
Input Capacitor	Cin	0.22 (Note2)	0.47	-	μF	Ceramic capacitor recommended
Output Capacitor	Со	0.22 (Note2)	0.47	-	μF	Ceramic capacitor recommended

(Note2) Caution that the capacitance to be kept higher than this specified values under all conditions considering temperature, DC bias, etc.

# **Electrical Characteristics**

(VIN=VOUT+1.0V  $^{(Note3)}$  , STBY=VIN ,  $C_{IN}$  =0.47  $\mu F$  ,  $C_{O}$  =0.47  $\mu F$  , unless otherwise specified.)

Parameter		Comple of		Limit		l lait	Conditions
		Symbol	Min	Тур	Max	Unit	Conditions
[Regulator Block]							
			VOUT		VOUT		IOLIT-0.04 A. VOLIT-4.0V
Output Valtage 1		VOUT1	×0.98		×1.02	V	IOUT=0.01mA, VOUT≥1.8V
Output Voltage 1		VO011	VOUT		VOUT	V	IOUT 0.04mA VOLIT 44.0V
			-36mV	-	+36mV		IOUT=0.01mA, VOUT<1.8V
			VOUT		VOUT		IOUT=0.01mA to 200mA
Output Valtage 2		VOUT2	×0.97	-	×1.03	V	VOUT≥1.8V
Output Voltage 2		V0012	VOUT		VOUT	V	IOUT=0.01mA to 200mA
			-54mV	-	+54mV		VOUT<1.8V
Operating Current		IIN	-	35	90	μΑ	IOUT=0mA
Operating Current (STB)	Y)	ISTBY	-	-	2.0	μΑ	STBY=0V
Dinnla Daigation Datia		RR	45	70		٩D	VRR=-20dBv, fRR=1kHz
Ripple Rejection Ratio		KK	45	70	-	dB	IOUT=10mA, Ta=25°C
			ı	800	1100	mV	1.0V ≤ VOUT < 1.2V(IOUT=200mA)
			-	600	900	mV	1.2V ≤ VOUT < 1.5V(IOUT=200mA)
			-	440	830	mV	1.5V ≤ VOUT < 1.8V(IOUT=200mA)
Dropout Voltage		VOAT	-	380	710	mV	1.8V ≤ VOUT < 2.5(IOUT=200mA)
		VSAT	-	280	620	mV	2.5V ≤ VOUT ≤ 2.6(IOUT=200mA)
			-	260	580	mV	2.7V ≤ VOUT ≤ 2.85(IOUT=200mA)
			-	240	530	mV	2.9V ≤ VOUT ≤ 3.1V(IOUT=200mA)
			-	220	490	mV	3.2V ≤ VOUT ≤ 3.4V(IOUT=200mA)
1. 5 1.		\		0		.,	VIN=VOUT+1.0V to 5.5V <sup>(Note4)</sup>
Line Regulation		VDL	-	2	20	mV	IOUT=0.01mA
Load Regulation		VDLO	-	10	80	mV	IOUT=0.01mA to 100mA
[Over Current Protection	(OCP) B	lock]					
Limit Current		ILMAX	220	400	700	mA	ILMAX@VOUT×0.95, Ta=25°C
Short Current		ISHORT	20	70	150	mA	VOUT=0V, Ta=25°C
[Standby Block]							
		DDOO	00	50	00	_	VIN=4.0V, STBY=0V
Discharge Resistor		RDSC	20	50	80	Ω	VOUT=4.0V, Ta=25°C
STBY Pin Pull-down Cu	rrent	ISTB	0.1	0.6	2.0	μA	STBY=1.5V
OTDV O	ON	VSTBH	1.2	-	6.0	V	
STBY Control Voltage	OFF	VSTBL	0	-	0.3	V	
(Note3) VIN=2.5V for VOI		l .	1	l .	1		<u> </u>

(Note3) VIN=2.5V for VOUT≤1.5V (Note4) VIN=2.5V to 3.6V for VOUT≤1.5V

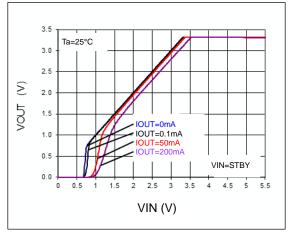


Figure 3. Output Voltage

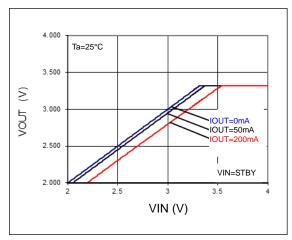


Figure 4. Output Voltage

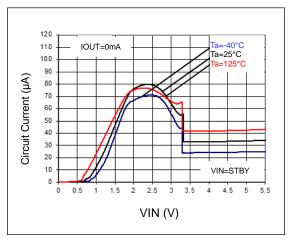


Figure 5. Circuit Current IIN

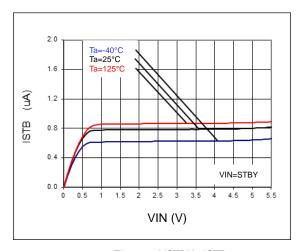


Figure 6. VSTBY - ISTB

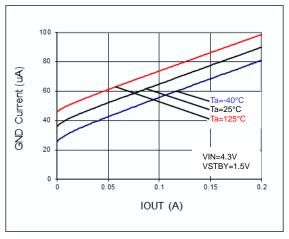


Figure 7. IOUT - IGND

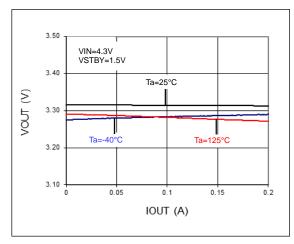


Figure 8. Load Regulation

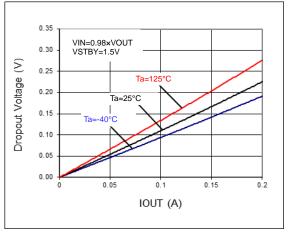


Figure 9. Dropout Voltage

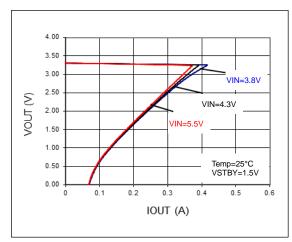


Figure 10. OCP Threshold

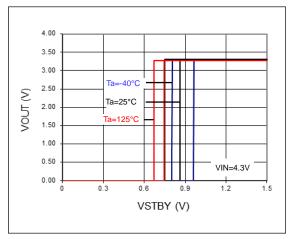


Figure 11. STBY Threshold

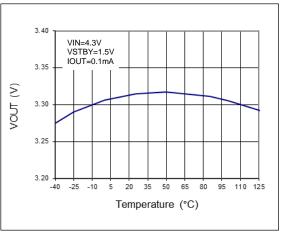


Figure 12. VOUT - Temp

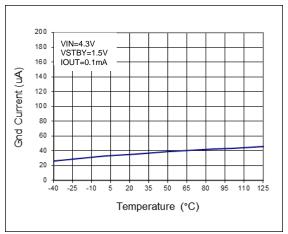


Figure 13. IGND - Temp

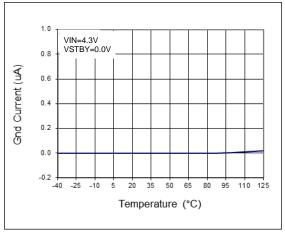


Figure 14. IGND - Temp (STBY)

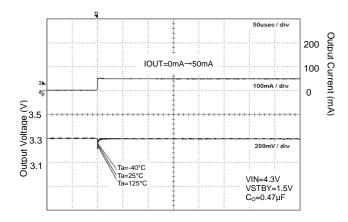


Figure 15. Load Response

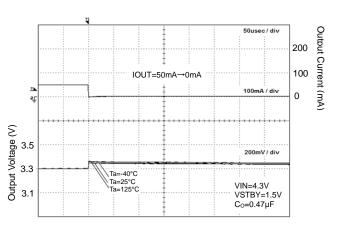


Figure 16. Load Response

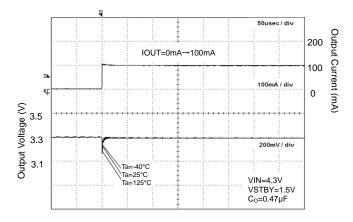


Figure 17. Load Response

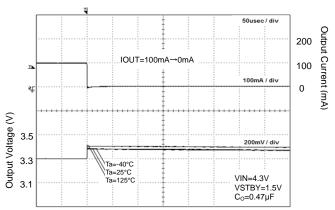


Figure 18. Load Response

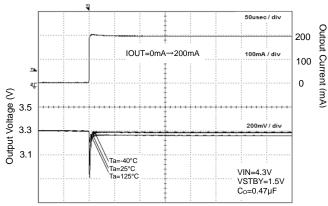


Figure 19. Load Response

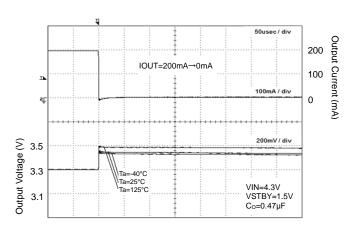


Figure 20. Load Response

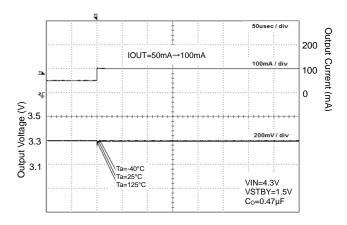


Figure 21. Load Response

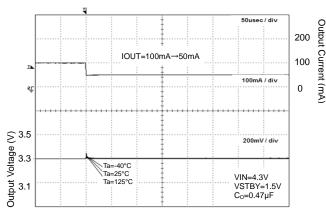


Figure 22. Load Response

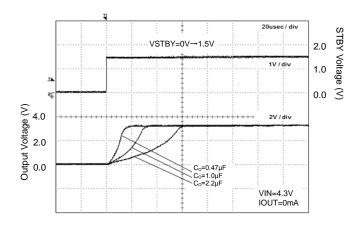


Figure 23. Start Up Time IOUT=0mA

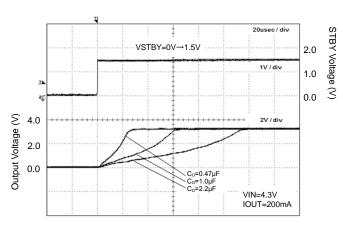


Figure 24. Start Up Time IOUT=200mA

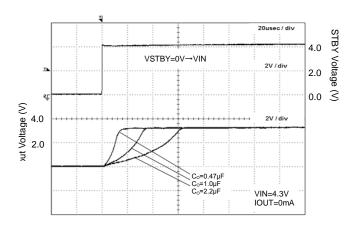


Figure 25. Start Up Time (VIN=STBY) IOUT=0mA

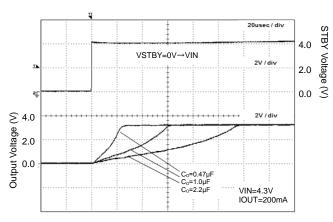
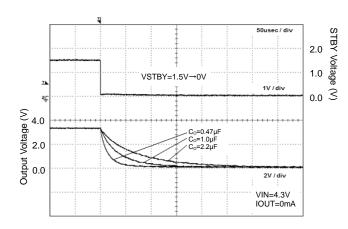


Figure 26. Start Up Time (VIN=STBY) IOUT=200mA



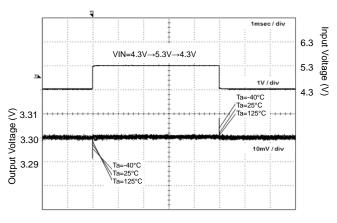


Figure 27. Discharge Time

Figure 28. VIN Response

#### About power dissipation (Pd)

As for power dissipation, an approximate estimate of the heat reduction characteristics and internal power consumption of IC are shown, so please use these for reference. Since power dissipation changes substantially depending on the implementation conditions (board size, board thickness, metal wiring rate, number of layers and through holes, etc.), it is recommended to measure Pd on a set board. Exceeding the power dissipation of IC may lead to deterioration of the original IC performance, such as causing operation of the thermal shutdown circuit or reduction in current capability. Therefore, be sure to prepare sufficient margin within power dissipation for usage.

Calculation of the maximum internal power consumption of IC (PMAX)

PMAX=(VIN-VOUT)xIOUT(MAX) (VIN: Input voltage VOUT: Output voltage IOUT(MAX): Maximum output current)

#### Measurement conditions

vieasuremei	nt conditions				
		Standard ROHM Board	Evaluation Board 1		
	of Board for surement	Top Lover (Top View)			
		Top Layer (Top View)	Top Layer (Top View)		
Impler	IC mentation osition				
		Bottom Layer (Bottom View)	Bottom Layer (Bottom View)		
	ement State	With board implemented (Wind speed 0 m/s)	With board implemented (Wind speed 0 m/s)		
	d Material	Glass epoxy resin (Double-side board)	Glass epoxy resin (Double-side board)		
Boa	ard Size	70 mm x 70 mm x 1.6 mm	40 mm x 40 mm x 1.6 mm		
Wiring	Top layer	Metal (GND) wiring rate: Approx. 0%	Metal (GND) wiring rate: Approx. 50%		
Rate	Bottom layer	Metal (GND) wiring rate: Approx. 50%	Metal (GND) wiring rate: Approx. 50%		
Throu	ugh Hole	Diameter 0.5mm x 6 holes	Diameter 0.5mm x 25 holes		
Power	Dissipation	0.69W	0.48W		
Thermal Resistance		θja=178.6°C/W	θja=256.4°C/W		

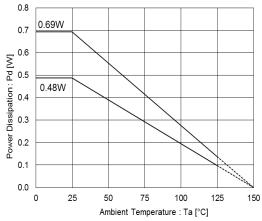


Figure 29. SSON004R1010 Power dissipation heat reduction characteristics (Reference)

\* Please design the margin that PMAX will be less than the Pd (PMAX < Pd) within the usage temperature range.

#### **Operation Notes**

#### 1. Absolute maximum ratings

Use of the IC exceeding the absolute maximum ratings (such as the input voltage or operating temperature range) may result in damage to the IC. Damage mode of the IC in such case can not be assumed (e.g. short mode or open mode). If operational values are expected to exceed the maximum ratings for the device, consider adding protective circuitry (such as fuses) to eliminate the risk of damaging the IC.

#### 2. GND potentia

The potential of the GND pin must be the minimum potential in the system in all operating conditions. Never connect a potential lower than GND to any pin, even if only transiently.

#### 3. Thermal design

Use a thermal design which ensure sufficient margin to the power dissipation rating (Pd) under actual operating conditions.

#### 4. Inter-pin shorts and mounting errors

Caution on the orientation and positioning of the IC for mounting on printed circuit boards. Improper mounting or shorts between pins may result in damage to the IC.

#### 5. Common impedance

Wiring traces should be as short and wide as possible to minimize common impedance. Bypass capacitors should be use え to keep ripple to a minimum.

#### 6. Voltage of STBY pin

To enable standby mode for all channels, set the STBY pin to 0.3 V or less, and for normal operation, to 1.2 V or more. Setting STBY to a voltage between 0.3 and 1.2 V may cause malfunction and should be avoided. Keep transition time between high and low (or vice versa) to a minimum.

Additionally, if STBY is shorted to VIN, the IC will switch to standby mode and disable the output discharge circuit, causing a temporary voltage to remain on the output pin. If the IC is switched on again while this voltage is present, overshoot may occur on the output. Therefore, in applications where these pins are shorted, the output should always be completely discharged before turning the IC on.

#### 7. Over-current protection circuit (OCP)

This IC features an integrated over-current and short-protection circuitry on the output to prevent destruction of the IC when the output is shorted. The OCP circuitry is designed only to protect the IC from irregular conditions (such as motor output shorts) and is not designed to be used as an active security device for the application. Therefore, applications should not be designed under the assumption that this circuitry will engage.

#### 8. Thermal shutdown circuit (TSD)

This IC also features a thermal shutdown circuit that is designed to turn the output off when the junction temperature of the IC exceeds approximately 150°C. This feature is intended to protect the IC only in the event of thermal overload and is not designed to guarantee operation or act as an active security device for the application. Therefore, applications should not be designed under the assumption that this circuitry will engage.

#### 9. Input/output capacitor

Capacitors must be connected between the input/output pins and GND for stable operation, and should be physically mounted as close to the IC pins as possible. The input capacitor helps to counteract increases in power supply impedance, and increases stability in applications with long or winding power supply traces. The output capacitance value is directly related to the overall stability and transient response of the regulator, and should be set to the largest possible value for the application to increase these characteristics. During design, keep in mind that in general, ceramic capacitors have a wide range of tolerances, temperature coefficients and DC bias characteristics, and that their capacitance values tend to decrease over time. Confirm these details before choosing appropriate capacitors for your application. (Refer to the technical note of the intended ceramic capacitors.)

## 10. About the equivalent series resistance (ESR) of a ceramic capacitor

Capacitors generally have ESR (equivalent series resistance) and it operates stably in the ESR-IOUT area shown on the below. Since ceramic capacitors, tantalum capacitors, electrolytic capacitors, etc. generally have different ESR, please check the ESR of the capacitor to be used and use it within the stability area range shown in the right graph for evaluation of the actual application.

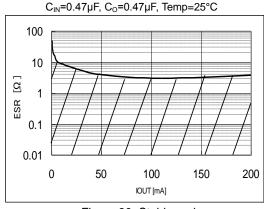


Figure 30. Stable region

#### **Input/Output Capacitor**

It is recommended that an input capacitor is placed near pins between the VIN pin and GND as well as an output capacitor between the VOUT pin and GND. The input is valid when the power supply impedance is high or when the PCB trace has significant length. For the output capacitor, the greater the capacitance, the more stable the output will be depending on the load and line voltage variations. However, please check the actual functionality of this capacitor by mounting it on a board for the actual application. Ceramic capacitors usually have different, thermal and equivalent series resistance characteristics, and may degrade gradually over continued use. For additional details, please check with the manufacturer, and select the best ceramic capacitor for your application.

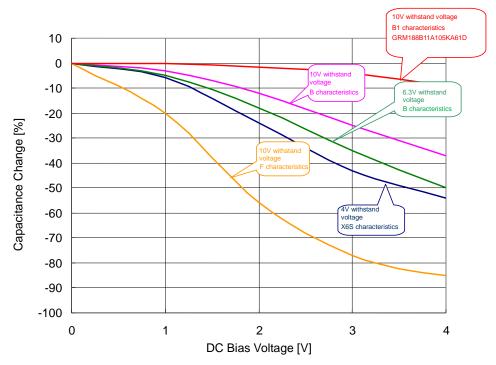


Figure 31. Capacity-bias characteristics

# I/O Equivalence Circuits

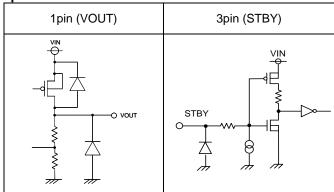
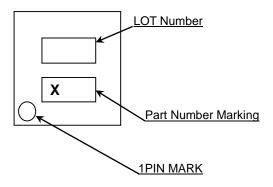


Figure 32. Input / Output equivalent circuit

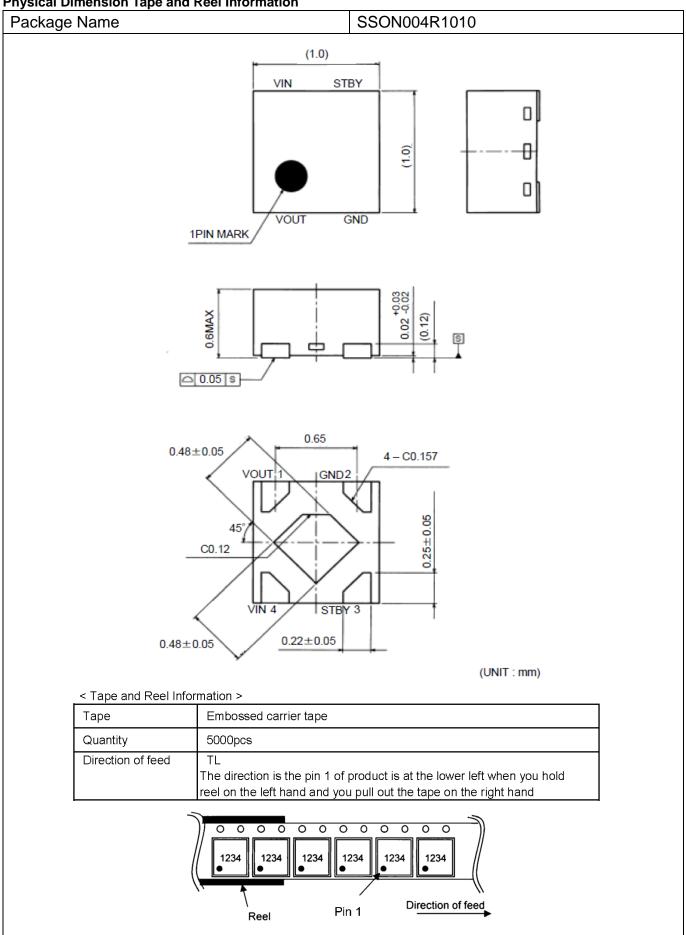
# **Marking Diagram**

SSON004R1010 (TOP VIEW)



	Marking	5	4	3	2	Q	1	U	0	Υ	R
Ī	Output Voltage	1.0V	1.2V	1.25V	1.5V	1.8V	2.5V	2.8V	2.85V	3.0V	3.3V
Ī	Part Number	BU10	BU12	BU1C	BU15	BU18	BU25	BU28	BU2J	BU30	BU33

**Physical Dimension Tape and Reel Information** 



# **Revision History**

Date	Revision	Changes
26.Dec.2014	001	New Release.
27.Aug.2015	002	P2 Add Line up
11.Apr.2016	003	Applied the ROHM Standard Style and improved understandability.  Add Equivalence Circuits

# **Notice**

#### **Precaution on using ROHM Products**

1. If you intend to use our Products in devices requiring extremely high reliability (such as medical equipment (Note 1), aircraft/spacecraft, nuclear power controllers, etc.) and whose malfunction or failure may cause loss of human life, bodily injury or serious damage to property ("Specific Applications"), please consult with the ROHM sales representative in advance. Unless otherwise agreed in writing by ROHM in advance, ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of any ROHM's Products for Specific Applications.

(Note1) Medical Equipment Classification of the Specific Applications

1. 1010 1) 1110 0110 011	igaipinoni Giacom	camen or and open	
JAPAN	USA	EU	CHINA
CLASSⅢ	CLASSⅢ	CLASS II b	CLASSⅢ
CLASSIV	CLASSIII	CLASSⅢ	CLASSIII

- 2. ROHM designs and manufactures its Products subject to strict quality control system. However, semiconductor products can fail or malfunction at a certain rate. Please be sure to implement, at your own responsibilities, adequate safety measures including but not limited to fail-safe design against the physical injury, damage to any property, which a failure or malfunction of our Products may cause. The following are examples of safety measures:
  - [a] Installation of protection circuits or other protective devices to improve system safety
  - [b] Installation of redundant circuits to reduce the impact of single or multiple circuit failure
- 3. Our Products are not designed under any special or extraordinary environments or conditions, as exemplified below. Accordingly, ROHM shall not be in any way responsible or liable for any damages, expenses or losses arising from the use of any ROHM's Products under any special or extraordinary environments or conditions. If you intend to use our Products under any special or extraordinary environments or conditions (as exemplified below), your independent verification and confirmation of product performance, reliability, etc, prior to use, must be necessary:
  - [a] Use of our Products in any types of liquid, including water, oils, chemicals, and organic solvents
  - [b] Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
  - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl<sub>2</sub>, H<sub>2</sub>S, NH<sub>3</sub>, SO<sub>2</sub>, and NO<sub>2</sub>
  - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
  - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
  - [f] Sealing or coating our Products with resin or other coating materials
  - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

### Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

#### **Precautions Regarding Application Examples and External Circuits**

- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
- You agree that application notes, reference designs, and associated data and information contained in this document are presented only as guidance for Products use. Therefore, in case you use such information, you are solely responsible for it and you must exercise your own independent verification and judgment in the use of such information contained in this document. ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of such information.

#### **Precaution for Electrostatic**

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

## **Precaution for Storage / Transportation**

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
  - [a] the Products are exposed to sea winds or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
  - [b] the temperature or humidity exceeds those recommended by ROHM
  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
  may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
  exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

#### **Precaution for Product Label**

A two-dimensional barcode printed on ROHM Products label is for ROHM's internal use only.

#### **Precaution for Disposition**

When disposing Products please dispose them properly using an authorized industry waste company.

#### **Precaution for Foreign Exchange and Foreign Trade act**

Since concerned goods might be fallen under listed items of export control prescribed by Foreign exchange and Foreign trade act, please consult with ROHM in case of export.

#### **Precaution Regarding Intellectual Property Rights**

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#### **General Precaution**

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# BU10JA2MNVX-C - Web Page

**Distribution Inventory** 

Part Number	BU10JA2MNVX-C
Package	SSON004R1010
Unit Quantity	5000
Minimum Package Quantity	5000
Packing Type	Taping
Constitution Materials List	inquiry
RoHS	Yes