

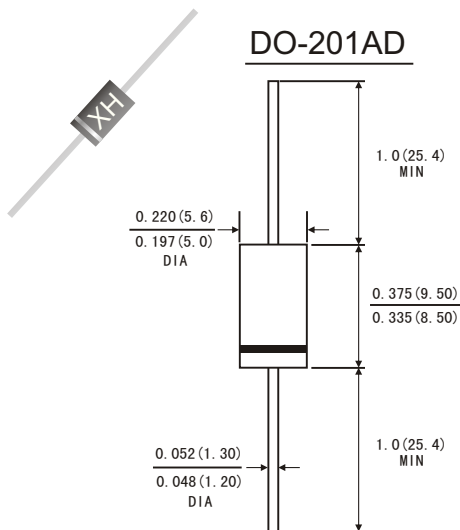


FEATURES

- Low forward voltage drop
- High current capability
- High reliability
- High surge current capability
- High temperature soldering guaranteed:260 °C/10 seconds at terminals
- Component in accordance to RoHS 2002/95/EC and WEEE 2002/96/EC

MECHANICAL DATA

- *Case:* JEDEC DO-201AD molded plastic body
- *Terminals:* Plated axial lead solderable per MIL-STD-750,method 2026
- *Polarity:* Color band denotes cathode end
- *Weight:* 1.2 grams



Dimensions in inches and (millimeters)

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

(Ratings at 25°C ambient temperature unless otherwise specified ,Single phase ,half wave 60Hz.,resistive or inductive load. For capacitive load, derate by 20%.)

	Symbols	BY 251	BY 252	BY 253	BY 254	BY 255	Units
Maximum Recurrent Peak Reverse Voltage	V_{RRM}	200	400	600	800	1300	Volts
Maximum RMS Voltage	V_{RMS}	140	280	420	560	910	Volts
Maximum DC Blocking Voltage	V_{DC}	200	400	600	800	1300	Volts
Maximum average Forward Rectified Current 0.5"(12.5mm)lead length at $T_A=75^{\circ}C$	$I_{(AV)}$	3.0					Amps
Peak Forward Surge Current (8.3ms half sine-wave superimposed on rated load (JEDEC method)	I_{FSM}	150.0					Amps
Maximum Instantaneous Forward Voltage at 3.0 A	V_F	1.0					Volts
Maximum Reverse current at rated DC Blocking Voltage	I_R	$T_A=25^{\circ}C$	5.0				μA
		$T_A=100^{\circ}C$	100.0				
Maximum Full Load Reverse Current, Full Cycle Average .375" (9.5mm) Lead Length @ $T_A=75^{\circ}C$		30					μA
Typical Junction Capacitance (Note 1)	C_J	40.0					pF
Typical Thermal Resistance (Note 2)	$R_{\theta JA}$	40.0					$^{\circ}C/W$
Operating and Storage temperature Range	T_J T_{STG}	-65 to+150					$^{\circ}C$

Note: 1.Measured at 1MHz and applied reverse voltage of 4.0V D. C

2. Mount onCu-Pad Size 16mm×16MM on P. C. B.



FIG.1-FORWARD CURRENT DERATING CURVE

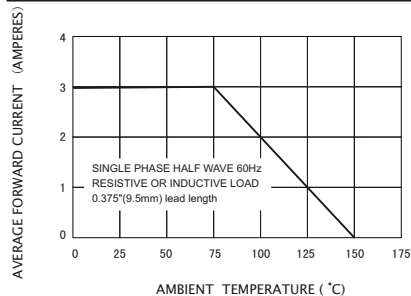


FIG.2-TYPICAL INSTANTANEOUS FORWARD VOLTAGE.(V)

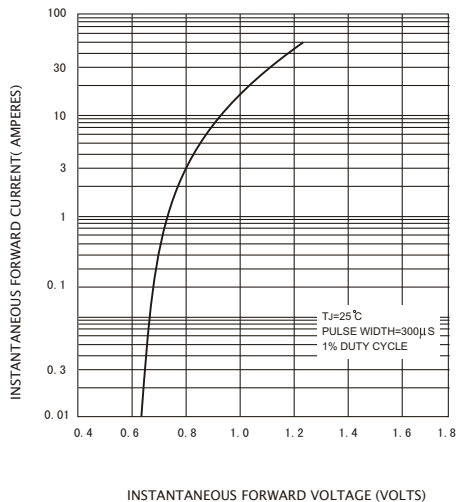


FIG.3-MAXIMUM NON-REPETITIVE PEAK FORWARD SURGE CURRENT

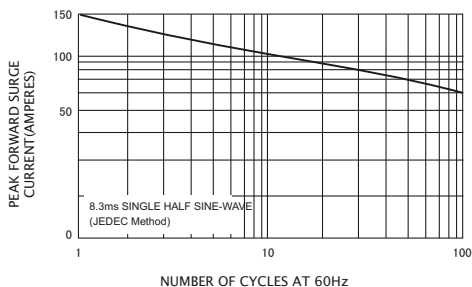


FIG.4-TYPICAL REVERSE CHARACTERISTICS

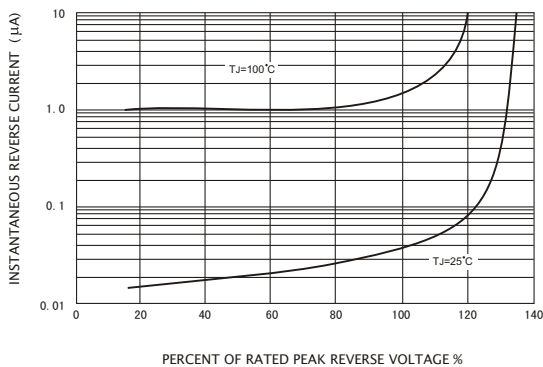


FIG.5-TYPICAL JUNCTION CAPACITANCE

