

CD54AC273/3A CD54ACT273/3A

COMPLETE DATA SHEET COMING SOON!

June 1997

Octal D Flip-Flop with Reset

Description

The CD54AC273/3A and CD54ACT273/3A are octal D flipflops with reset that utilize the Harris Advanced CMOS Logic technology. Information at the D input is transferred to the Q output on the positive-going edge of the clock pulse. All eight flip-flops are controlled by a common clock (CP) and a common reset ($\overline{\text{MR}}$). Resetting is accomplished by a low-voltage level independent of the clock.

The CD54AC273/3A and CD54ACT273/3A are supplied in 20 lead dual-in-line ceramic packages (F suffix).

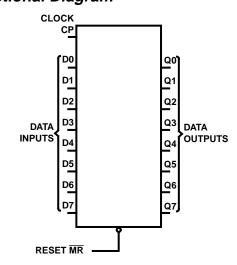
ACT INPUT LOAD TABLE

INPUT	UNIT LOAD (NOTE 1)
Dn	0.5
MR	0.57
СР	1

NOTE:

1. Unit load is ΔI_{CC} limit specified in DC Electrical Specifications Table, e.g., 2.4mA Max at +25°C.

Functional Diagram



Absolute Maximum Ratings

DC Supply Voltage, V _{CC} 0.5V to +6V
DC Input Diode Current, I _{IK}
For $V_1 < -0.5V$ or $V_1 > V_{CC} + 0.5V$ ±20mA
DC Output Diode Current, I _{OK}
For $V_O < -0.5V$ or $V_O > V_{CC} + 0.5V$
DC Output Source or Sink Current, Per Output Pin, IO
For $V_O > -0.5V$ or $V_O < V_{CC} + 0.5V$
DC V _{CC} or GND Current, I _{CC} or I _{GND}
For Up to 4 Outputs Per Device, Add ±25mA For Each
Additional Output±100mA

Power Dissipation Per Package, P _D
$T_A = -55^{\circ}C$ to $+100^{\circ}C$ (Package F)
T _A = +100°C to +125°C (Package F) Derate Linearly at
8mW/°C to 300mW
Operating Temperature Range, T _A
Package Type F55°C to +125°C
Storage Temperature, T _{STG} 65°C to +150°C
Lead Temperature (During Soldering)
At Distance 1/16in. ± 1/32in. (1.59mm ± 0.79mm)
From Case For 10s Max
Unit Inserted Into a PC Board (Min Thickness 1/16in., 1.59mm)
With Solder Contacting Lead Tips Only+300°C

CAUTION: Stresses above those listed in "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied.

Recommended Operating Conditions

Supply Voltage Range, V _{CC}
Unless Otherwise Specified, All Voltages Referenced to GND
T _A = Full Package Temperature Range
CD54AC Types
CD54ACT Types
DC Input or Output Voltage, V _I , V _O 0V to V _{CC}

Operating Temperature, T _A	55°C to +125°C
Input Rise and Fall Slew Rate, dt/dv	
at 1.5V to 3V (AC Types)	0ns/V to 50ns/V
at 3.6V to 5.5V (AC Types)	0ns/V to 20ns/V
at 4.5V to 5.5V (AC Types)	0ns/V to 10ns/V