

## **isc Silicon PNP Power Transistor**

# **CD568B**

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	<b>TION</b> m Lot-to-Lot variations for robust de able operation	PIN 1. BASE 2.COLLECTOR 3. BMITTER 1 2 3 TO-220C package		
ABSOLU	ကြေ TE MAXIMUM RATINGS(Ta=25℃)			
SYMBOL	PARAMETER	VALUE	UNIT	K V J <sup>1</sup> J <sup>0Q</sup> D V J
V <sub>CBO</sub>	Collector-Base Voltage	-200	V	
V <sub>CEO</sub>	Collector-Emitter Voltage	-150	V	
V <sub>EBO</sub>	Emitter-Base Voltage	-6	V	A 15.50 15.90   B 9.80 10.20   C 4.20 4.50
Ιc	Collector Current-Continuous	-1	А	D 0.70 0.90 F 3.40 3.70 G 4.98 5.18
Pc	Collector Power Dissipation @ T <sub>c</sub> =25°C	1.8	W	H 2.68 2.90 J 0.44 0.60 K 12.80 13.40
TJ	Junction Temperature	125	°C	L 1.20 1.45 0 2.70 2.90 R 2.30 2.70 S 1.29 1.35
T <sub>stg</sub>	Storage Temperature Range	-55~125	°C	3 1.29 1.35   U 6.45 6.65   V 8.66 8.86



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### **ELECTRICAL CHARACTERISTICS**

#### $T_{\text{C}}\text{=}25\,^{\circ}\!\!\!\!\!\!\mathrm{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	МАХ	UNIT
V <sub>(BR)CBO</sub>	Collector-Base Breakdown Voltage	Ic= -0.1mA; I <sub>E</sub> = 0	-200			V
V <sub>(BR)CEO</sub>	Collector-Emitter Breakdown Voltage	I <sub>C</sub> = -50mA; I <sub>B</sub> = 0	-150			V
V <sub>(BR)EBO</sub>	Emitter-Base Breakdown Voltage	I <sub>E</sub> = -5mA; I <sub>C</sub> = 0	-6			V
V <sub>CE(sat)</sub>	Collector-Emitter Saturation Voltage	I <sub>C</sub> = -0.5Α; I <sub>B</sub> = -0.05Α			-2.0	V
V <sub>BE(sat)</sub>	Base-Emitter On Voltage	I <sub>C</sub> = -0.5Α; I <sub>B</sub> = -0.05Α			-1.0	V
I <sub>CBO</sub>	Collector Cutoff Current	V <sub>CB</sub> = -120V; I <sub>E</sub> = 0			-5	μA
I <sub>CEO</sub>	Emitter Cutoff Current	V <sub>CE</sub> = -100V; I <sub>C</sub> = 0			-100	μA
h <sub>FE</sub>	DC Current Gain	I <sub>C</sub> = -4A; V <sub>CE</sub> = -0.5V	55		270	

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