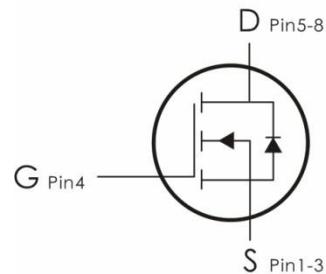
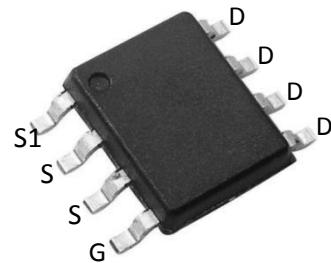


## Description:

This N-Channel MOSFET uses advanced trench technology and design to provide excellent  $R_{DS(on)}$  with low gate charge.

It can be used in a wide variety of applications.



## Features:

- 1)  $V_{DS}=30V, I_D=10A, R_{DS(ON)}<13m\Omega @ V_{GS}=10V$
- 2) Low gate charge.
- 3) Green device available.
- 4) Advanced high cell density trench technology for ultra  $R_{DS(ON)}$ .
- 5) Excellent package for good heat dissipation.

## Absolute Maximum Ratings: ( $T_c=25^\circ C$ unless otherwise noted)

Symbol	Parameter	Ratings	Units
$V_{DS}$	Drain-Source Voltage	30	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Drain Current – Continuous ( $T_c=25^\circ C$ )	10	A
	Drain Current – Continuous ( $T_c=100^\circ C$ )	6	
$I_{DM}$	Drain Current – Pulsed	50	
$P_D$	Power Dissipation ( $T_c=25^\circ C$ )	2.5	W
$T_J, T_{STG}$	Operating and Storage Junction Temperature Range	-55 to +150	°C

## Thermal Characteristics:

Symbol	Parameter	Max	Units
$R_{Theta}$	Thermal Resistance,Junction to Ambient <sup>2</sup>	50	°C/W

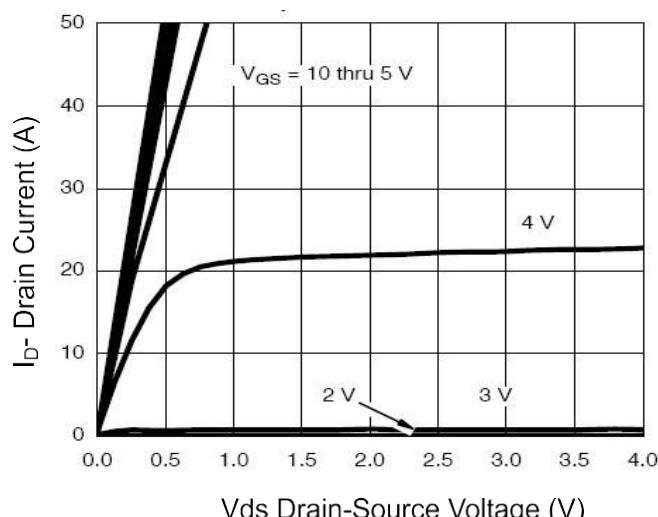
**Electrical Characteristics:** ( $T_C=25^\circ\text{C}$  unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
<b>Off Characteristics</b>						
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250 \mu\text{A}$	30	33	---	V
$I_{\text{DSS}}$	Drain-Source Leakage Current	$V_{\text{DS}}=30\text{V}, V_{\text{GS}}=0\text{V}, T_J=25^\circ\text{C}$	---	---	1	$\mu\text{A}$
$I_{\text{GSS}}$	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{V}$	---	---	$\pm 100$	nA
<b>On Characteristics<sup>3</sup></b>						
$V_{\text{GS(th)}}$	GATE-Source Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}, I_{\text{D}}=250 \mu\text{A}$	1	1.6	3	V
$R_{\text{DS(ON)}}$	Static Drain-Source On Resistance	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=10\text{A}$	---	10	13	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}, I_{\text{D}}=5\text{A}$	---	13	18	
$G_{\text{FS}}$	Forward Transconductance	$V_{\text{DS}}=5\text{V}, I_{\text{D}}=10\text{A}$	15	---	---	S
<b>Dynamic Characteristics<sup>4</sup></b>						
$C_{\text{iss}}$	Input Capacitance	$V_{\text{DS}}=15\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$	---	1550	---	$\text{pF}$
$C_{\text{oss}}$	Output Capacitance		---	300	---	
$C_{\text{rss}}$	Reverse Transfer Capacitance		---	180	---	
<b>Switching Characteristics<sup>4</sup></b>						
$t_{\text{d(on)}}$	Turn-On Delay Time <sup>2,3</sup>	$V_{\text{DS}}=25\text{V}, I_{\text{D}}=1\text{A}, R_{\text{GEN}}=6 \Omega, V_{\text{GS}}=10\text{V}$	---	30	---	ns
$t_r$	Rise Time <sup>2,3</sup>		---	200	---	ns
$t_{\text{d(off)}}$	Turn-Off Delay Time <sup>2,3</sup>		---	100	---	ns
$t_f$	Fall Time <sup>2,3</sup>		---	80	---	ns
$Q_g$	Total Gate Charge <sup>2,3</sup>		---	13	---	nC
$Q_{\text{gs}}$	Gate-Source Charge <sup>2,3</sup>	$V_{\text{GS}}=5\text{V}, V_{\text{DS}}=15\text{V}, I_{\text{D}}=10\text{A}$	---	5.5	---	nC
$Q_{\text{gd}}$	Gate-Drain "Miller" Charge <sup>2,3</sup>		---	3.5	---	nC
<b>Drain-Source Diode Characteristics</b>						
$V_{\text{SD}}$	Source-Drain Diode Forward Voltage <sup>3</sup>	$V_{\text{GS}}=0\text{V}, I_{\text{S}}=10\text{A}, T_J=25^\circ\text{C}$	---	---	1.2	V
$I_s$	Continuous Source Current <sup>2</sup>		---	---	10	A

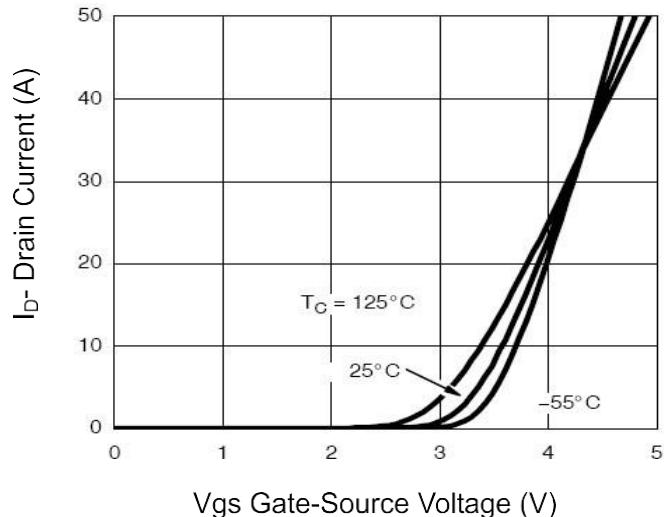
**Notes:**

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board,  $t \leq 10$  sec.
3. Pulse Test: Pulse Width  $\leq 300\mu\text{s}$ , Duty Cycle  $\leq 2\%$ .
4. Guaranteed by design, not subject to production

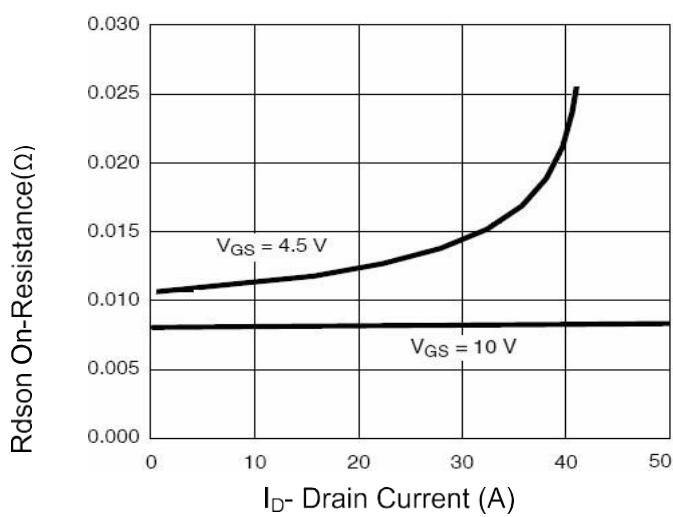
**Typical Characteristics:** ( $T_C=25^\circ\text{C}$  unless otherwise noted)



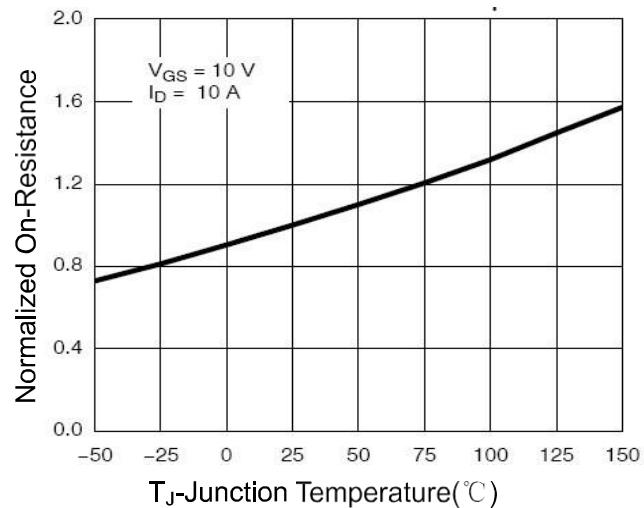
**Figure 1 Output Characteristics**



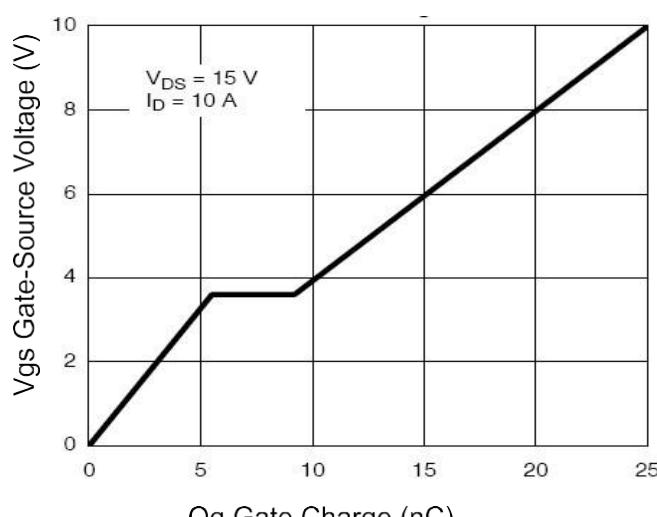
**Figure 2 Transfer Characteristics**



**Figure 3 Rdson-Drain Current**

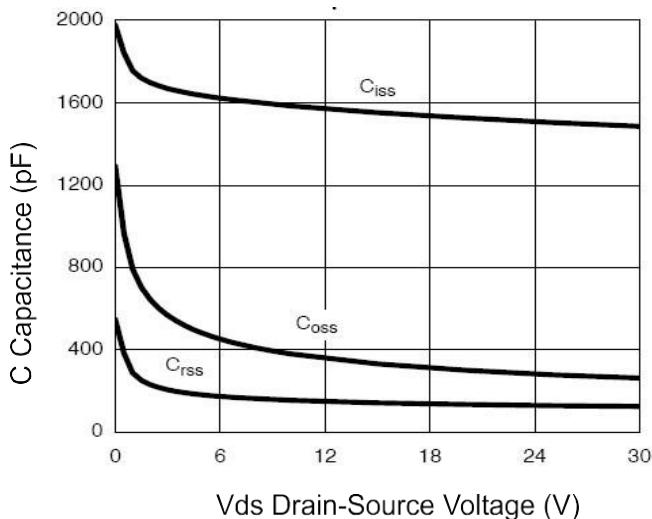


**Figure 4 Rdson-JunctionTemperature**

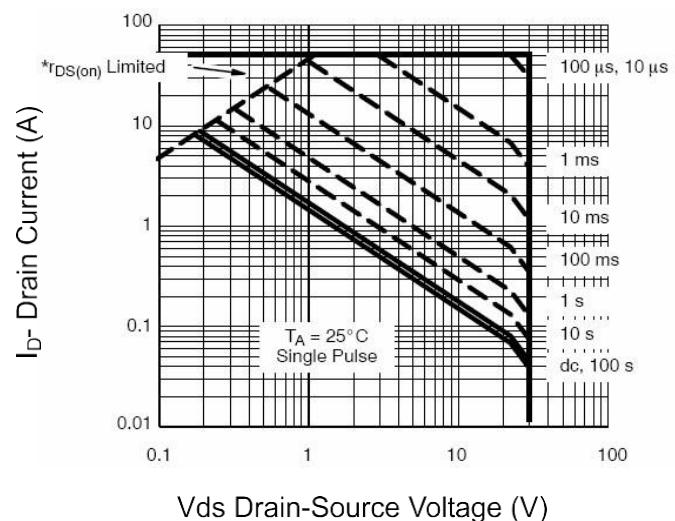


**Figure 5 Gate Charge**

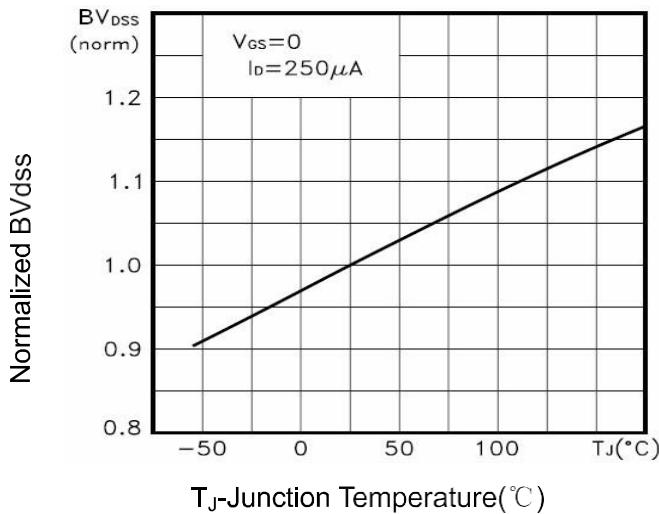
**Figure 6 Source- Drain Diode Forward**



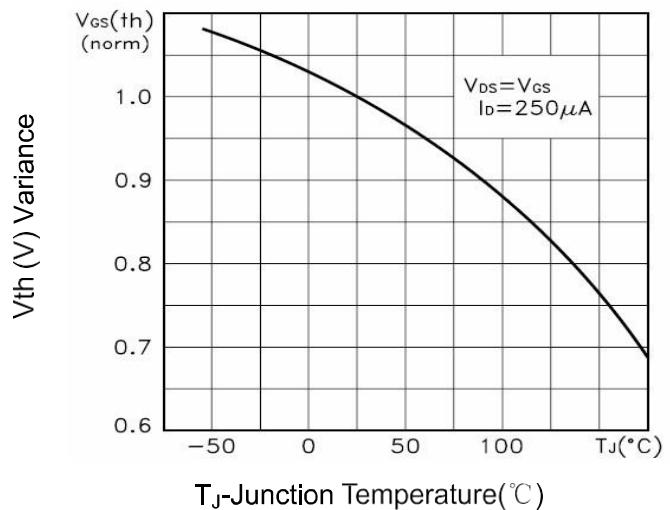
**Figure 6 Capacitance vs Vds**



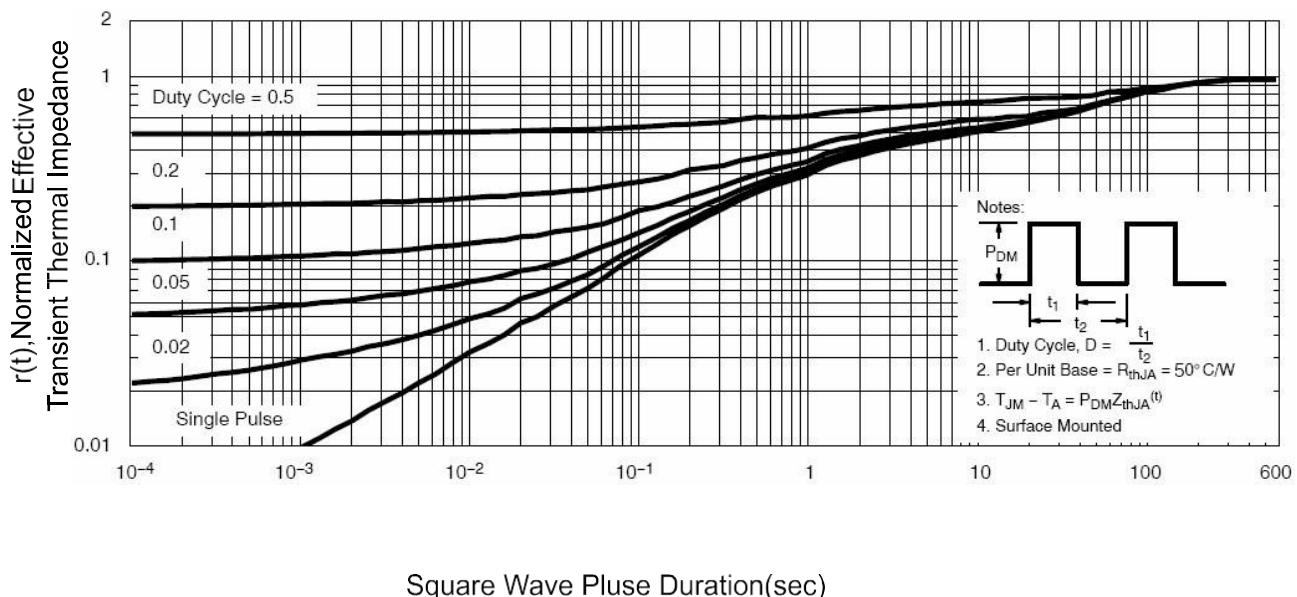
**Figure 7 Safe Operation Area**



**Figure 8  $BV_{DSS}$  vs Junction Temperature**



**Figure 9  $V_{GS(\text{th})}$  vs Junction Temperature**



**Figure 10 Normalized Maximum Transient Thermal Impedance**



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