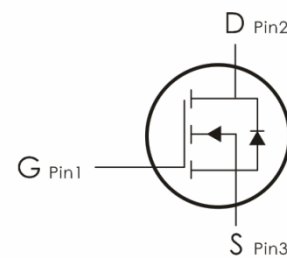


## Description:

This N-Channel MOSFET uses advanced trench technology and design to provide excellent  $R_{DS(on)}$  with low gate charge. It can be used in a wide variety of applications.

## Features:

- 1)  $V_{DS}=150V, I_D=100A, R_{DS(ON)} < 15m\ \Omega @ V_{GS}=10V$
- 2) Low gate charge.
- 3) Green device available.
- 4) Advanced high cell density trench technology for ultra  $R_{DS(ON)}$ .
- 5) Excellent package for good heat dissipation.



## Absolute Maximum Ratings: ( $T_C=25^\circ C$ unless otherwise noted)

Symbol	Parameter	Ratings	Units
$V_{DS}$	Drain-Source Voltage	150	V
$V_{GS}$	Gate-Source Voltage	$\pm 25$	V
$I_D$	Continuous Drain Current-	100	A
	Continuous Drain Current- $T_C=100^\circ C$	65	
	Pulsed Drain Current	300	
$E_{AS}$	Single Pulse Avalanche Energy	272	mJ
$P_D$	Power Dissipation	428	W
$T_J, T_{STG}$	Operating and Storage Junction Temperature Range	-55-+150	$^\circ C$

## Thermal Characteristics:

Symbol	Parameter	Max	Units
$R_{\theta JC}$	Thermal Resistance, Junction to Case <sup>2</sup>	0.29	$^\circ C/W$
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	65	

**Package Marking and Ordering Information:**

Part NO.	Marking	Package
PI015NG	I015N	TO-220

**Electrical Characteristics:** ( $T_c=25^\circ\text{C}$  unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
<b>Off Characteristics</b>						
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\ \mu\text{A}$	150	---	---	V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{GS}=0V, V_{DS}=150V, T_J=25^\circ\text{C}$	---	0.05	1	$\mu\text{A}$
		$V_{GS}=0V, V_{DS}=150V, T_J=150^\circ\text{C}$	---	---	20	$\mu\text{A}$
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0A$	---	10	100	nA
<b>On Characteristics</b>						
$V_{GS(th)}$	GATE-Source Threshold Voltage	$V_{GS}=V_{DS}, I_D=250\ \mu\text{A}$	3	4	5	V
$R_{DS(on)}$	Drain-Source On Resistance	$V_{GS}=10V, I_D=50A$	---	10	15	m $\Omega$
		$V_{GS}=4.5V, I_D=A$	---	22	27	
$G_{FS}$	Forward Transconductance	$V_{DS}=25V, I_D=30A$	---	101	---	S
<b>Dynamic Characteristics</b>						
$C_{iss}$	Input Capacitance	$V_{DS}=75V, V_{GS}=0V, f=1\text{MHz}$	---	3593	---	pF
$C_{oss}$	Output Capacitance		---	331	---	
$C_{rss}$	Reverse Transfer Capacitance		---	92	---	
<b>Switching Characteristics</b>						
$t_{d(on)}$	Turn-On Delay Time	$V_{DD}=30V, I_D=2A,$ $R_{GEN}=2.5\ \Omega, V_{GS}=10V$	---	18	---	ns
$t_r$	Rise Time		---	93	---	ns
$t_{d(off)}$	Turn-Off Delay Time		---	34	---	ns
$t_f$	Fall Time		---	70	---	ns
$R_g$	Gate resistance	$V_{GS}=0V, V_{DS}=0V,$ $f=1\text{MHz}$	---	1.7	---	$\Omega$
$Q_g$	Total Gate Charge	$V_{GS}=10V, V_{DS}=30V,$ $I_D=30A$	---	69	---	nC
$Q_{gs}$	Gate-Source Charge		---	24	---	nC
$Q_{gd}$	Gate-Drain "Miller" Charge		---	26	---	nC

Drain-Source Diode Characteristics						
<b>V<sub>SD</sub></b>	Source-Drain Diode Forward Voltage	V <sub>GS</sub> =0V, I <sub>S</sub> =50A	---	0.9	1.3	V
<b>T<sub>rr</sub></b>	Reverse Recovery Time	I <sub>F</sub> =50A, dI/dt=100A/μs	---	70	---	NS
<b>Q<sub>rr</sub></b>	Reverse Recovery Charge	S	---	233	---	NC

Typical Characteristics: (T<sub>c</sub>=25°C unless otherwise noted)

Fig 1: Output Characteristics

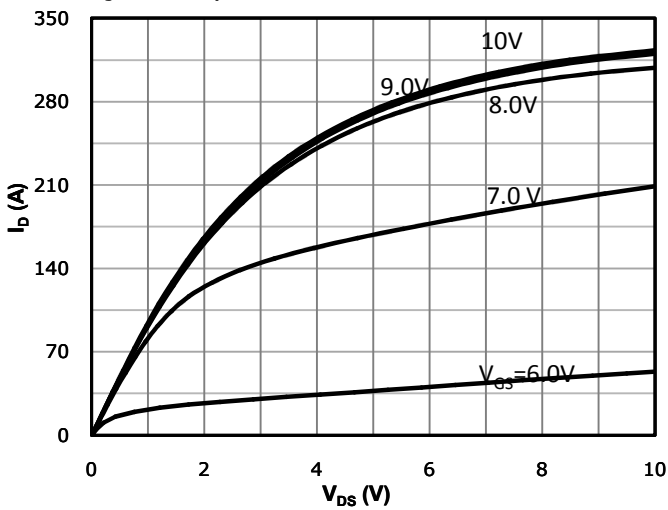


Fig 2: Transfer Characteristics

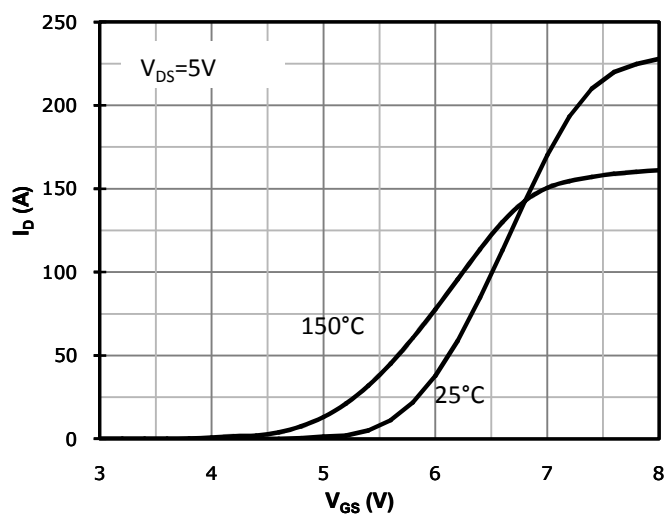


Fig 3: R<sub>DS(on)</sub> vs Drain Current and Gate Voltage

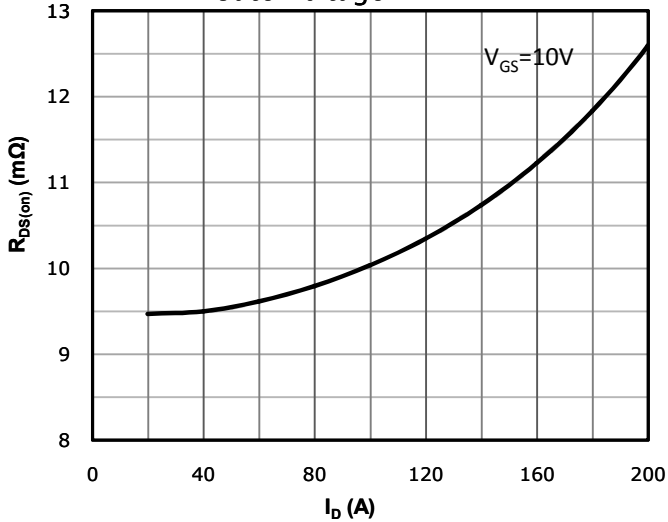


Fig 4: R<sub>DS(on)</sub> vs Gate Voltage

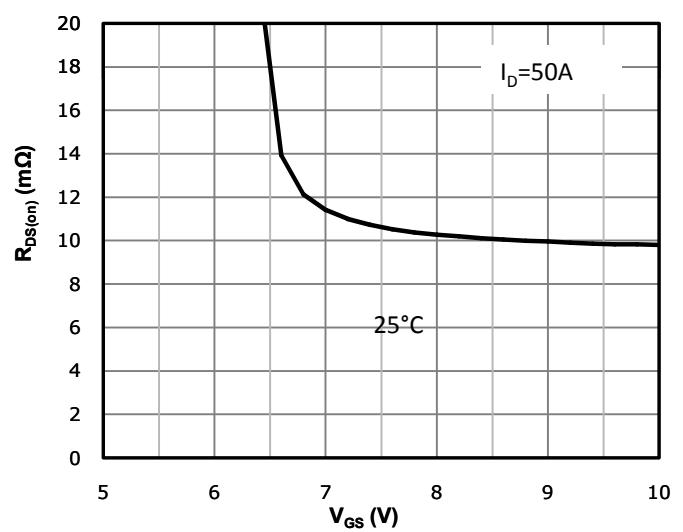


Fig 5: Rds(on) vs. Temperature

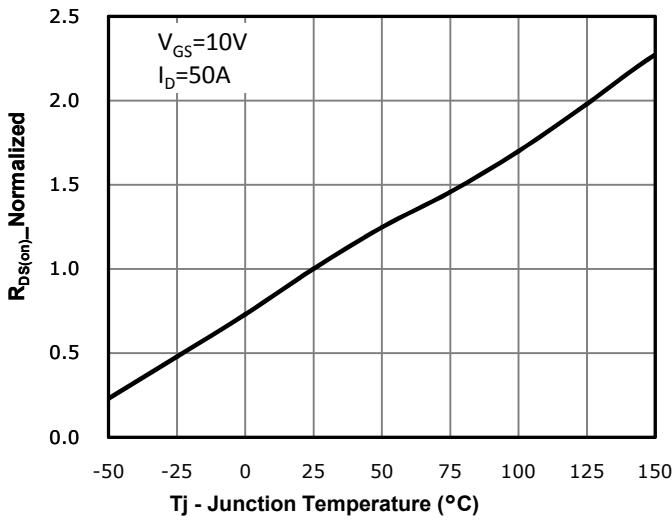


Fig 6: Capacitance Characteristics

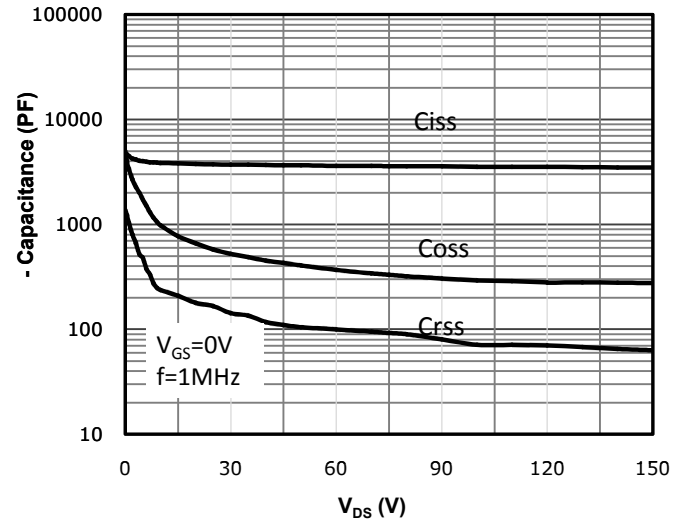


Fig 7: Gate Charge Characteristics

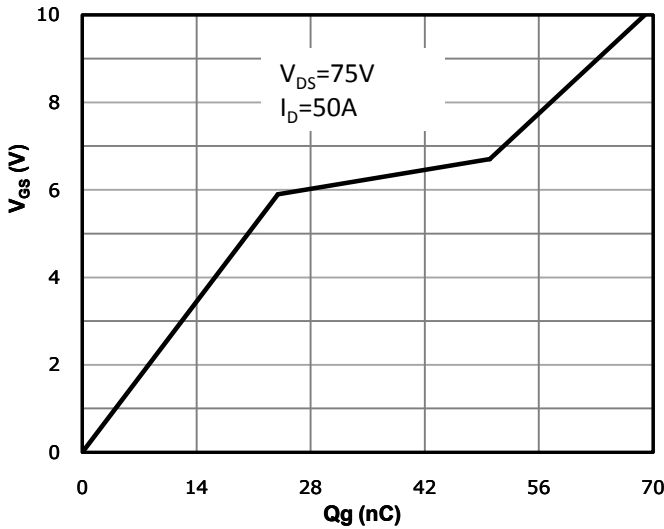


Fig 8: Body-diode Forward Characteristics

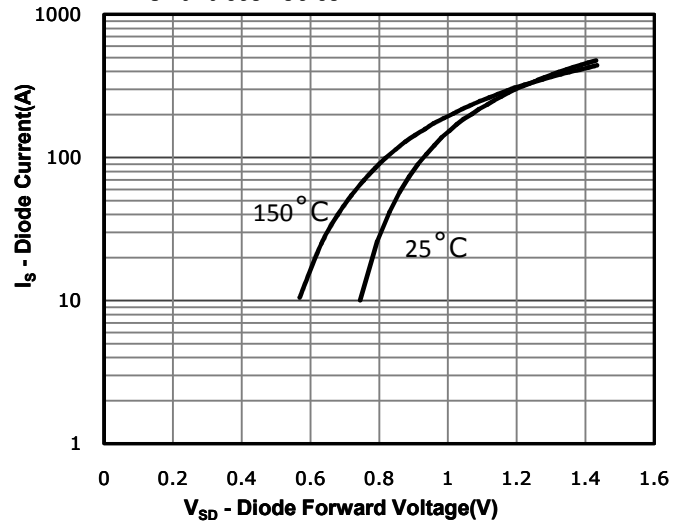


Fig 9: Power Dissipation

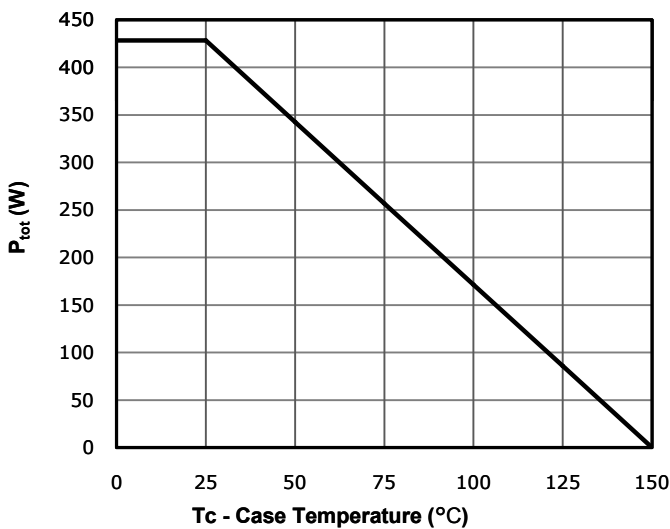


Fig 10: Drain Current Derating

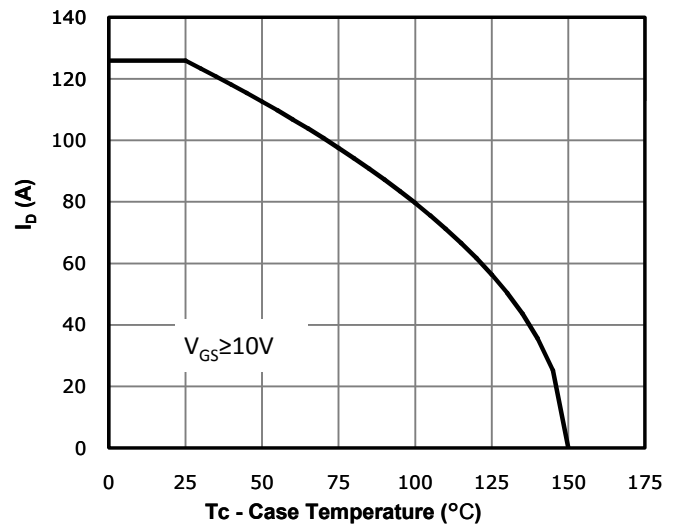


Fig 11: Safe Operating Area

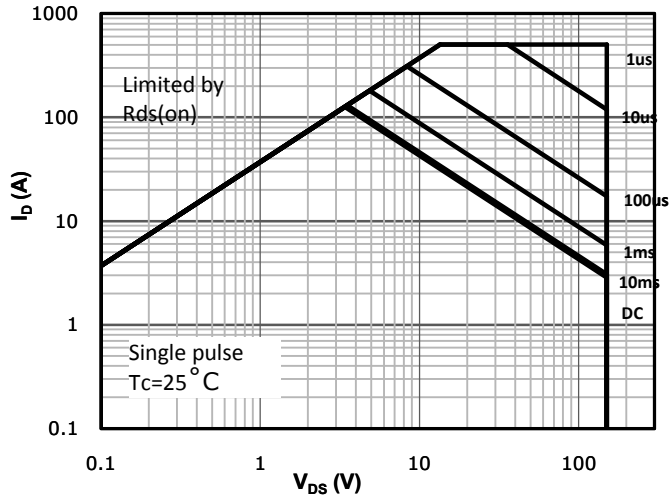
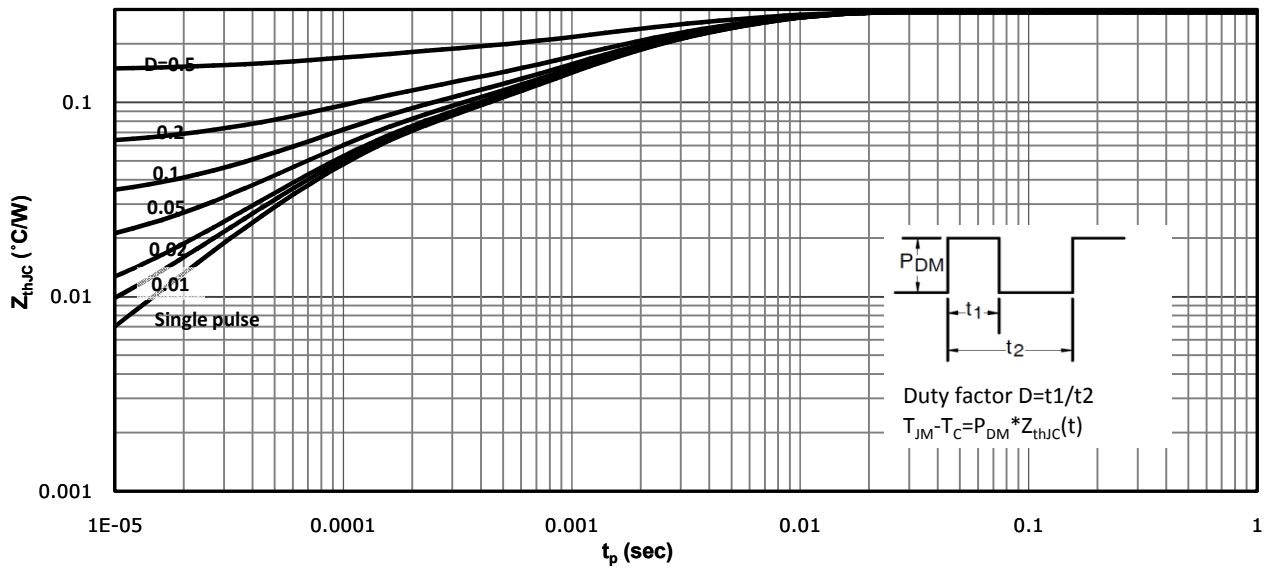
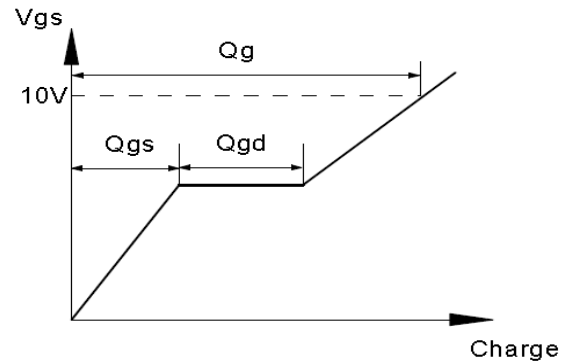
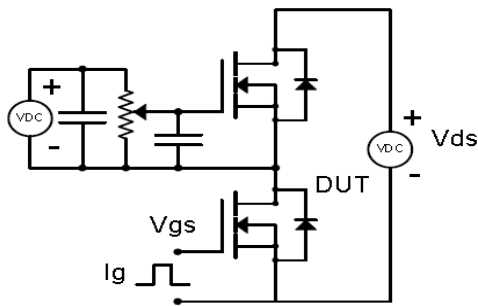
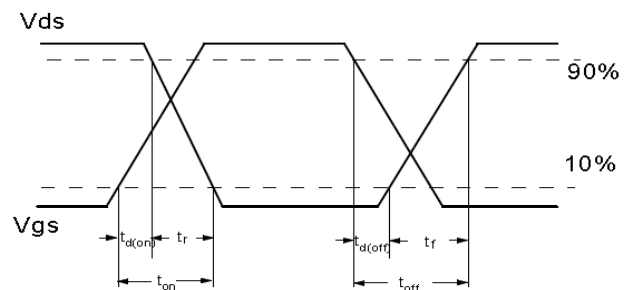
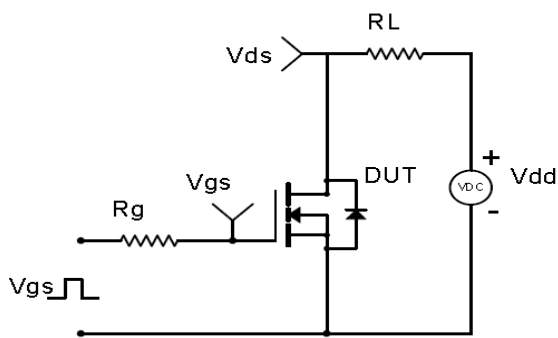
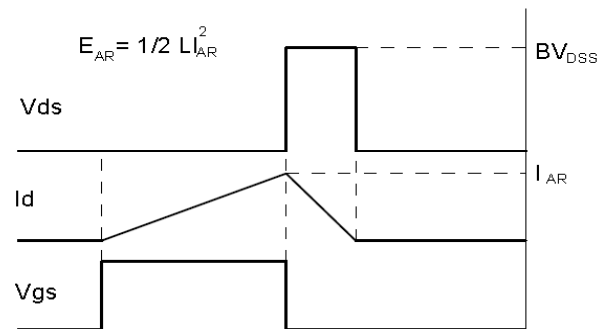
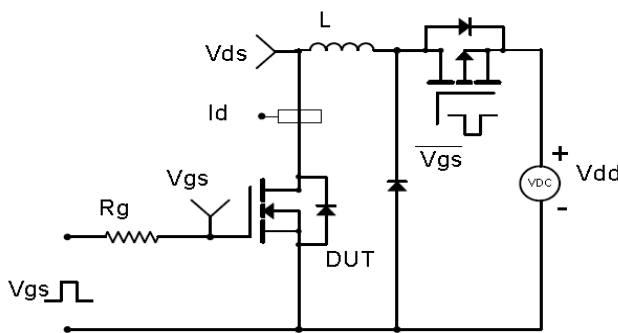
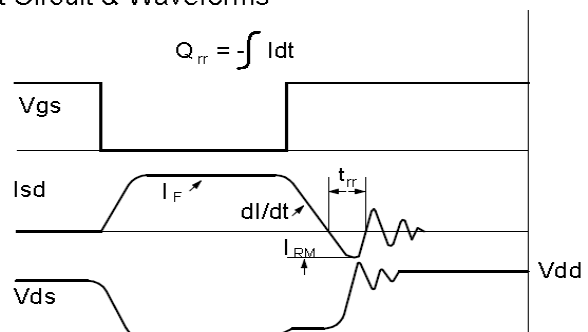
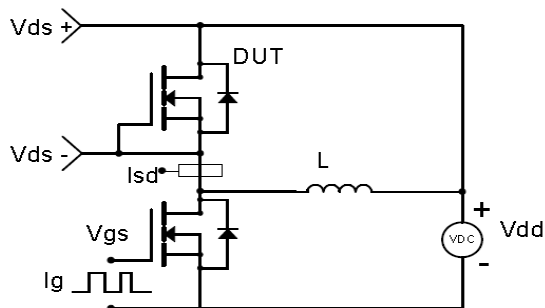
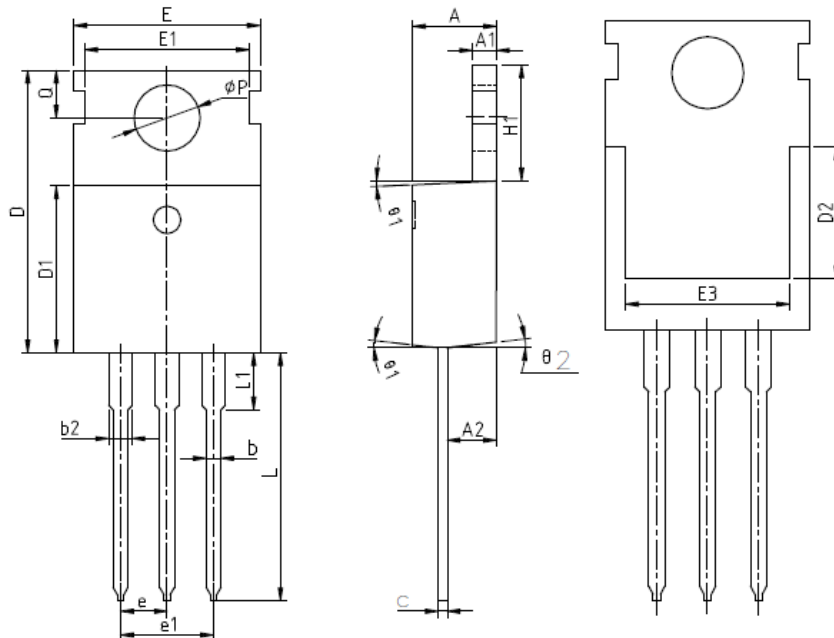


Fig 12: Max. Transient Thermal Impedance



**Test Circuit & Waveform**
**Gate Charge Test Circuit & Waveform**

**Resistive Switching Test Circuit & Waveforms**

**Unclamped Inductive Switching (UIS) Test Circuit & Waveforms**

**Diode Recovery Test Circuit & Waveforms**


**Package Outline: TO-220-3L**


SYMBOL	MIN	NOM	MAX
A	4.27	4.57	4.87
A1	1.15	1.30	1.45
A2	2.10	2.40	2.70
b	0.70	0.80	1.00
b2	1.17	1.27	1.50
c	0.40	0.50	0.65
D	15.10	15.60	16.10
D1	8.80	9.10	9.40
D2	5.70	6.70	7.00
E	9.70	10.00	10.30
E1	-	8.70	-
E2	9.65	10.00	10.35
E3	7.00	8.00	8.40
e	2.54 BSC		
e1	5.08 BSC		
H1	6.00	6.50	6.85
L	12.75	13.50	13.90
L1	-	3.10	3.40
$\phi P$	3.45	3.60	3.75
Q	2.60	2.80	3.00
$\theta 1$	4°	7°	10°
$\theta 2$	0°	3°	6°



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