



Dual Graphics Clock Generator

Features

- Generates 16 preset video clocks, 8 preset memory clocks, and buffers the reference frequency output
- Built-in power supply conditioning circuitry provides excellent jitter performance and eliminates the need for an external VDD dropping resistor
- Requires only two external components: one 14.318 MHz crystal and one 0.1 μ F decoupling capacitor
- Supports VGA, Super-VGA, XGA™, and 8514 graphic standards
- Supports output frequencies up to 135 MHz
- Drop-in replacement for ICS2494 and AV9194
- CMOS technology in 20-pin PDIP and SOIC
- 5V or 3.3V supply. For specific details on the 3.3V version, please consult Chrontel.

XGA is a trademark of IBM

Description

CH9294 is a dual PLL clock generator designed for high frequency graphics applications. It can also be used in applications requiring multiple clocks, such as PC motherboards, disk drives, CD-ROM systems, and modems.

The CH9294 provides separate memory clock (MCLK) and video clock (VCLK) outputs, and a 14.318 MHz reference clock output. Other input frequencies can be used to obtain different output frequencies. Internal loop filter elements minimize external part count.

The STROBE pin should be tied high or left open if not used. FSx pins are latched on the falling edge of STROBE.

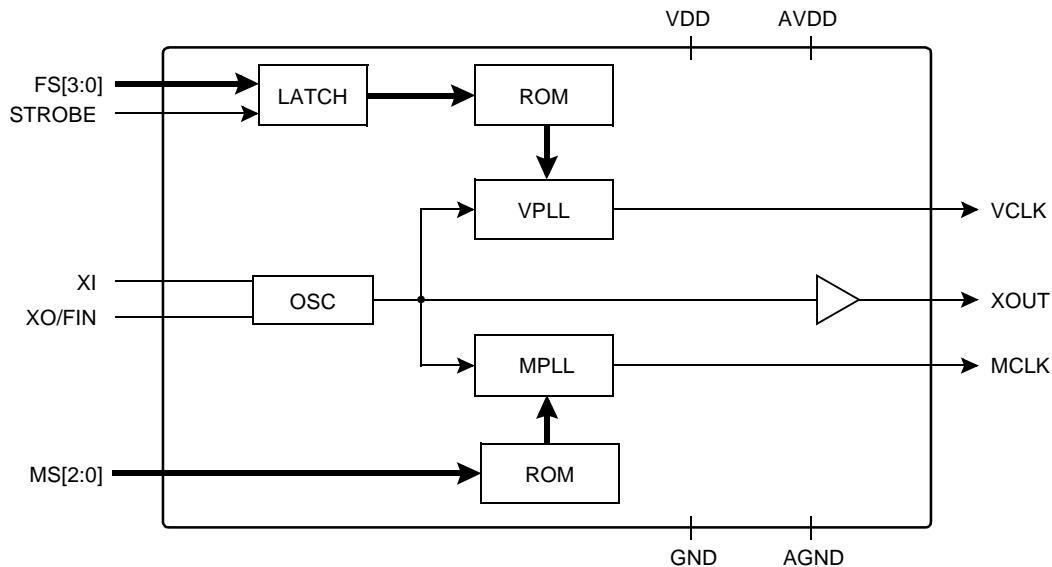


Figure 1: Block Diagram