



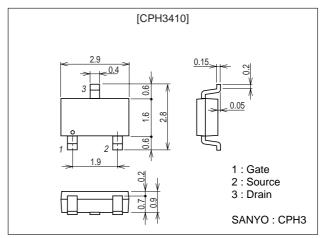
Ultrahigh-Speed Switching Applications

Features

- · Low ON-resistance.
- · Ultrahigh-speed switching.
- 2.5V drive.

Package Dimensions

unit : mm 2152A



Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		20	V
Gate-to-Source Voltage	VGSS		±10	V
Drain Current (DC)	ID		2.5	Α
Drain Current (Pulse)	I _{DP}	PW≤10μs, duty cycle≤1%	10	Α
Allowable Power Dissipation	PD	Mounted on a ceramic board (900mm ² X0.8mm)	1	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Drain-to-Source Breakdown Voltage	V(BR)DSS	I _D =1mA, V _{GS} =0	20			V
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =20V, V _{GS} =0			1	μΑ
Gate-to-Source Leakage Current	IGSS	VGS=±8V, VDS=0			±10	μΑ
Cutoff Voltage	VGS(off)	$V_{DS}=10V$, $I_{D}=1mA$	0.4		1.3	V
Forward Transfer Admittance	yfs	V _{DS} =10V, I _D =1.2A	2.8	4		S
Static Drain-to-Source On-State Resistance	RDS(on)1	ID=1.2A, VGS=4V		80	105	mΩ
	RDS(on)2	I _D =0.6A, V _G S=2.5V		110	155	mΩ

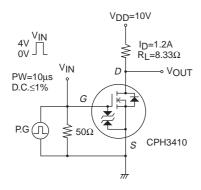
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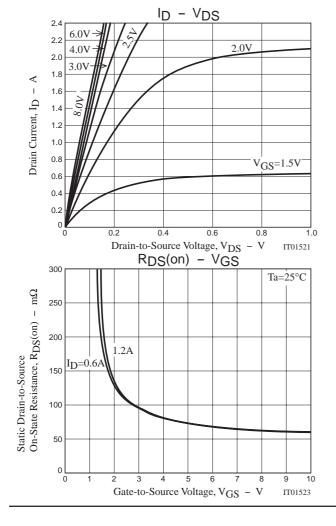
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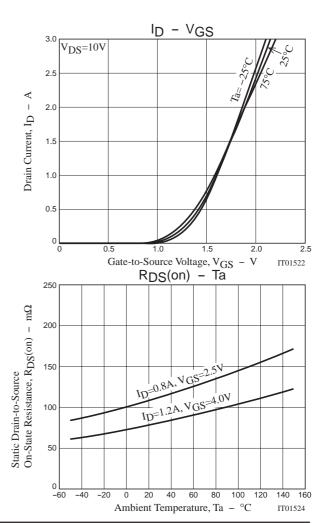
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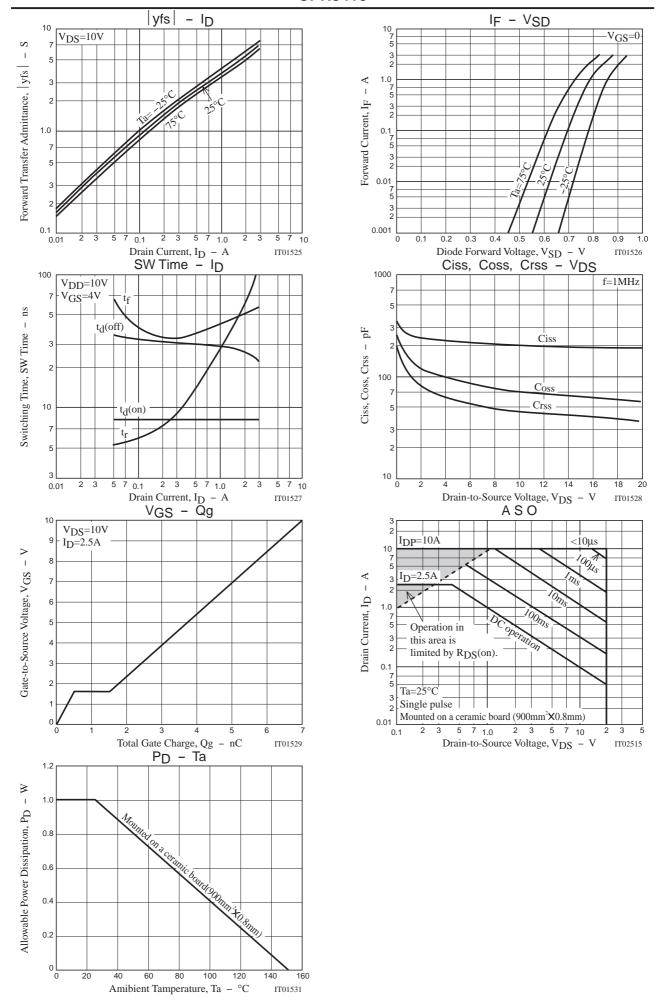
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Input Capacitance	Ciss	V _{DS} =10V, f=1MHz		200		pF
Output Capacitance	Coss	V _{DS} =10V, f=1MHz		70		pF
Reverse Transfer Capacitance	Crss	V _{DS} =10V, f=1MHz		45		pF
Turn-ON Delay Time	t _d (on)	See specified Test Circuit		8		ns
Rise Time	t _r	See specified Test Circuit		30		ns
Turn-OFF Delay Time	t _d (off)	See specified Test Circuit		28		ns
Fall Time	tf	See specified Test Circuit		42		ns
Total Gate Charge	Qg	V _{DS} =10V, V _{GS} =10V, I _D =2.5A		7		nC
Gate-to-Source Charge	Qgs	V _{DS} =10V, V _{GS} =10V, I _D =2.5A		0.5		nC
Gate-to-Drain "Miller" Charge	Qgd	V _{DS} =10V, V _{GS} =10V, I _D =2.5A		1		nC
Diode Forward Voltage	V _{SD}	I _S =2.5A, V _{GS} =0		0.85	1.2	V

Switching Time Test Circuit









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