



SANYO Semiconductors

DATA SHEET

An ON Semiconductor Company

CPH3457 — N-Channel Silicon MOSFET General-Purpose Switching Device Applications

Features

- ON-resistance $R_{DS(on)} = 73\text{m}\Omega$ (typ.)
- 1.8V drive
- Halogen free compliance
- Protection diode in

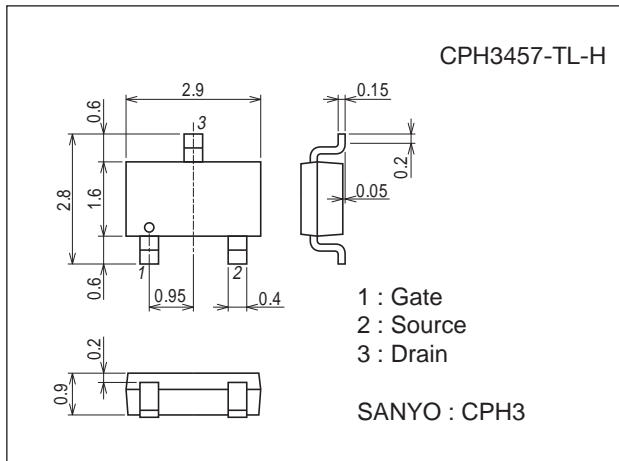
Specifications

Absolute Maximum Ratings at $T_a = 25^\circ\text{C}$

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V_{DSS}		30	V
Gate-to-Source Voltage	V_{GSS}		± 12	V
Drain Current (DC)	I_D		3	A
Drain Current (Pulse)	I_{DP}	$PW \leq 10\mu\text{s}$, duty cycle $\leq 1\%$	12	A
Allowable Power Dissipation	P_D	When mounted on ceramic substrate ($900\text{mm}^2 \times 0.8\text{mm}$)	1.0	W
Channel Temperature	T_{ch}		150	$^\circ\text{C}$
Storage Temperature	T_{stg}		-55 to +150	$^\circ\text{C}$

Package Dimensions

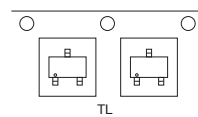
unit : mm (typ)
7015A-004



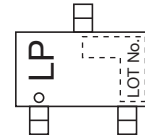
Product & Package Information

- Package : CPH3
- JEITA, JEDEC : SC-59, TO-236, SOT-23
- Minimum Packing Quantity : 3,000 pcs./reel

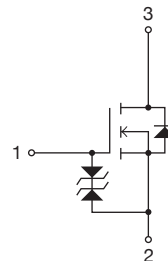
Packing Type: TL



Marking



Electrical Connection

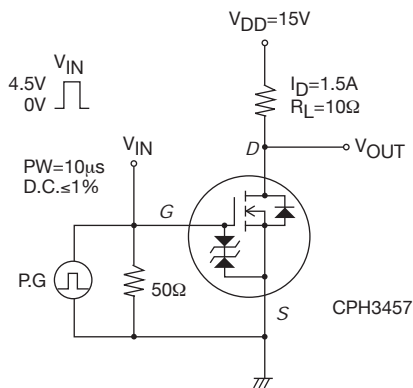


CPH3457

Electrical Characteristics at $T_a=25^\circ\text{C}$

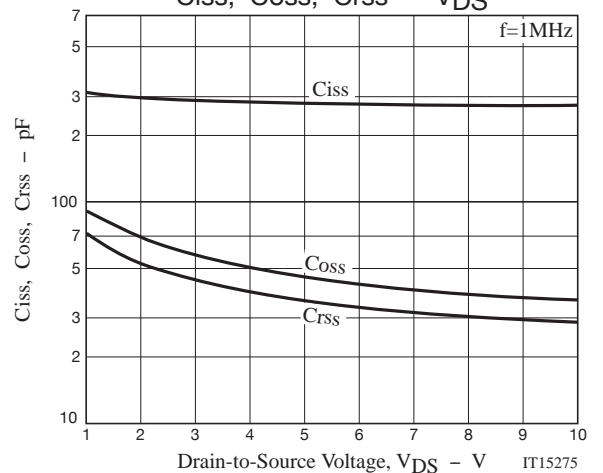
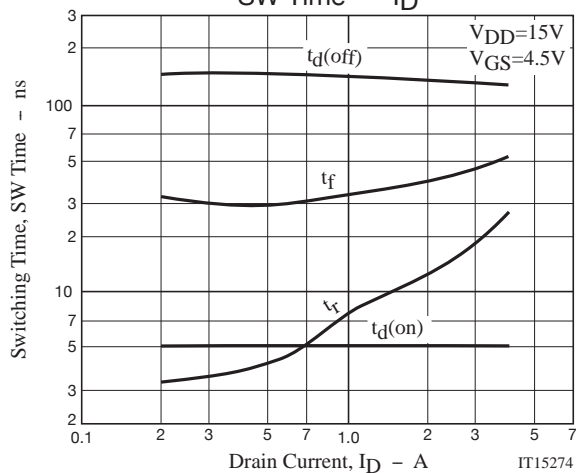
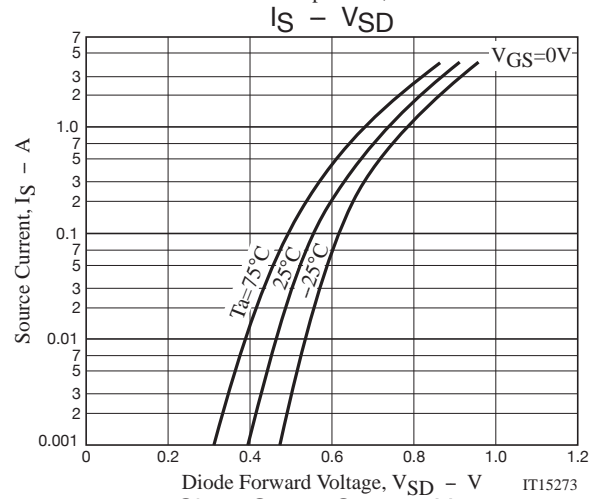
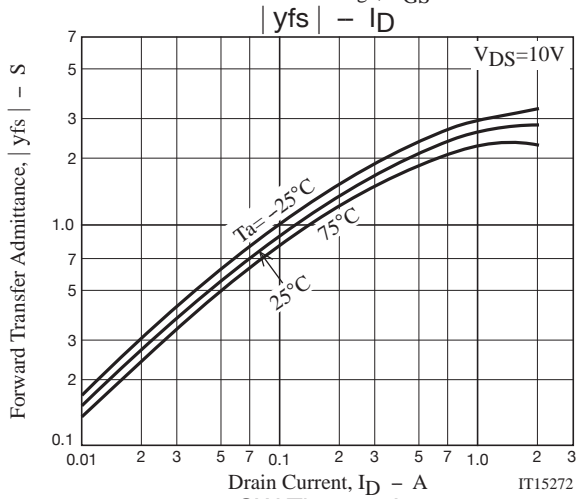
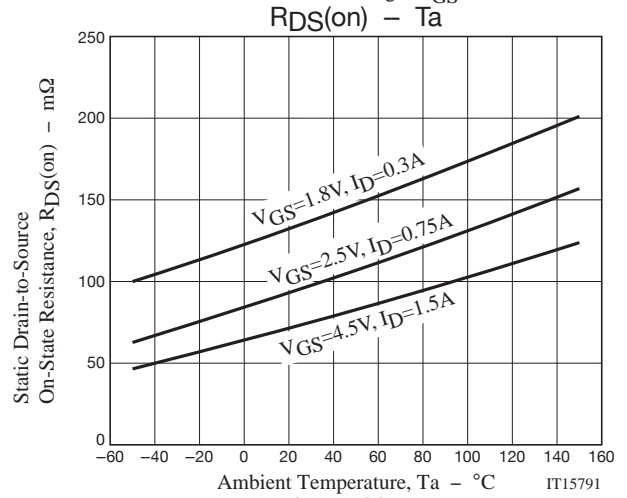
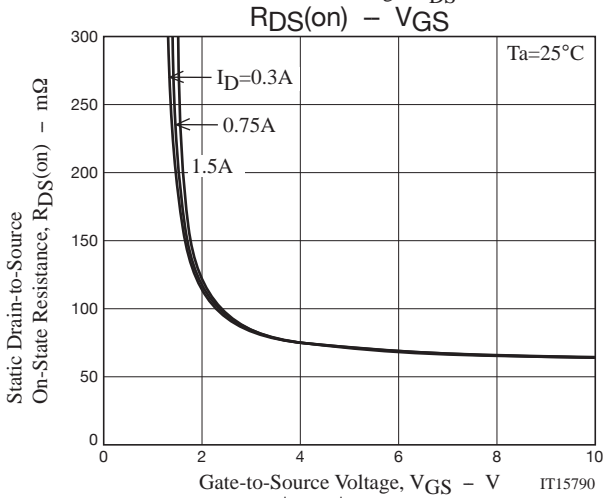
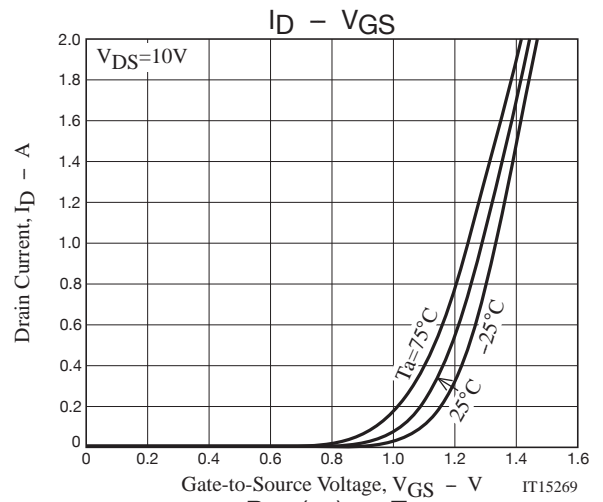
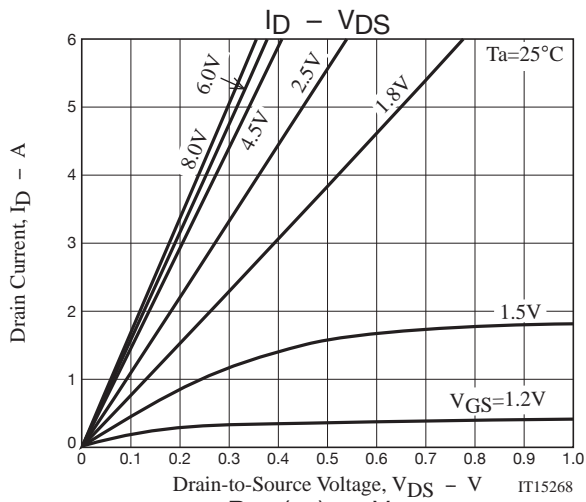
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	$I_D=1\text{mA}, V_{GS}=0\text{V}$	30			V
Zero-Gate Voltage Drain Current	I_{DSS}	$V_{DS}=30\text{V}, V_{GS}=0\text{V}$			1	μA
Gate-to-Source Leakage Current	I_{GSS}	$V_{GS}=\pm 8\text{V}, V_{DS}=0\text{V}$			± 10	μA
Cutoff Voltage	$V_{GS(off)}$	$V_{DS}=10\text{V}, I_D=1\text{mA}$	0.4		1.3	V
Forward Transfer Admittance	$ y_{fs} $	$V_{DS}=10\text{V}, I_D=1.5\text{A}$		2.7		S
Static Drain-to-Source On-State Resistance	$R_{DS(on)1}$	$I_D=1.5\text{A}, V_{GS}=4.5\text{V}$		73	95	$\text{m}\Omega$
	$R_{DS(on)2}$	$I_D=0.75\text{A}, V_{GS}=2.5\text{V}$		95	133	$\text{m}\Omega$
	$R_{DS(on)2}$	$I_D=0.3\text{A}, V_{GS}=1.8\text{V}$		135	203	$\text{m}\Omega$
Input Capacitance	C_{iss}	$V_{DS}=10\text{V}, f=1\text{MHz}$		265		pF
Output Capacitance	C_{oss}	$V_{DS}=10\text{V}, f=1\text{MHz}$		35		pF
Reverse Transfer Capacitance	C_{rss}	$V_{DS}=10\text{V}, f=1\text{MHz}$		28		pF
Turn-ON Delay Time	$t_{d(on)}$	See specified Test Circuit.		5.1		ns
Rise Time	t_r	See specified Test Circuit.		10		ns
Turn-OFF Delay Time	$t_{d(off)}$	See specified Test Circuit.		137		ns
Fall Time	t_f	See specified Test Circuit.		36		ns
Total Gate Charge	Q_g	$V_{DS}=15\text{V}, V_{GS}=4.5\text{V}, I_D=3\text{A}$		3.5		nC
Gate-to-Source Charge	Q_{gs}	$V_{DS}=15\text{V}, V_{GS}=4.5\text{V}, I_D=3\text{A}$		0.57		nC
Gate-to-Drain "Miller" Charge	Q_{gd}	$V_{DS}=15\text{V}, V_{GS}=4.5\text{V}, I_D=3\text{A}$		0.93		nC
Diode Forward Voltage	V_{SD}	$I_S=3\text{A}, V_{GS}=0\text{V}$		0.87	1.2	V

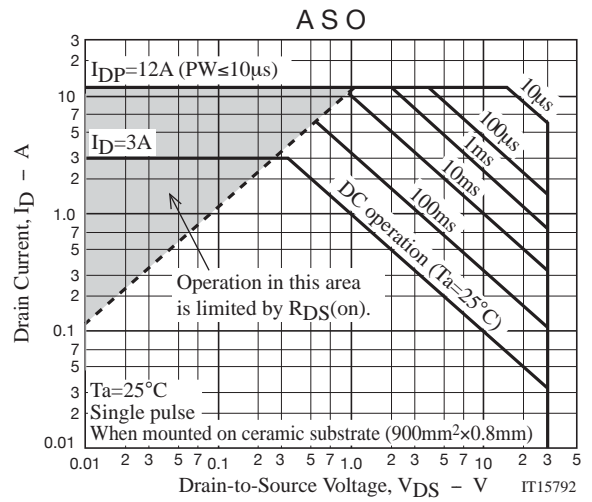
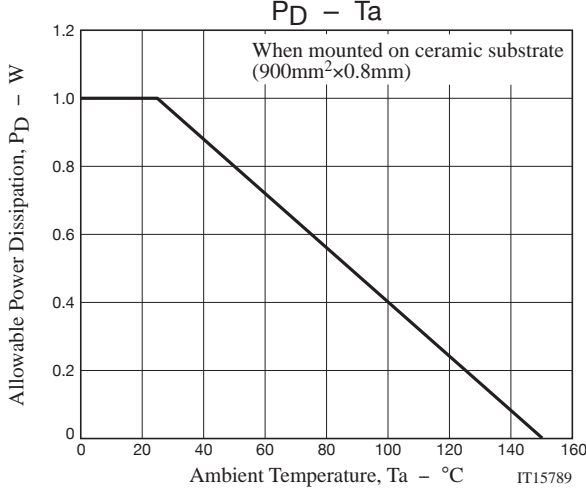
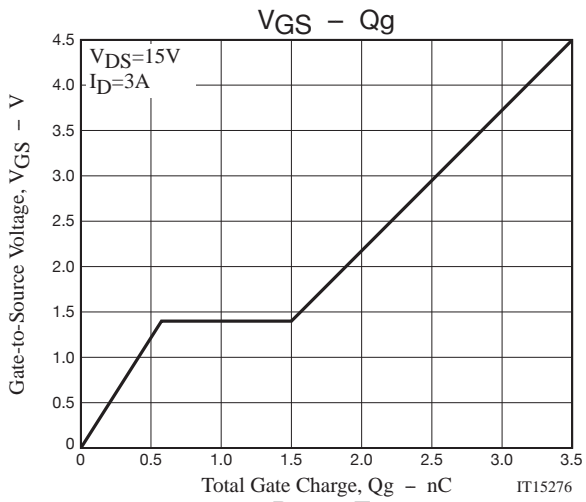
Switching Time Test Circuit



Ordering Information

Device	Package	Shipping	memo
CPH3457-TL-H	CPH3	3,000pcs./reel	Pb Free and Halogen Free





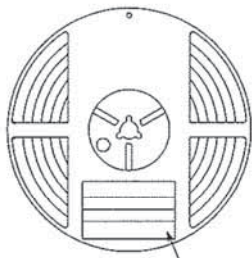
Embossed Taping Specification

CPH3457-TL-H

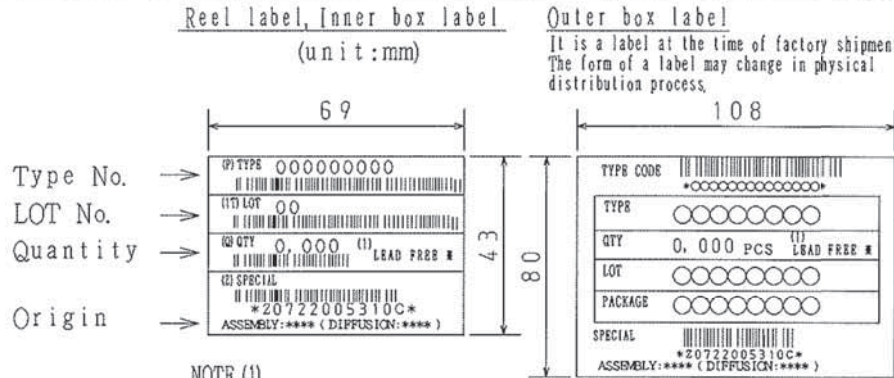
1. Packing Format

Package Name	Carrier Tape Type	Maximum Number of devices contained (pcs)			Packing format	
		Reel	Inner box	Outer box	Inner BOX (C-1)	Outer BOX (A-7)
CPH3	CPH3	3,000	15,000	90,000	5 reels contained Dimensions:mm (external) 183×72×185	6 inner boxes contained Dimensions:mm (external) 440×195×210

Packing method



Reel label



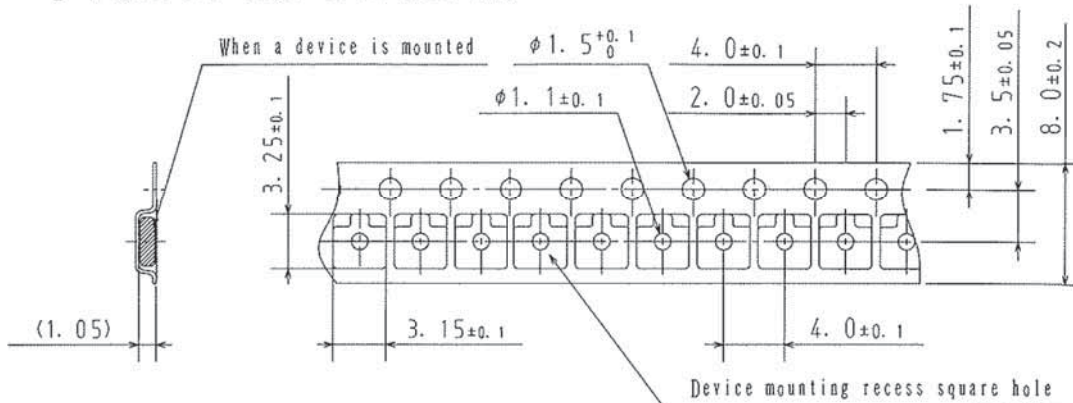
NOTE (1)

The LEAD FREE * description shows that the surface treatment of the terminal is lead free.

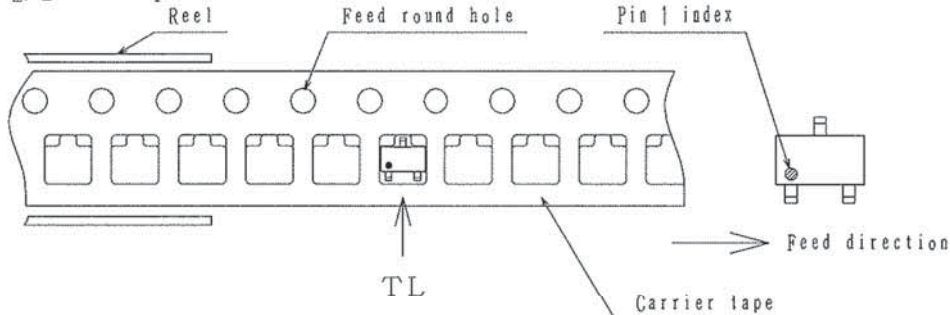
Label	JEITA Phase
LEAD FREE 3	JEITA Phase 3A
LEAD FREE 4	JEITA Phase 3

2. Taping configuration

2-1. Carrier tape size (unit:mm)



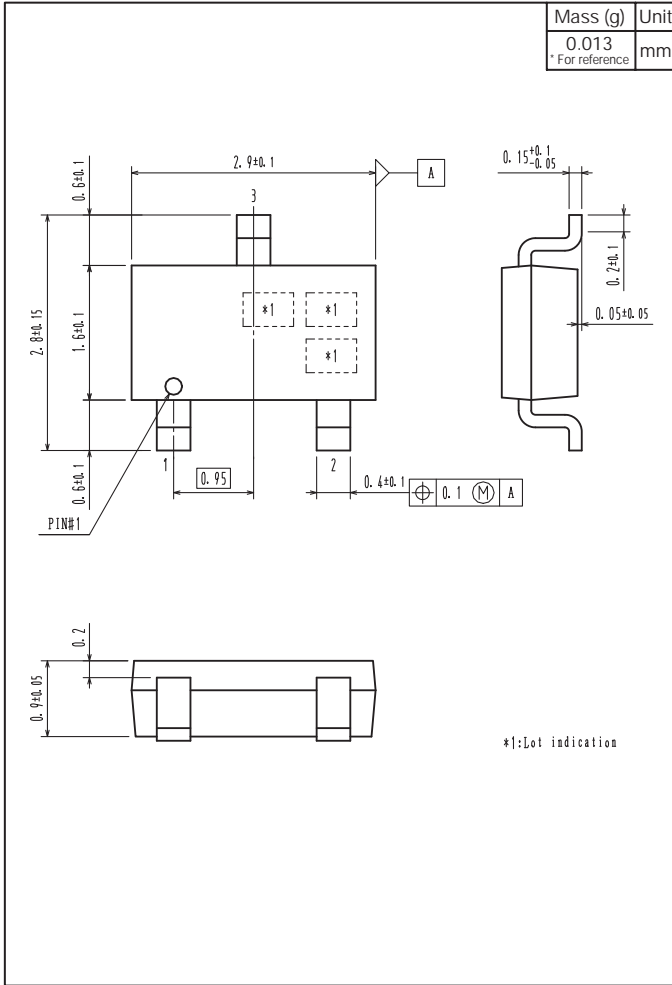
2-2. Device placement direction



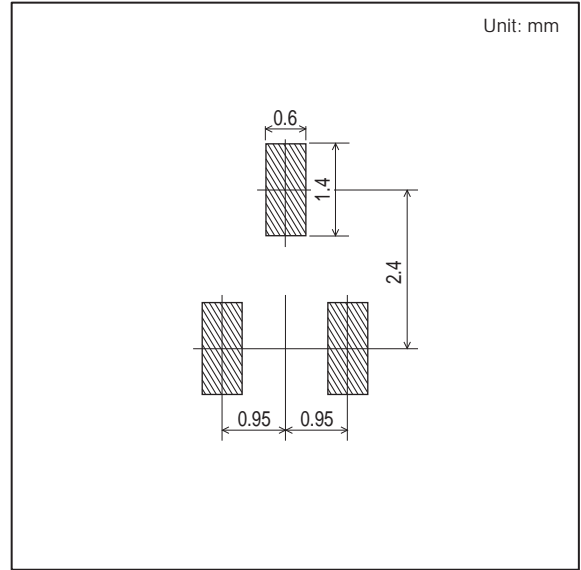
Those with one electrode terminal on the feed hole side.....TL

CPH3457

Outline Drawing CPH3457-TL-H



Land Pattern Example



Note on usage : Since the CPH3457 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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