

Signal Processor LSI for Single CCD Color Camera

Description

The CXD3172AR is a signal processor LSI for Ye, Cy, Mg and G single CCD color cameras. In addition to basic camera signal processing functions, it includes an AE/AWB detection circuit, a sync signal generation circuit and an external sync circuit, etc.

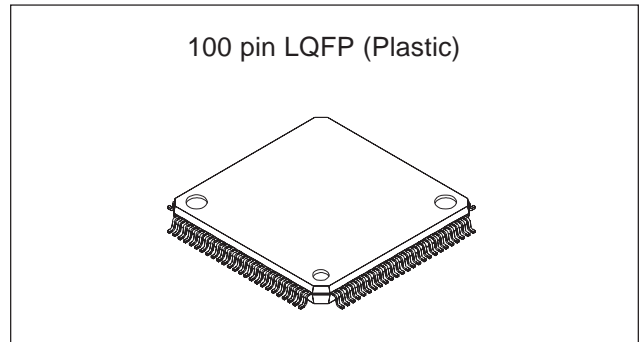
This chip also has a built-in microcontroller to realize basic camera functions such as AE/AWB without an external microcomputer.

Features

- Generates timing pulses to drive the single CCD image sensor
 - Built-in H/V driver for CCD image sensor
 - Luminance/chroma signal processing
- Supports NTSC/PAL modes
- Supports 510H/760H system CCD image sensor
- Built-in 10-bit A/D converter
- Built-in EVR (3ch)
- Analog composite output
 - Built-in digital encoder
 - 10-bit D/A converter output
- Digital output
 - Conforms to ITU-REC656/ITU-REC601 format
- Supports external sync functions
 - Sync separation circuit
 - Phase comparator
- AE/AWB detector
- Block control functions with a built-in microcontroller
 - AE/AWB/CLAMP/Blemish detection and compensation
- Peripheral IC control function
 - EVR/EEPROM communication control
- Serial communication function (2 mode selection)
 - Microcomputer communication/start-stop synchronous system communication (RS232C)
- Auto blemish detection and compensation function
- Mirror function

Applications

- Industrial CCD cameras
(Surveillance/FA/image input cameras)
- Multimedia CCD cameras
(Teleconferencing/personal computer cameras)



Absolute Maximum Ratings

• Supply voltage	V _{DD}	V _{SS} - 0.5 to +4.6	V
	AVD	V _{SS} - 0.5 to +4.6	V
	V _H	V _L - 0.5 to V _L + 26.0	V
	V _M	V _L - 0.5 to V _L + 26.0	V
• Input voltage	V _I	V _{SS} - 0.5 to V _{DD} + 0.5	V
• Output voltage	V _O	V _{SS} - 0.5 to V _{DD} + 0.5	V
• Operating Temperature	T _{opr}	-20 to +75	°C
• Storage Temperature	T _{stg}	-55 to +150	°C

Recommended Operating Conditions

• Supply voltage	V _{DD}	3.0 to 3.6	V
	AVD1, 3, 4, 5, 6	3.0 to 3.6	V
	AVD2	3.0 to 5.5	V
	V _H	11.64 to 15.45	V
	V _L	-7.5 to -4.5	V
	V _M	0	V
• Operating Temperature	T _{opr}	-20 to +75	°C

Applicable CCD Image Sensors*

- 510H color CCDs
(Type 1/3, 1/4, NTSC/PAL)
- 760H color CCDs
(Type 1/3, 1/4, NTSC/PAL)

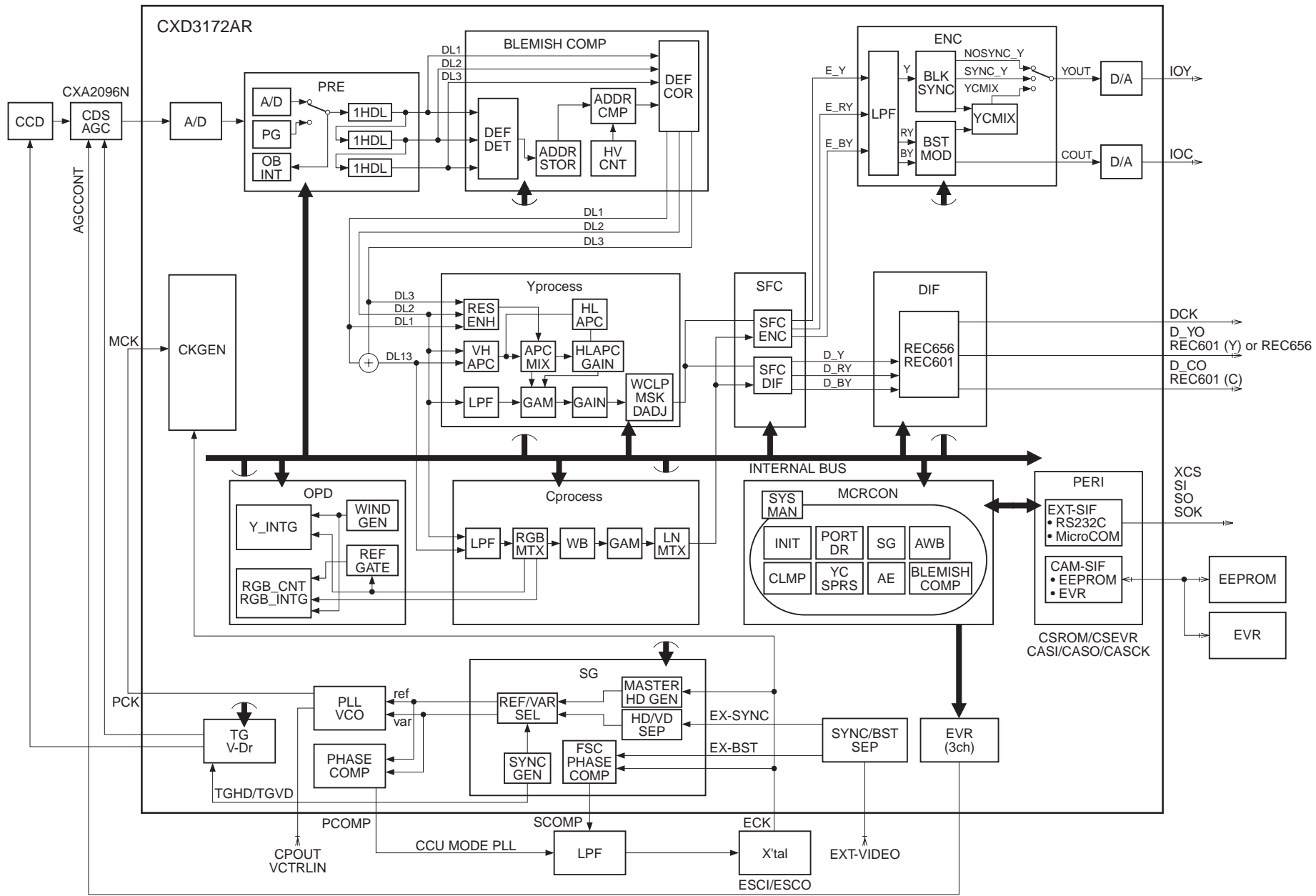
Supported Relates LSIs

- AGC CXA2096N
- EVR MB88347L (Fujitsu Limited.)
- EEPROM AK6480A
(Asahi Kasei Microsystems Co.,Ltd.)
BR9080A (ROHM)

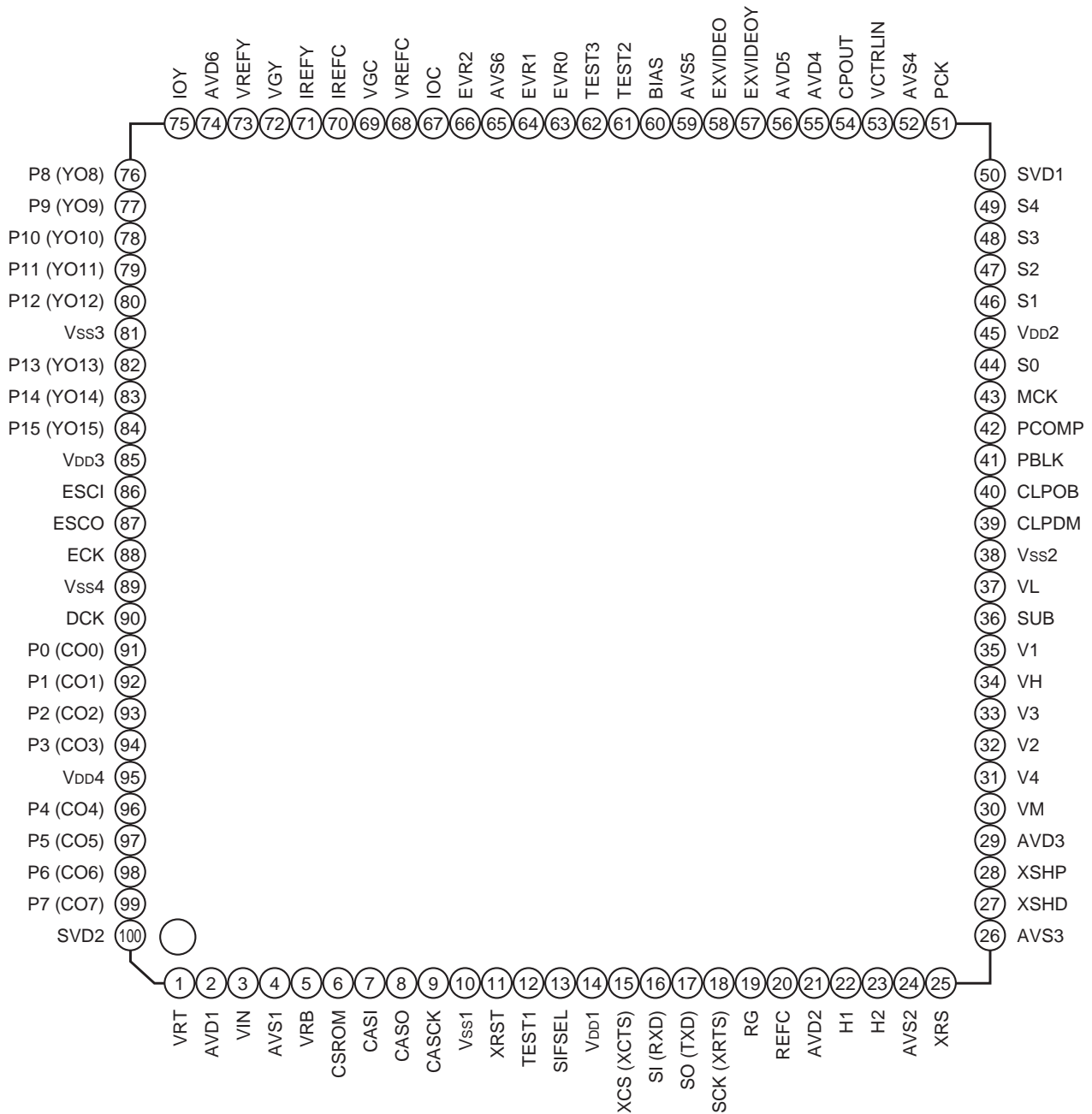
* Applicable CCD image sensors are applicable products as of preparing this data sheet. They may be changed according to the version up and production stop of CCD image sensor.

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Block Diagram



Pin Configuration



Note) Symbols in parentheses are the signal names when the function is switched by the communication parameter settings.

Pin Description

Pin No.	Symbol	I/O	Description	Power supply
1	VRT	I	A/D converter reference voltage (top) input.	AVD1 AVS1
2	AVD1	—	Power supply for A/D converter.	
3	VIN	I	Analog signal input. (for A/D converter)	
4	AVS1	—	GND	
5	VRB	I	A/D converter reference voltage (bottom) input.	
6	CSROM	O	Chip select output for camera peripheral ICs. (to EEPROM)	VDD1 VSS1
7	CASI	I	Serial data input for system communication.	
8	CASO	O	Serial data output for system communication.	
9	CASCK	O	Serial clock output for system communication.	
10	VSS1	—	GND	
11	XRST	I	Reset input.	
12	TEST1	I	Test	
13	SIFSEL	I	Serial interface mode switching. 0: microcomputer (3 wires) 1: RS232C	
14	VDD1	—	Power supply for logic.	
15	XCS	I	Chip select input for 3 wires serial interface. (when SIFSEL = 0)	
16	SI	I	Serial data input for 3 wires serial interface. (when SIFSEL = 0)	
17	SO	O	Serial data output for 3 wires serial interface. (when SIFSEL = 0)	
18	SCK	I/O	Serial clock input for 3 wires serial interface. (when SIFSEL = 0)	
19	RG	O	Reset gate pulse output.	
20	REFC	I	Capacitor connection.	
21	AVD2	—	Power supply for horizontal driving pulse. (3.3V/5.0V)	
22	H1	O	CCD horizontal register transfer pulse output.	
23	H2	O	CCD horizontal register transfer pulse output.	
24	AVS2	—	GND	
25	XRS	O	Resampling pulse output.	AVD3 AVS3
26	AVS3	—	GND	
27	XSHD	O	Data sample-and-hold pulse output.	
28	XSHP	O	Precharge level sample-and-hold pulse output.	
29	AVD3	—	Power supply for sample-and-hold pulse.	VH VL VM
30	VM	—	V-Driver Middle level power supply.	
31	V4	O	CCD vertical register transfer pulse output.	
32	V2	O	CCD vertical register transfer pulse output.	
33	V3	O	CCD vertical register transfer pulse output.	
34	VH	—	V-Driver High level power supply.	
35	V1	O	CCD vertical register transfer pulse output.	
36	SUB	O	CCD electronic shutter pulse output.	
37	VL	—	V-Driver Low level power supply.	

Pin No.	Symbol	I/O	Description	Power supply
38	Vss2	—	GND	V _{DD2} V _{SS2}
39	CLPDM	O	Dummy data clamp pulse output.	
40	CLPOB	O	Optical black clamp pulse output.	
41	PBLK	O	Preblanking pulse output.	
42	PCOMP	O	Phase comparator output.	
43	MCK	I	System drive clock input.	
44	S0	I/O	Sync signal input/output 0.	
45	V _{DD2}	—	Power supply for logic.	
46	S1	I/O	Sync signal input/output 1.	
47	S2	I/O	Sync signal input/output 2.	
48	S3	I/O	Sync signal input/output 3.	
49	S4	O	Sync signal output.	
50	SVD1	—	Sub power supply.	
51	PCK	O	PLL clock output.	
52	AVS4	—	GND	
53	VCTRLIN	I	Built-in VCO input.	
54	CPOUT	O	Built-in charge pump output.	
55	AVD4	—	Analog power supply for PLL.	AVD5 AVS5
56	AVD5	—	Power supply for burst separator.	
57	EXVIDEOY	I	Y signal input for external synchronization.	
58	EXVIDEO	I	Video signal input for external synchronization.	AVD6 AVS6
59	AVS5	—	GND	
60	BIAS	I	Bias current source.	
61	TEST2	I	Test	
62	TEST3	I	Test	
63	EVR0	O	EVR0 analog output.	
64	EVR1	O	EVR1 analog output.	
65	AVS6	—	GND	
66	EVR2	O	EVR2 analog output.	
67	IOC	O	Analog chroma output.	
68	VREFC	I	Reference voltage setting. (for chroma signal D/A converter)	
69	VGC	I	Capacitor connection. (approx. 0.1μF) (for chroma signal D/A converter)	
70	IREFC	I	Reference current setting. (for chroma signal D/A converter)	
71	IREFY	I	Reference current setting. (for luminance signal D/A converter)	
72	VGY	I	Capacitor connection. (approx. 0.1μF) (for luminance signal D/A converter)	
73	VREFY	I	Reference voltage setting. (for luminance signal D/A converter)	
74	AVD6	—	Power supply for DA converter/EVR.	
75	IOY	O	Analog Y output/composite video output.	

Pin No.	Symbol	I/O	Description	Power supply	
76	P8 (YO8)	I/O	Port 8 input or Y digital signal output or YUV digital signal output.	V _{DD3} V _{SS3}	
77	P9 (YO9)	I/O	Port 9 input or Y digital signal output or YUV digital signal output.		
78	P10 (YO10)	I/O	Port 10 input or Y digital signal output or YUV digital signal output.		
79	P11 (YO11)	I/O	Port 11 input or Y digital signal output or YUV digital signal output.		
80	P12 (YO12)	I/O	Port 12 input or Y digital signal output or YUV digital signal output.		
81	V _{SS3}	—	GND		
82	P13 (YO13)	I/O	Port 13 input or Y digital signal output or YUV digital signal output.		
83	P14 (YO14)	I/O	Port 14 input or Y digital signal output or YUV digital signal output.		
84	P15 (YO15)	I/O	Port 15 input or Y digital signal output or YUV digital signal output.		
85	V _{DD3}	—	Power supply.		
86	ESCI	I	Oscillation cell input.		
87	ESCO	O	Oscillation cell output.		
88	ECK	I	Encoder clock input.		
89	V _{SS4}	—	GND		V _{DD4} V _{SS4}
90	DCK	O	Clock output for digital output.		
91	P0 (CO0)	I/O	Port 0 input or C digital signal output.		
92	P1 (CO1)	I/O	Port 1 input or C digital signal output.		
93	P2 (CO2)	I/O	Port 2 input or C digital signal output.		
94	P3 (CO3)	I/O	Port 3 input or C digital signal output.		
95	V _{DD4}	—	Power supply.		
96	P4 (CO4)	I/O	Port 4 input or C digital signal output or EEPROM BUSY signal input or OPD frame pulse output.		
97	P5 (CO5)	I/O	Port 5 input or C digital signal output or VD output.		
98	P6 (CO6)	I/O	Port 6 input or C digital signal output or HD output.		
99	P7 (CO7)	I/O	Port 7 input or C digital signal output.		
100	SVD2	—	Sub power supply.		

Electrical Characteristics

DC Characteristics

(Within recommended operating range)

Item	Symbol	Conditions	Min.	Typ.	Max.	Unit
Supply voltage 1	V _{DD1, 2, 3, 4}		3.0	3.3	3.6	V
	AVD1	A/D input amplitude = 1Vp-p	3.0	3.3	3.6	V
	AVD2		3.0	—	5.5	V
	AVD3, 4, 5		3.0	3.3	3.6	V
	AVD6	D/A output amplitude = 1Vp-p	3.0	3.3	3.6	V
Supply voltage 2	SVD1, 2		—	3.3	—	V
Supply voltage 3	V _H		11.64	—	15.45	V
	V _L		-7.5	—	-4.5	V
	V _M		—	0	—	V
Output voltage 1	V _{OH1} *1	I _{OH} = 1.0mA	V _{DD} - 0.4			V
	V _{OL1} *1	I _{OL} = 1.0mA			0.4	V
Output voltage 2	V _{OH2} *2, *3	I _{OH} = 4.0mA	V _{DD} - 0.4			V
	V _{OL2} *2, *3	I _{OL} = 4.0mA			0.4	V
Output voltage 3	V _{OH3} *4	I _{OH} = 12.0mA	V _{DD} /2			V
	V _{OL3} *4	I _{OL} = 12.0mA			V _{DD} /2	V
Output voltage 4	V _{OH4} *5	I _{OH} = 4.0mA	V _H - 0.25			V
	V _{OL4} *5	I _{OL} = 5.4mA			V _L + 0.25	V
Output voltage 5	V _{OH5} *6	I _{OH} = 5.0mA	V _M - 0.25			V
	V _{OL5} *6	I _{OL} = 10.0mA			V _L + 0.25	V
Output voltage 6	V _{OH6} *7	I _{OH} = 7.2mA	V _H - 0.25			V
	V _{OM61} *7	I _{OL} = 5.0mA			V _M + 0.25	V
	V _{OM62} *7	I _{OH} = 5.0mA	V _M - 0.25			V
	V _{OL6} *7	I _{OL} = 10.0mA			V _L + 0.25	V
Input voltage	V _{T+} *1, *3, *8		0.7V _{DD}			V
	V _{T-} *1, *3, *8				0.2V _{DD}	V
Hysteresis	V _{T+} - V _{T-} *1, *3, *8			0.5		V
Input leak current	I _{IH} *8	V _{IN} = V _{DD}	40	100	240	μA

*1 S0, S1, S2, S3, P0, P1, P2, P3, P4, P5, P6, P7, P8, P9, P10, P11, P12, P13, P14, P15

*2 CSROM, CASO, CASCK, SO, CLPDM, CLPOB, PBLK, S4, PCK, DCK

*3 SCK

*4 ESCI, ESCO

*5 SUB

*6 V2, V4

*7 V1, V3

*8 CASI, XRST, SIFSEL, XCS, SI, TEST2, TEST3

*9 TEST1

I/O Pin Capacitance $(V_{DD} = V_I = 0V, f = 1MHz)$

Item	Symbol	Min.	Typ.	Max.	Unit
Input pin capacitance	C _{IN}			9	pF
Output pin capacitance	C _{OUT}			11	pF
I/O pin capacitance	C _{I/O}			11	pF

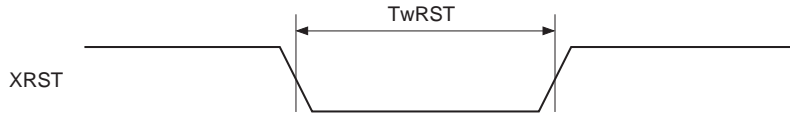
AC Characteristics

(Within recommended operating range)

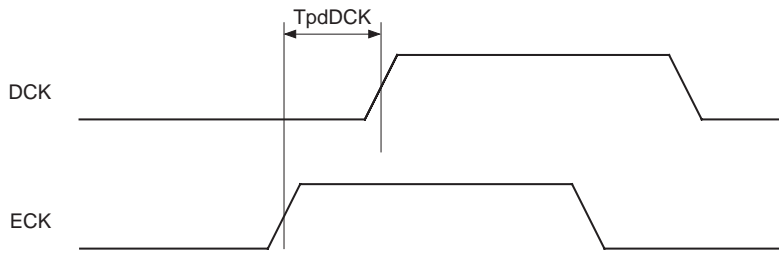
Classification	Item	Symbol	Min.	Typ.	Max.	Unit
Reset input	Min. low time of reset operation of XRST pin	TwRST	500	—	—	ns
Digital output	P0 to P15 output delay time against DCK ↑	TpdP	5	—	20	ns
	S0 output delay time against DCK ↑	TpdS0	6	—	28	ns
	S1 output delay time against DCK ↑	TpdS1	6	—	30	ns
	DCK output delay time against ECK ↑	TpdDCK	7	—	24	ns
SYNC block sync output	HD, VD, FLD and SYNC output delay time against ECK ↑	TpdSY	10	—	45	ns
Serial communication I/O	SCK input pulse width (High period)	TwHSCK	—	580	—	ns
	SCK input pulse width (Low period)	TwLSCK	—	580	—	ns
	XCE input setup time against SCK ↓	TsuXCE	580	—	—	ns
	XCE input hold time against SCK ↑	ThXCE	580	—	—	ns
	SI input setup time against SCK ↑	TsuSI	0	—	—	ns
	SI input hold time against SCK ↑	ThSI	0	—	—	ns
	SO output transit time against XCE ↓ (Hi-Z → Data active)	TzdSO	0	—	—	ns
	SO transit time against XCE ↑ (Data active → Hi-Z)	TdzSO	0	—	—	ns
	SO output delay time against SCK ↓	TpdSO	—	—	580	ns

AC Characteristics diagram

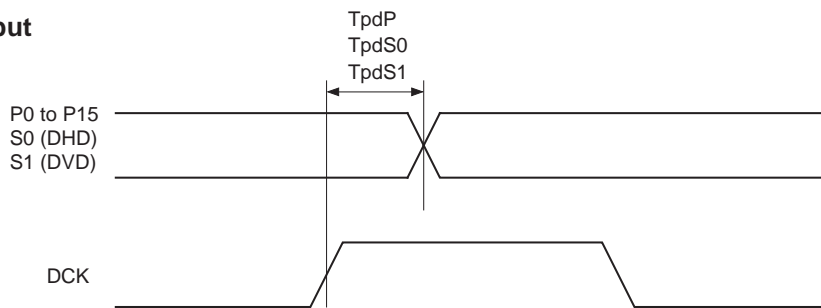
1. Reset input



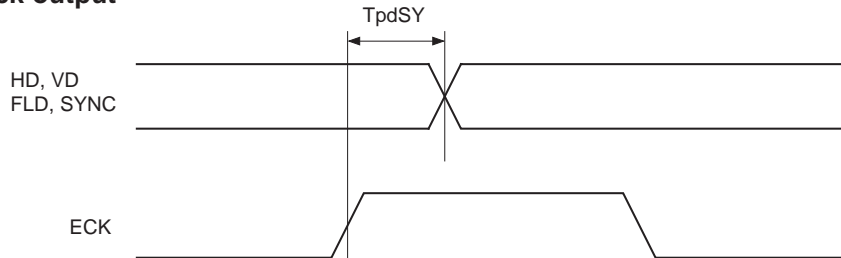
2. DCK output



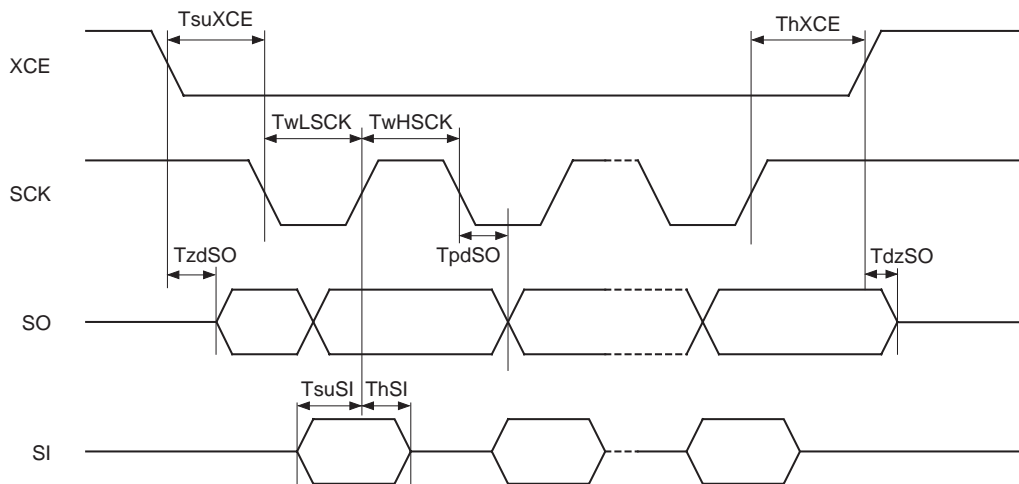
3. Digital output



4. SYNC block output



5. Serial communication I/O



Relationship between MODESEL and Each Clock

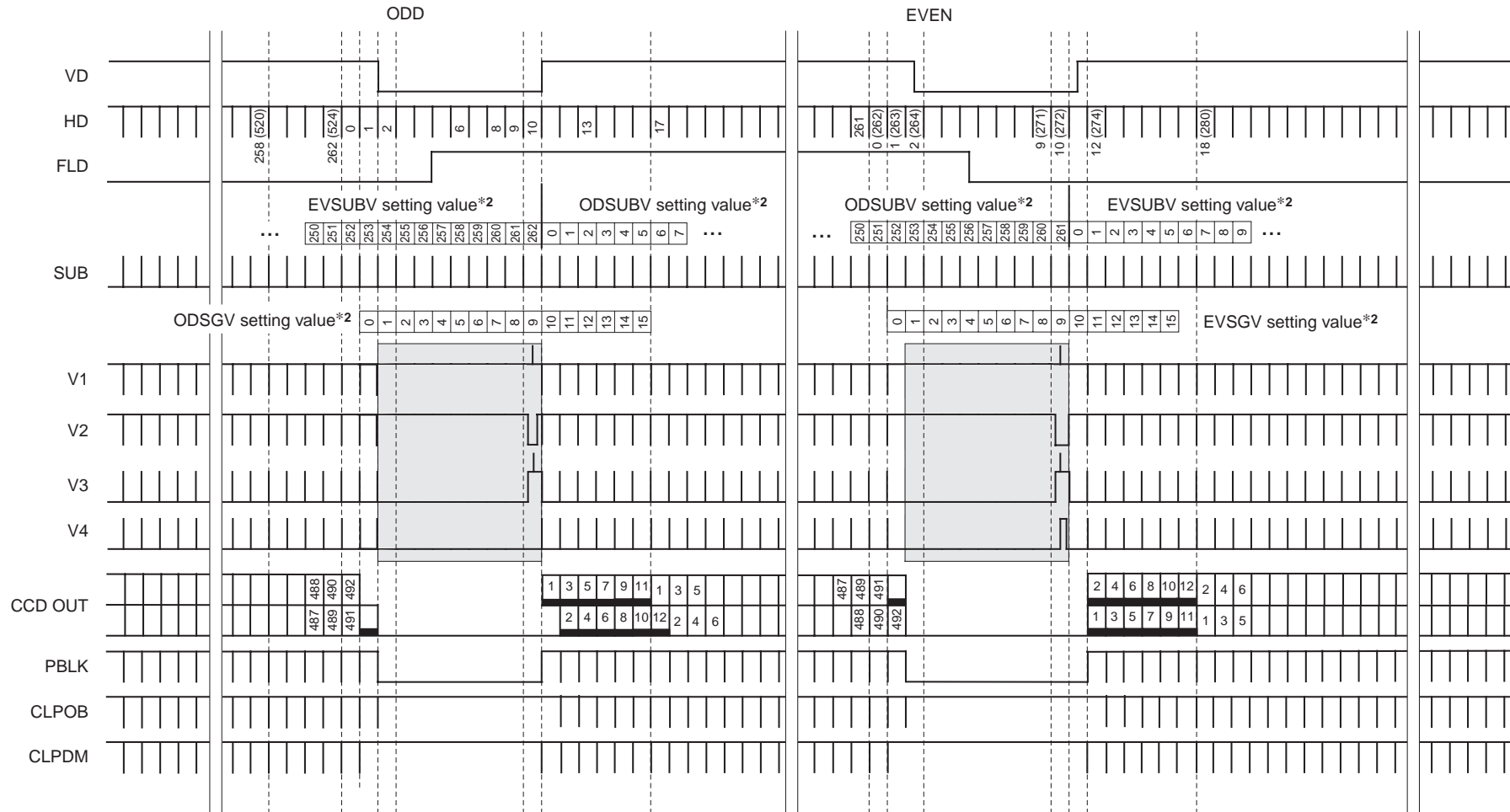
TV system	CCD type	MODESEL	ECK	MCK	DSPCK*1	Remarks
NTSC	510H	0	38.13986MHz	(ECK = MCK)	MCK/4	ECK and MCK are connected internally.
		1	28.63636MHz	38.13986MHz		
		2	27.00000MHz			
	760H	6	28.63636MHz	(ECK = MCK)	MCK/2	ECK and MCK are connected internally.
		8	27.00000MHz	28.63636MHz		
PAL	510H	3	37.87500MHz	(ECK = MCK)	MCK/4	ECK and MCK are connected internally.
		4	35.46895MHz	37.87500MHz		
		5	27.00000MHz			
	760H	9	28.37500MHz	(ECK = MCK)	MCK/2	ECK and MCK are connected internally.
		A	35.46895MHz	28.37500MHz		
		B	27.00000MHz			

*1 DSPCK: clock which is not output by external pins

See the above table for the relationship between encoder clock (ECK) and system drive clock (MCK).

Vertical Timing Chart MODESEL 0, 1, 2 [510H NTSC]

Applicable CCD image sensor:
ICX206AK/226AK/254AK/404AK

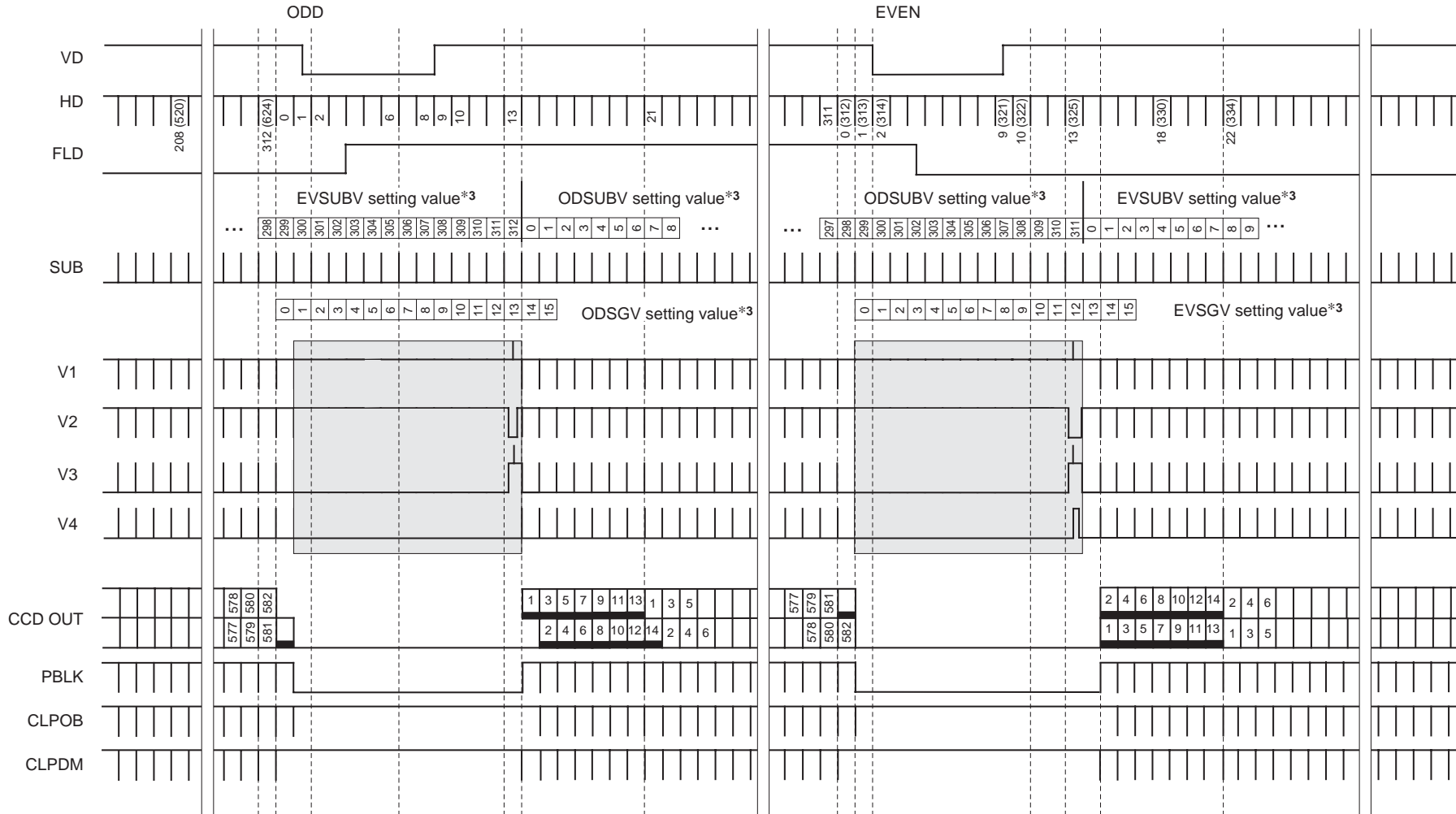


The shaded area shows the position variable range of the read SG pulse. However, note that the range may over depending on the setting.

*2 The value changes by the parameter (Category6: TG) setting.

Vertical Timing Chart MODESEL 3, 4, 5 [510H PAL]

Applicable CCD image sensor:
ICX207AK/227AK/255AK/405AK

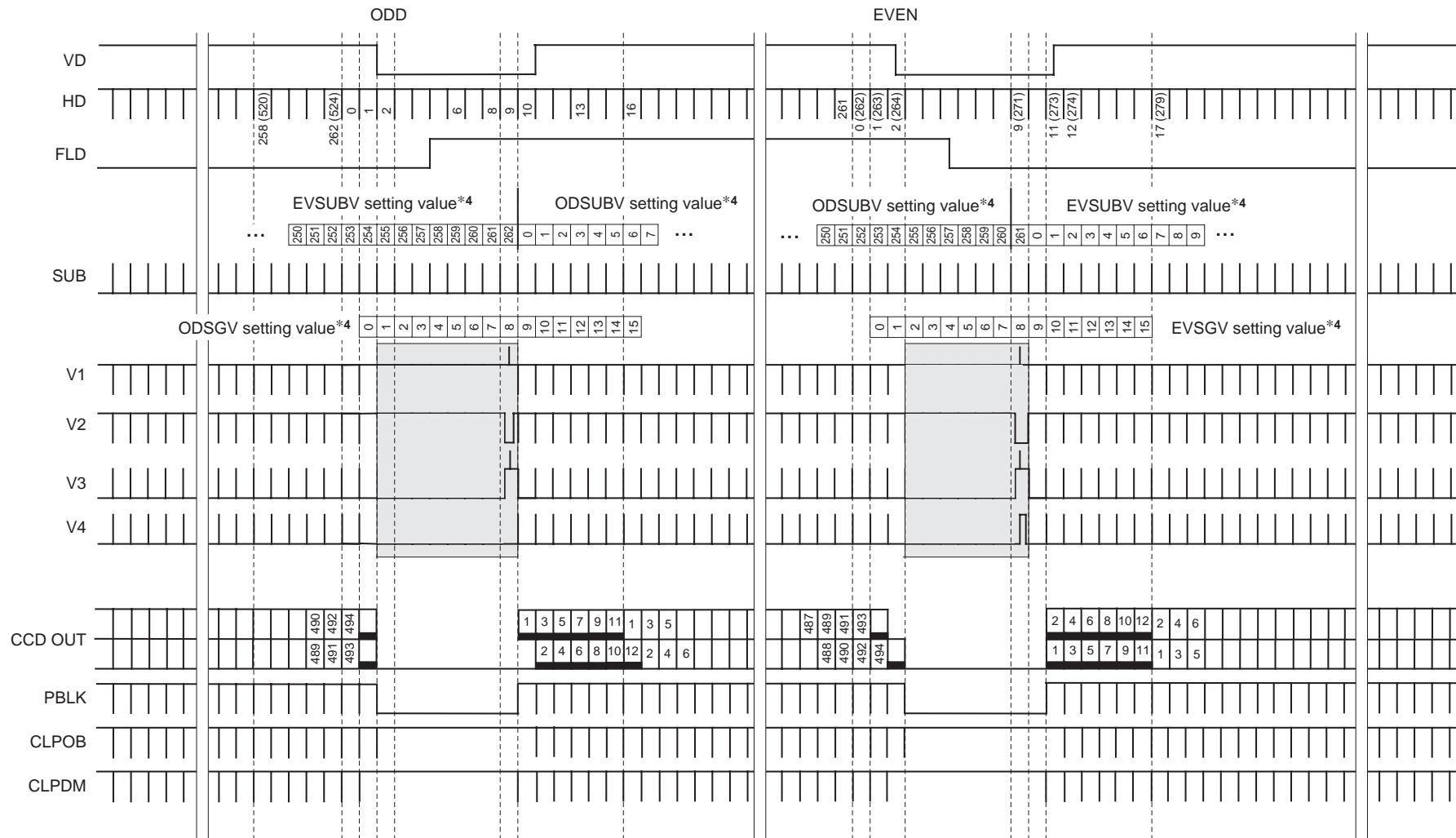


The shaded area shows the position variable range of the read SG pulse. However, note that the range may over depending on the setting.

*3 The value changes by the parameter (Category6: TG) setting.

Vertical Timing Chart MODESEL 6, 8 [760H NTSC]

Applicable CCD image sensor:
ICX228AK/258AK/278AK/408AK

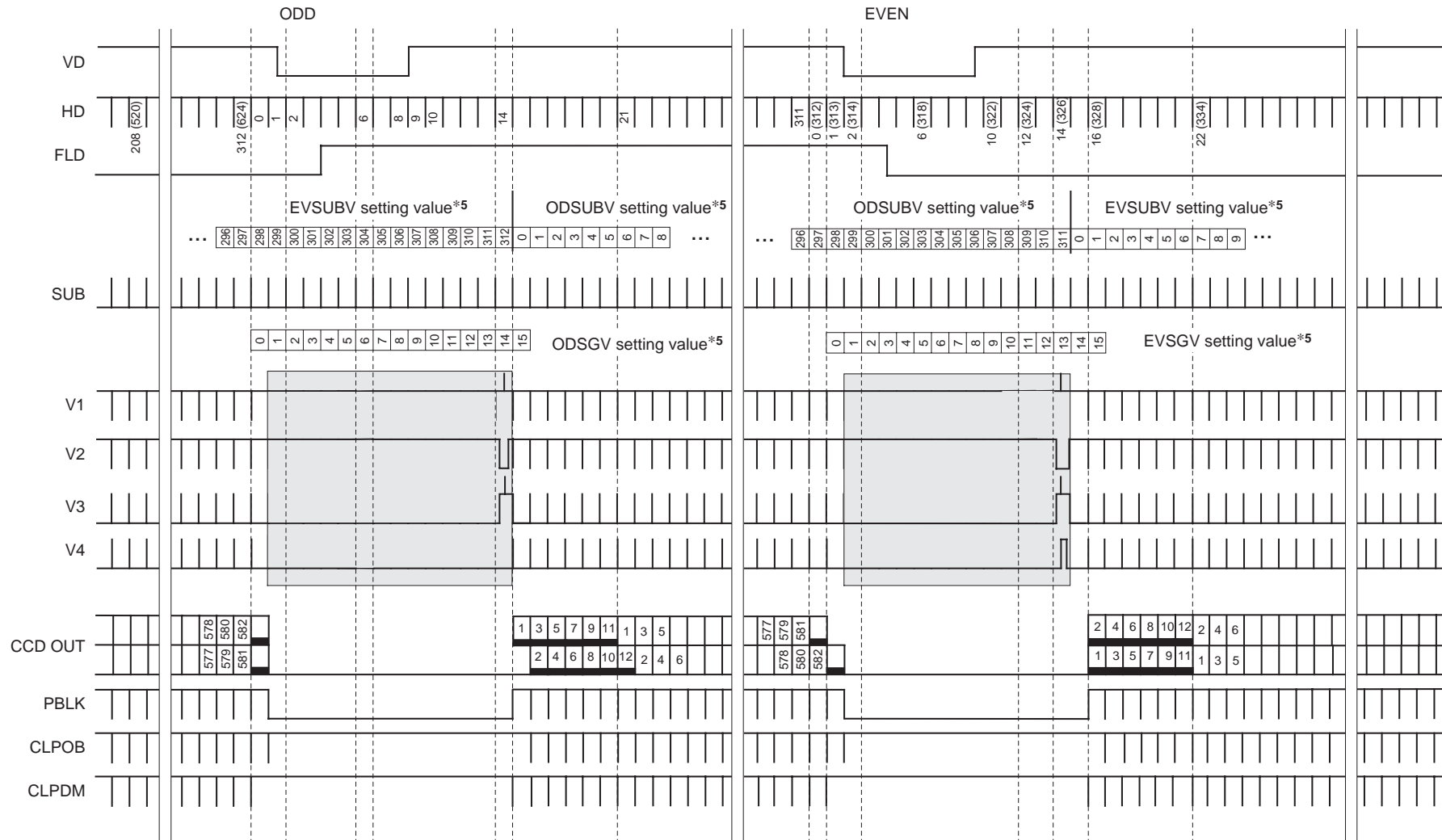


The shaded area shows the position variable range of the read SG pulse. However, note that the range may over depending on the setting.

*4 The value changes by the parameter (Category6: TG) setting.

Vertical Timing Chart MODESEL 9, A, B [760H PAL]

Applicable CCD image sensor:
ICX229AK/259AK/279AK/409AK

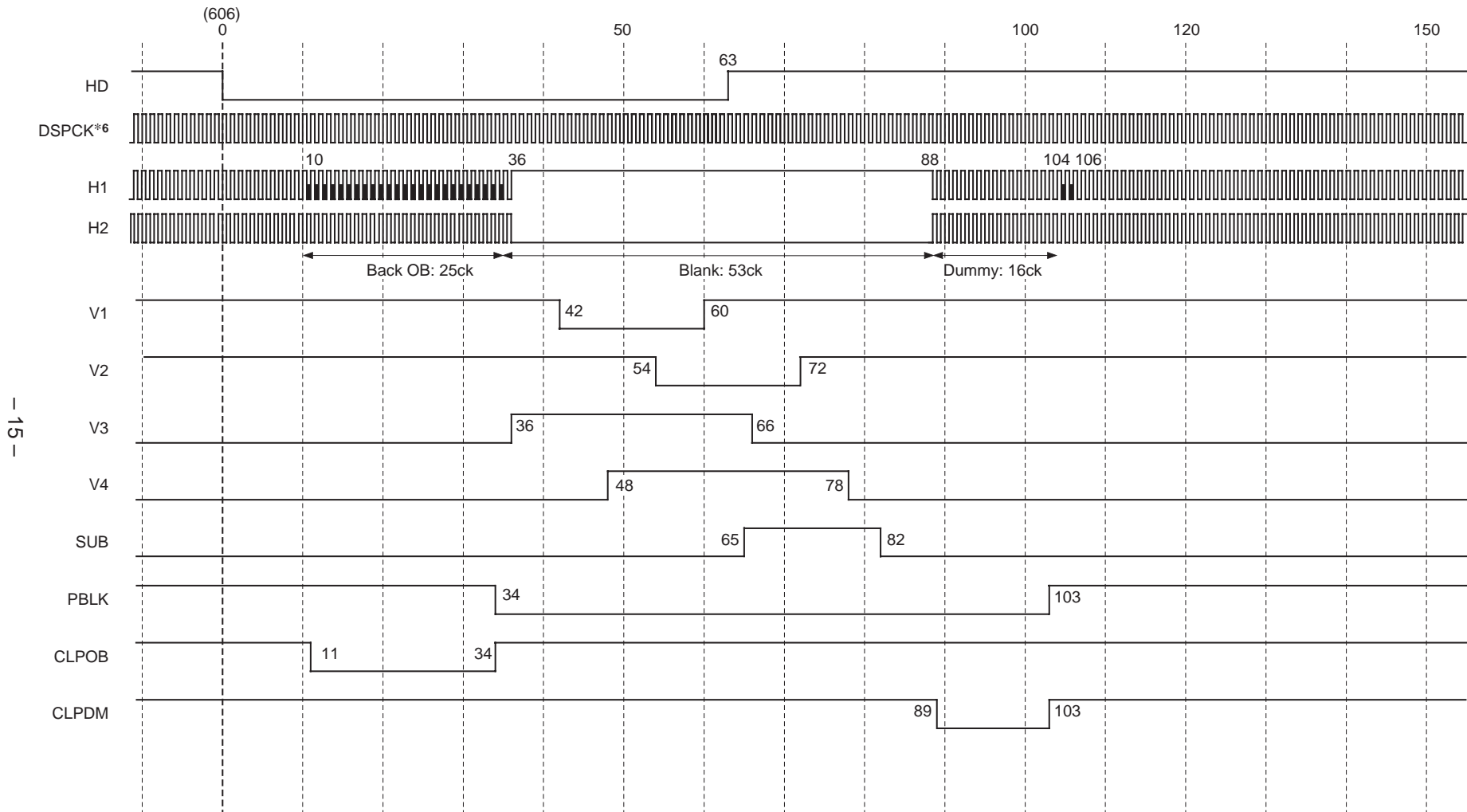


The shaded area shows the position variable range of the read SG pulse. However, note that the range may over depending on the setting.

*5 The value changes by the parameter (Category6: TG) setting.

Horizontal Timing Chart MODESEL 0, 1, 2 [510H NTSC] DSPCK: 606f_H (9.534965MHz: 104.88ns)

Applicable CCD image sensor:
ICX206AK/226AK/254AK/404AK

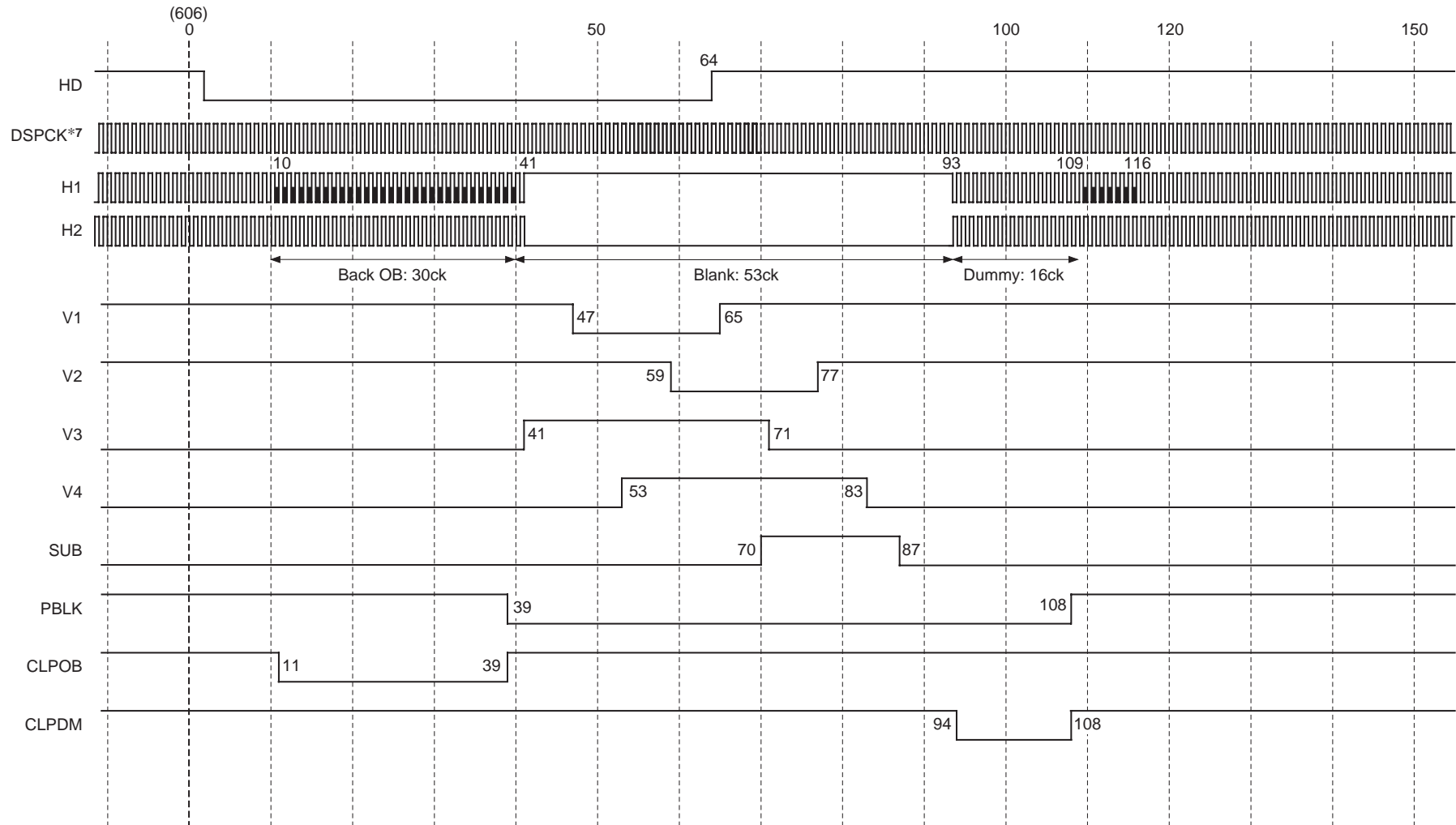


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*6 DSPCK is a clock which is not output by external pins. See the previously described table (Relationship between MODESEL and Each Clock).

Horizontal Timing Chart MODESEL 3, 4, 5 [510H PAL] DSPCK: 606f_H (9.46875MHz: 105.60ns)

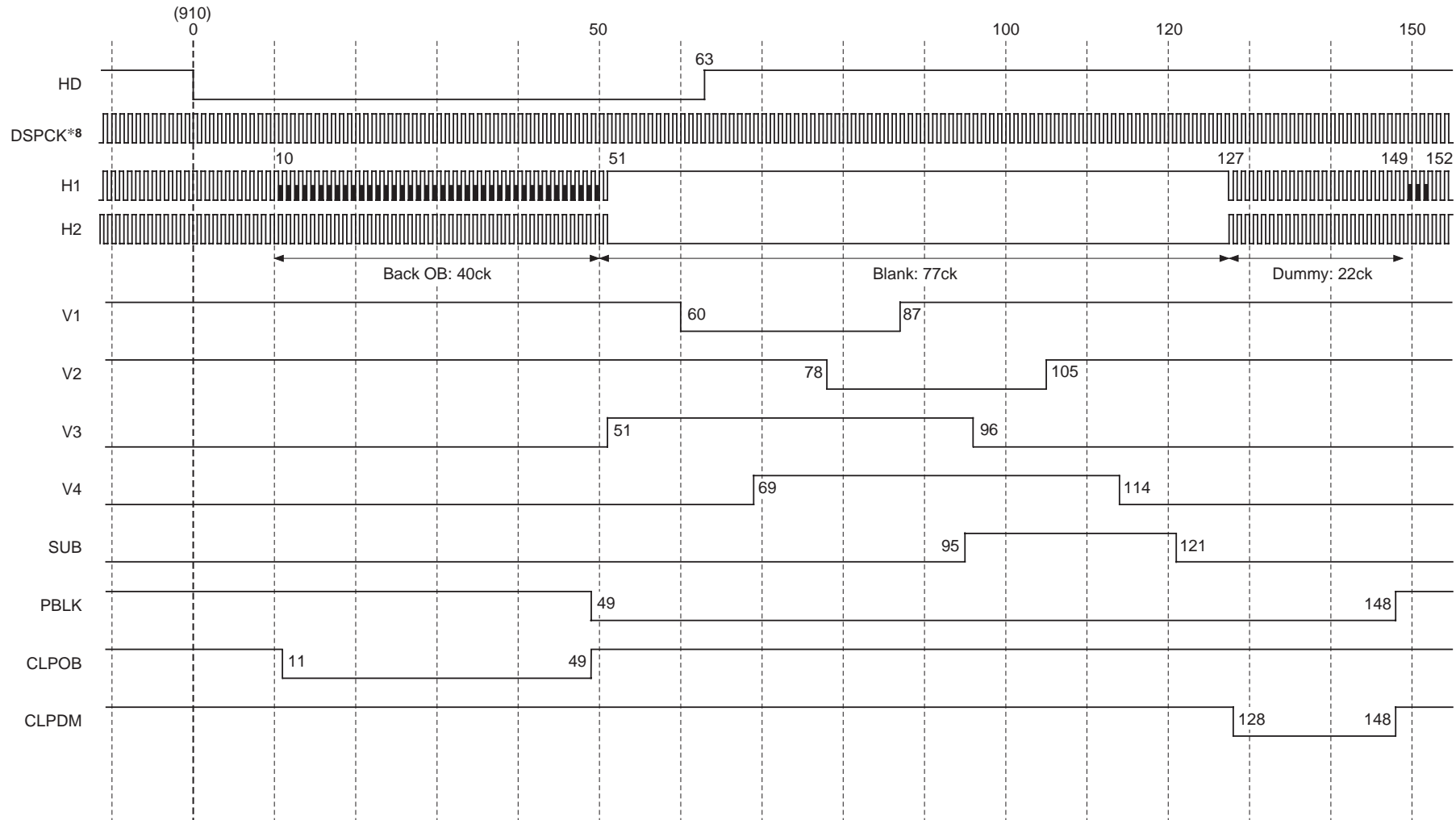
Applicable CCD image sensor:
ICX207AK/227AK/255AK/405AK



*7 DSPCK is a clock which is not output by external pins. See the previously described table (Relationship between MODESEL and Each Clock).

Horizontal Timing Chart MODESEL 6, 8 [760H NTSC] DSPCK: 910f_H (14.31818MHz: 69.84ns)

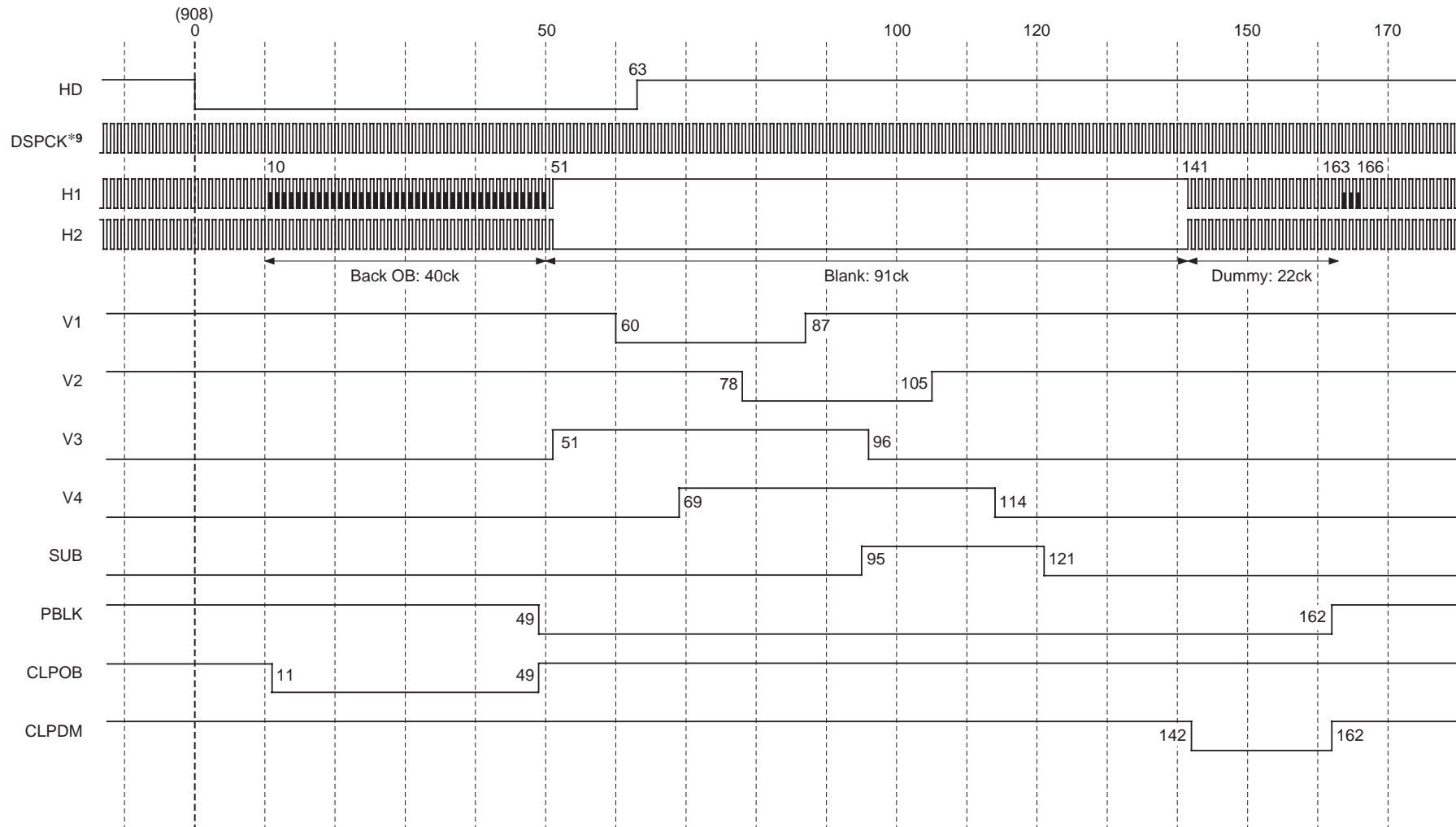
Applicable CCD image sensor:
ICX228AK/258AK/278AK/408AK



*8 DSPCK is a clock which is not output by external pins. See the previously described table (Relationship between MODESEL and Each Clock).

Horizontal Timing Chart MODESEL 9, A, B [760H PAL] DSPCK: 908f_H (14.1875MHz: 70.48ns)

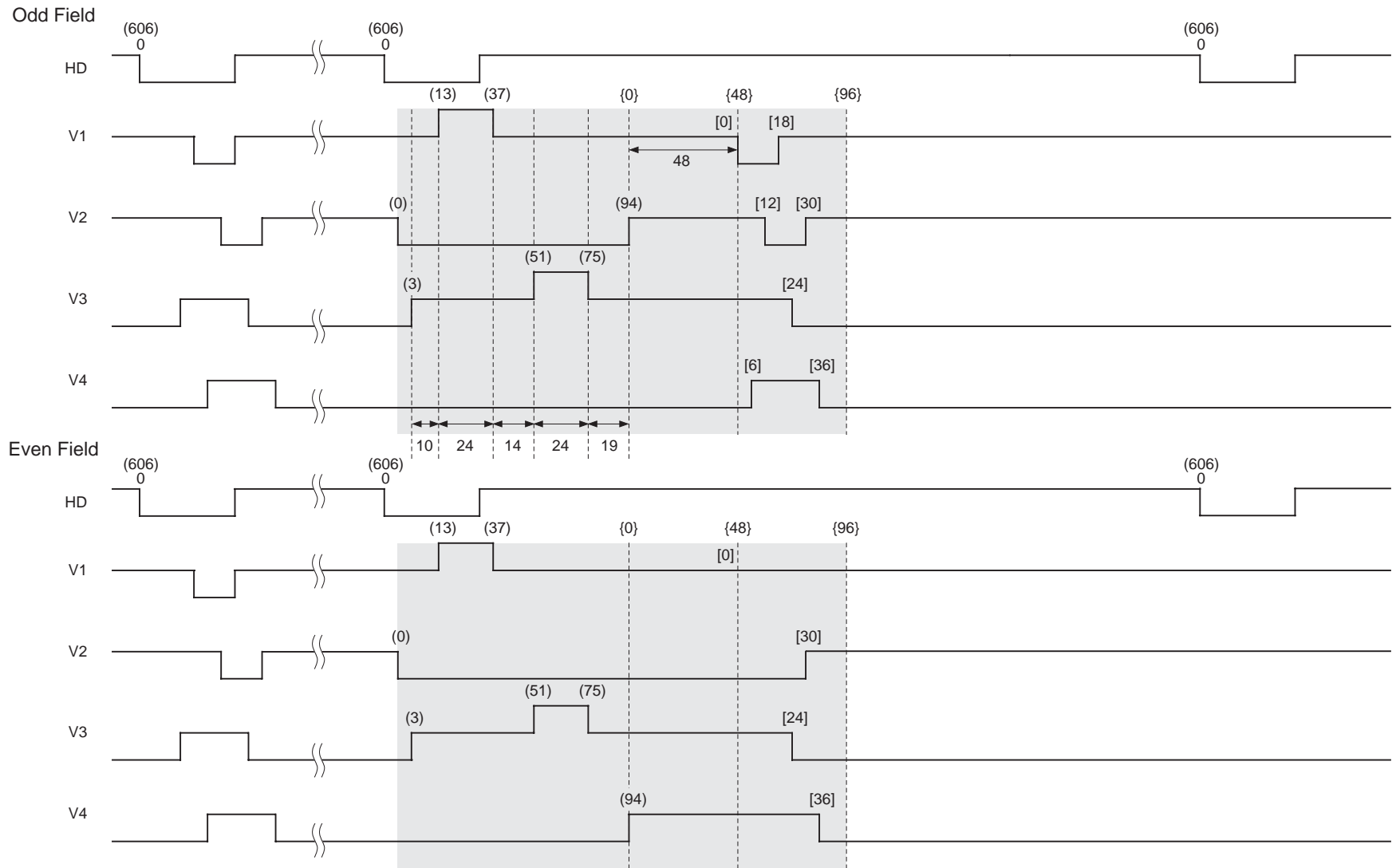
Applicable CCD image sensor:
ICX229AK/259AK/279AK/409AK



*9 DSPCK is a clock which is not output by external pins. See the previously described table (Relationship between MODESEL and Each Clock).

Horizontal Timing Chart MODESEL 0, 1, 2 [510H NTSC]

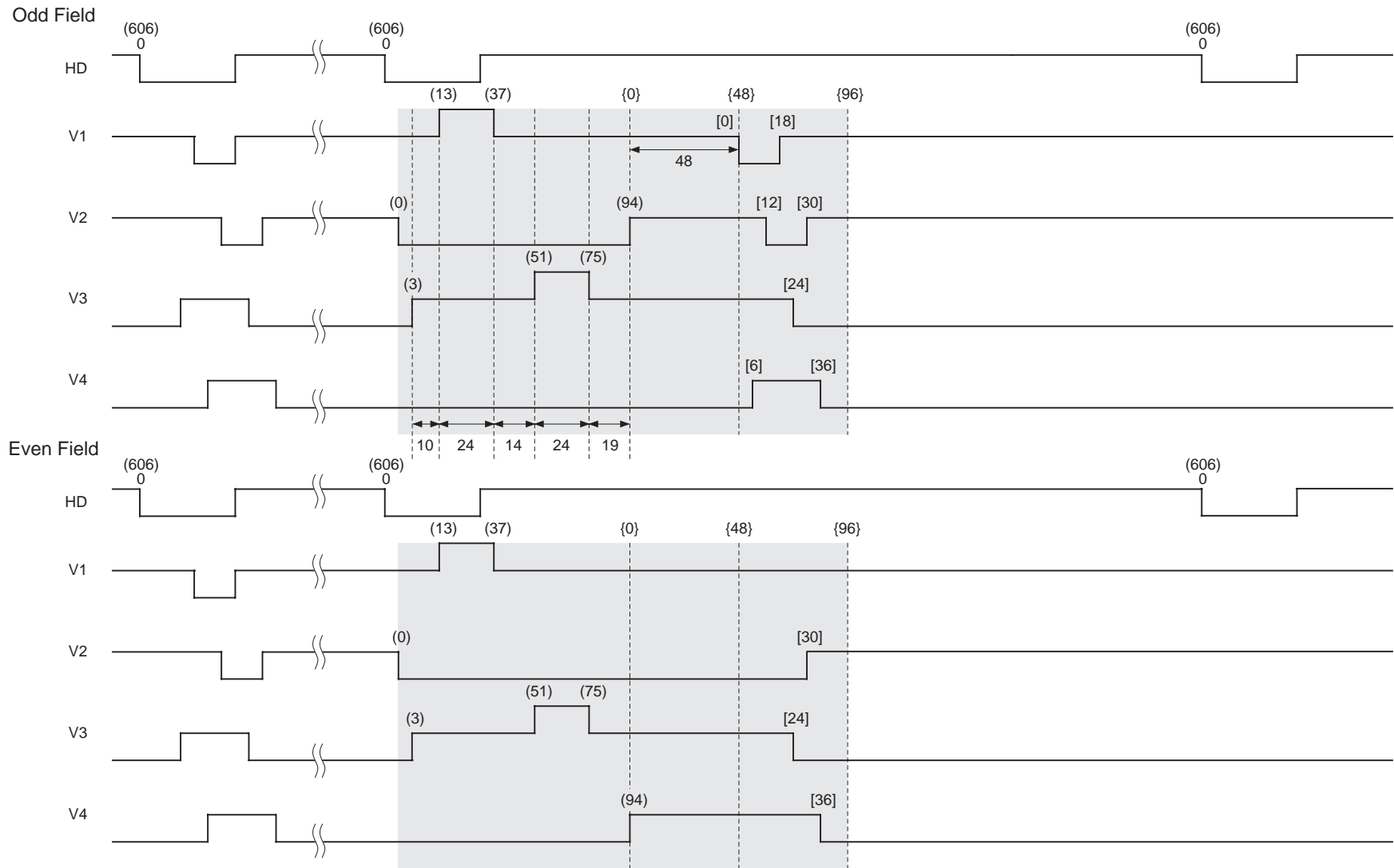
Applicable CCD image sensor:
ICX206AK/226AK/254AK/404AK



The shaded pulse area shifts while keeping the relative position relationship during shutter setting.

Horizontal Timing Chart MODESEL 3, 4, 5 [510H PAL]

Applicable CCD image sensor:
ICX207AK/227AK/255AK/405AK

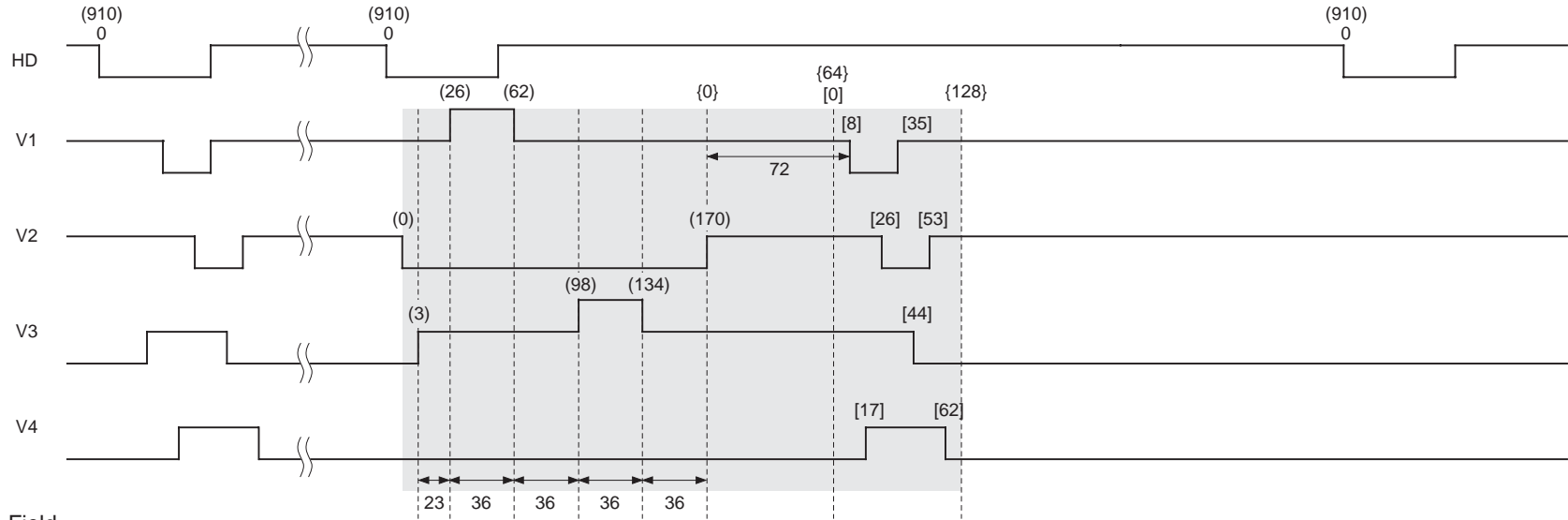


The shaded pulse area shifts while keeping the relative position relationship during shutter setting.

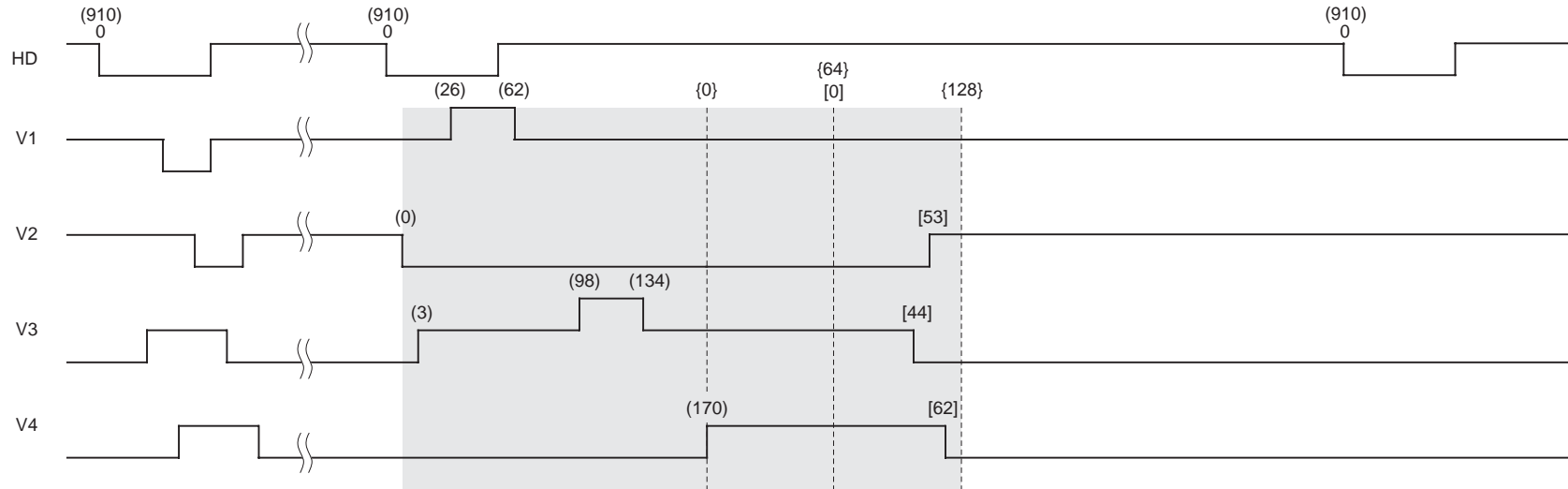
Horizontal Timing Chart MODESEL 6, 8 [760H NTSC]

Applicable CCD image sensor:
ICX228AK/258AK/278AK/408AK

Odd Field



Even Field

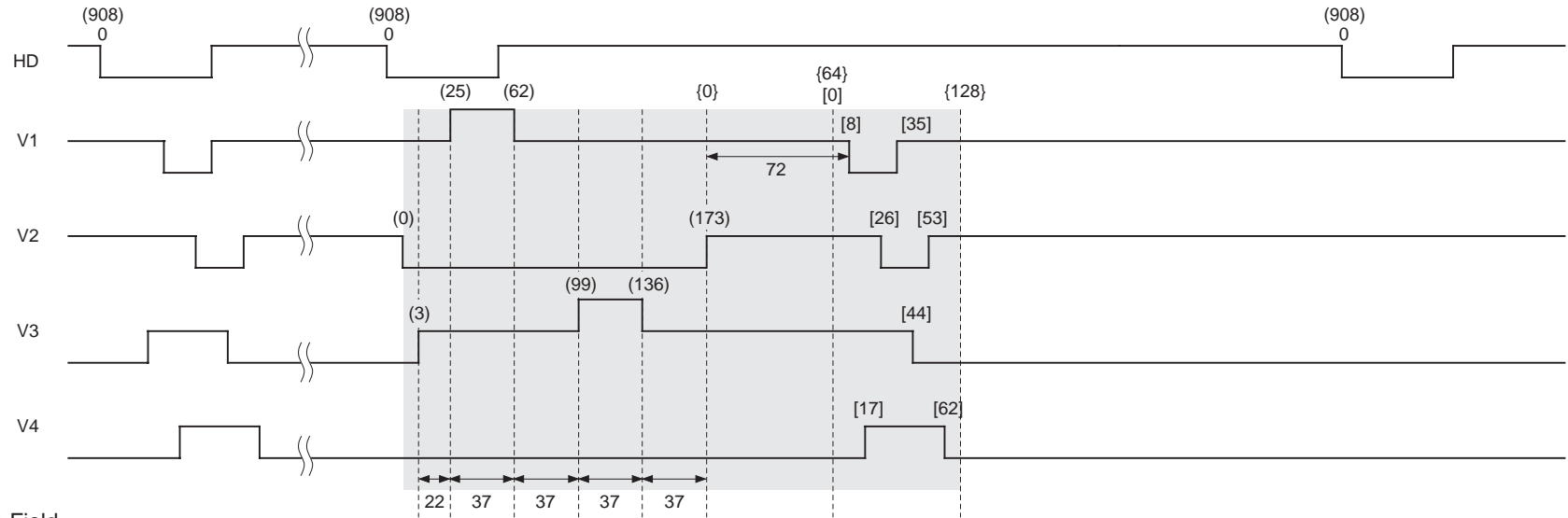


The shaded pulse area shifts while keeping the relative position relationship during shutter setting.

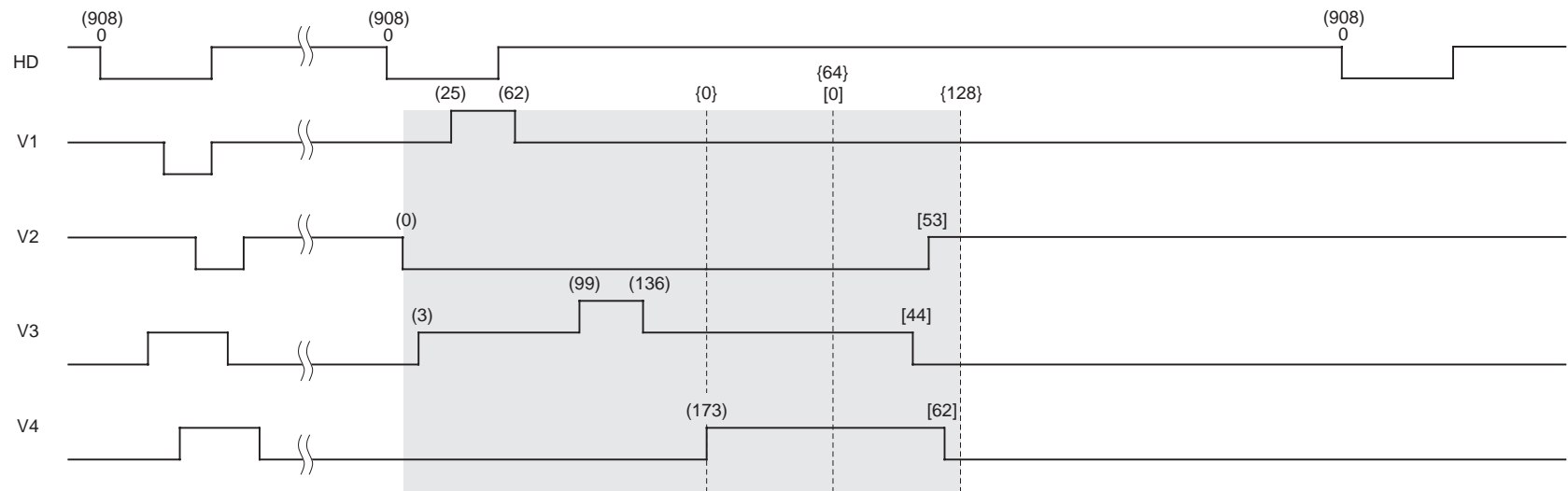
Horizontal Timing Chart MODESEL 9, A, B [760H PAL]

Applicable CCD image sensor:
ICX229AK/259AK/279AK/409AK

Odd Field



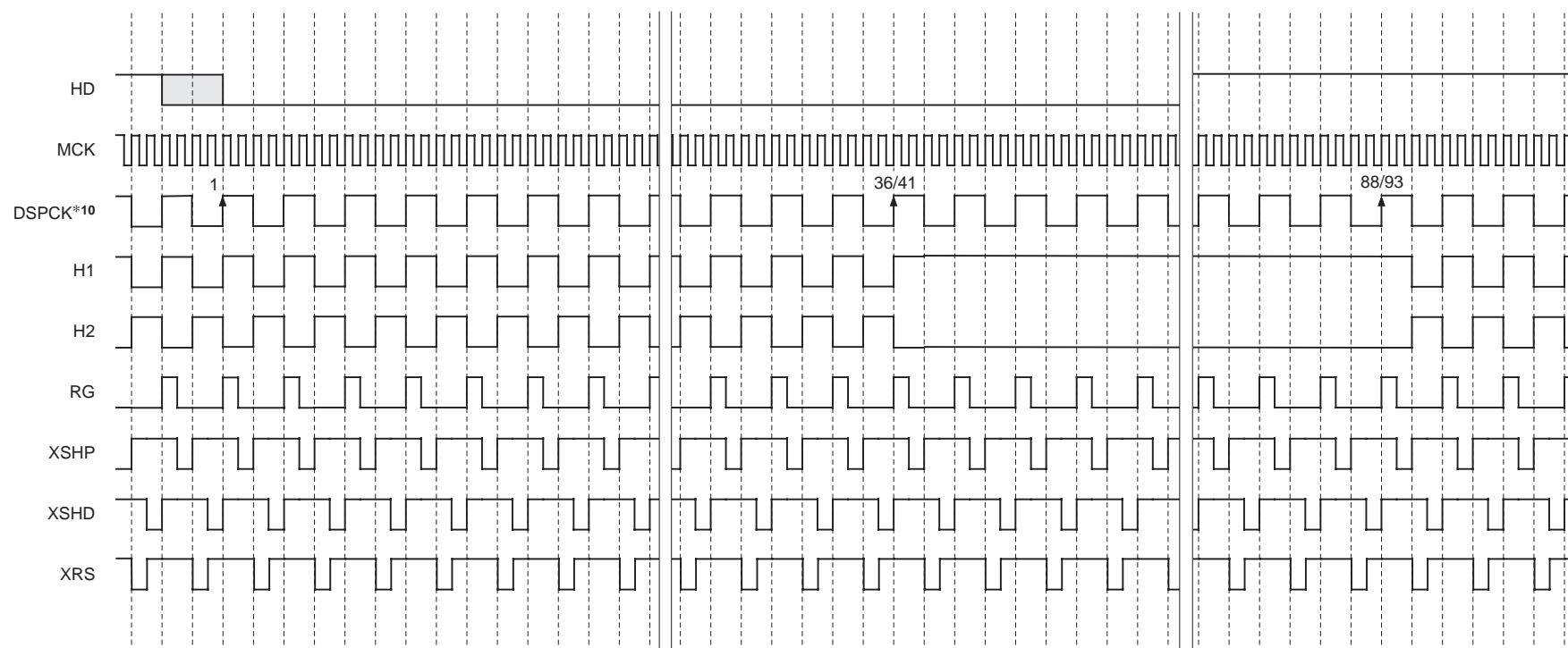
Even Field



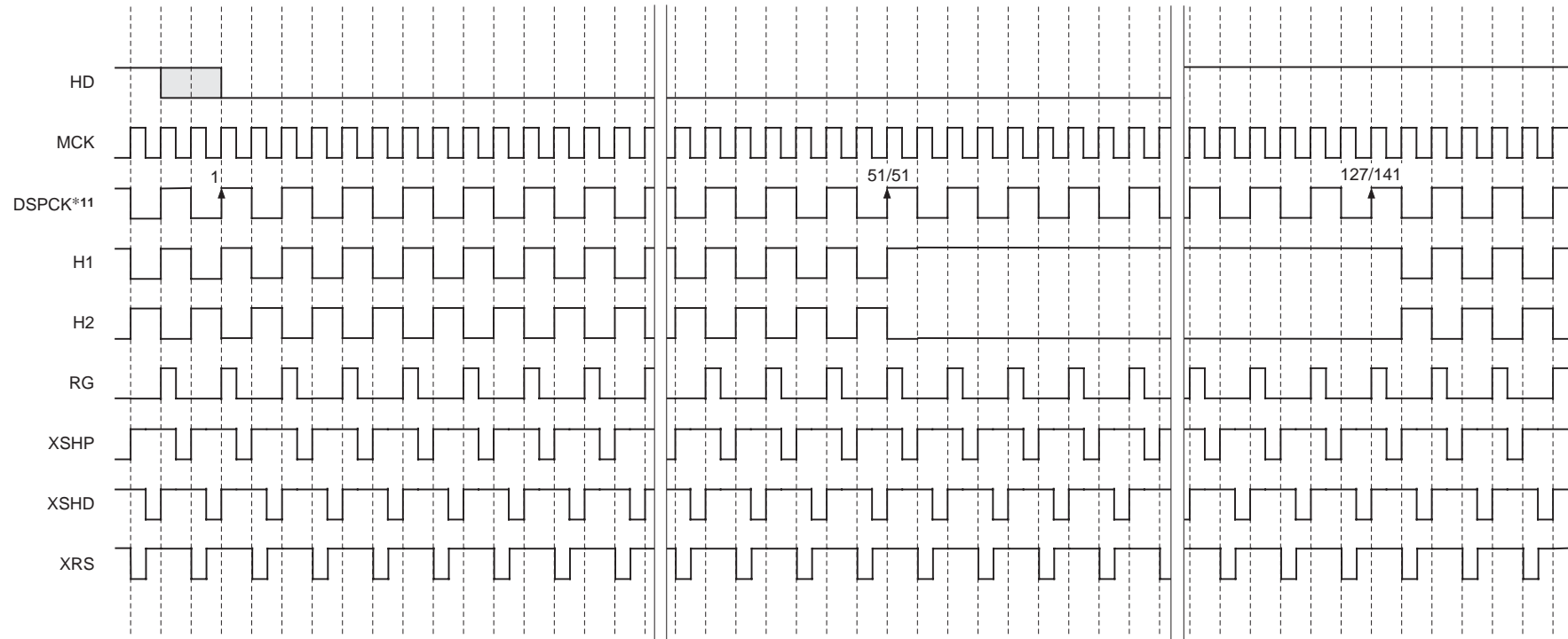
The shaded pulse area shifts while keeping the relative position relationship during shutter setting.

High-speed waveform pulse MODESEL 0, 1, 2, 3, 4, 5 [510H NTSC/PAL]

Applicable CCD image sensor:
 ICX206AK/226AK/254AK/404AK
 ICX207AK/227AK/255AK/405AK



*10 DSPCK is a clock which is not output by external pins. See the previously described table (Relationship between MODESEL and Each Clock).
 * The phase relationship of each pulse shows the logical position relationship. For the actual output, a delay is added to each pulse.
 * High-speed pulse pin setting shown above indicates the state of initial setting (delay, duty) of the parameter (Category 6: TG)



*11 DSPCK is a clock which is not output by external pins. See the previously described table (Relationship between MODESEL and Each Clock).
 * The phase relationship of each pulse shows the logical position relationship. For the actual output, a delay is added to each pulse.
 * High-speed pulse pin setting shown above indicates the state of initial setting (delay, duty) of the parameter (Category 6: TG)

Category 1: SYSCON

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
1	0	NTPAL	MODESEL control parameter TV system switching 0h: NTSC, 1h: PAL	0h	132h
	1	Fix	"0h" fix	0h	
	2	CCDTYPE	MODESEL control parameter CCD pixel number switching 0h: 510H, 1h: 760H	0h	
	3	Const	"0h" fix	0h	
	4	MIRROR	Mirror switching 0h: Normal, 1h: Mirror	0h	
	5	Fix	"0h" fix	0h	
	6	DEFON	Blemish compensation function ON/OFF 0h: off, 1h: on	1h	
	7	DYNDETON	Dynamic blemish detection mode switching 0h: off, 1h: on	0h	
2	0	DYNDELON	Dynamic incorrect blemish detection release mode switching 0h: off, 1h: on	0h	133h
	1	Fix	"0h" fix	0h	
	2				
	3				
	4				
	5	Const	"0h" fix	0h	
	6				
	7				
3	0	ENCMODE	MODESEL control parameter Encode mode selection 0h: 8fsc-encode, 1h: digital-encode, 2h: SG27-encode	1h	134h
	1				
	2	DACMODE	DAC Out mode selection (DAC1, DAC2) 0h: DAC1 = composite, DAC2 = unused, 1h: DAC1 = component Y, DAC2 = component C	0h	
	3	Const	"0h" fix	0h	
	4	TGCKSEL	SGMODE control parameter TG clock selector	0h	
	5				
	6	INTLCKSEL	MODESEL control parameter Internal clock selection	0h	
7					
4	0	Fix	"0h" fix	0h	135h
	1				
	2				
	3				
	4				
	5				
	6	DIFCKSEL	MODESEL control parameter Clock selector for YUV digital output 0h: ECK/2, 1h: ECK, 2h: MCK/2, 3h: MCK	0h	
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
5	0	SYNCLV	Sync level setting NTSC: 9h, PAL: 7h	9h	136h
	1				
	2				
	3				
	4				
	5	CCLPOFF	Chroma clipping switching signal 0h: on, 1h: off	0h	
	6	HILLIM	High luminance limit level setting 0h: off, 1h: 96%, 2h: 93%, 3h: 90%	0h	
7					
6	0	PEDLIM	Pedestal limit level setting 0h: -40 IRE, 1h: -25 IRE, 2h: -9 IRE, 3h: 7 IRE	0h	137h
	1				
	2	Fix	"0h" fix	0h	
	3	Fix	"1h" fix	1h	
	4	Fix	"3h" fix	3h	
	5				
	6	Fix	"0h" fix	0h	
	7				
7	0	Fix	"0h" fix	0h	138h
	1				
	2	Fix	"1h" fix	1h	
	3	PALSEQ	PAL field sequence (dedicated for VRHR mode) 0h: off, 1h: on	0h	
	4	YDSEL	Port[8:15]/ digital signal output switching 0h: port driver[15:8], 1h: Y/YUV out	0h	
	5	CDSEL	Port[0:7]/ REC601C signal output switching 0h: port driver[7:0], 1h: Cr out	0h	
	6	S0IN	SGMODE control parameter S0 pin I/O setting switching 0h: DHD OUT, 1h: VRI IN	1h	
	7	S1IN	SGMODE control parameter S1 pin I/O setting switching 0h: DVD OUT, 1h: HRI IN	0h	
8	0	S2SEL	SGMODE control parameter S2 pin I/O setting switching 0h: FSC comparison output, 1h: DHD, 2h: DVD, 3h: SYNC, 4h: TEST output, 5h: TEST output, 6h: FLD, 7h: LALT IN	0h	139h
	1				
	2				
	3	S3SEL	SGMODE control parameter S3 pin I/O setting switching 0h: DHD, 1h: DVD, 2h: HD, 3h: VD, 4h: NRYBY, 5h: TEST output, 6h: FLD, 7h: analog shift FSC input	0h	
	4				
	5				
	6	S4SEL	SGMODE control parameter S4 pin I/O setting switching 0h: chip select output for external EVR, 1h: FSC output, 2h: HD, 3h: VD	0h	
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
9	0	Fix	"0h" fix	0h	13Ah
	1				
	2				
	3				
	4				
	5				
	6				
	7				
10	0	Fix	"0h" fix	0h	13Bh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
11	0	Fix	"0h" fix	0h	13Ch
	1	PLLSTB	Internal PLL standby switching 0h: normal operation, 1h: standby	1h	
	2	YDACSTB	Y D/A converter (DAC1) standby switching 0h: normal operation, 1h: standby	0h	
	3	CDACSTB	C D/A convert (DAC2) standby switching 0h: normal operation, 1h: standby	0h	
	4	BSTSTB	Burst separator standby switching 0h: normal operation, 1h: standby	0h	
	5	Fix	"0h" fix	0h	
	6	VHOUT	VD/HD output setting switching 0h: port driver [6:5], 1h: P5 = VD, P6 = HD output	0h	
	7	ADCKINV	AD clock inversion control 0h: normal, 1h: inverted	0h	
12	0	ADCKDL	AD clock delay control 0h: 0ns, 1h: 5ns, 2h: 10ns, 3h: 15ns	0h	13Dh
	1				
	2	YDACCKSEL	SGMODE control parameter Clock select for YDAC and encoder part 0h: ECK, 1h: MCK	0h	
	3	Fix	"0h" fix	0h	
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
13	0	BSTLV	Burst level setting NTSC: 38h, PAL: 3Ah	38h	13Eh
	1				
	2				
	3				
	4				
	5				
	6				
	7	Fix	"0h" fix	0h	

Category 2: PICT1

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
1	0	Fix	"1h" fix	1h	13Fh
	1	Fix	"0h" fix	0h	
	2				
	3	YLPFOFF	Y LPF processing 0h: on, 1h: off	0h	
	4	HAPGH	H-aperture correction (high band) gain adjustment 0h: ×0, 1h: ×1, 2h: ×2, 3h: ×4	2h	
	5				
	6				
7	HAPGL	H-aperture correction (low band) gain adjustment 0h: ×0, 1h: ×1/2, 2h: ×1, 3h: ×2	2h		
2	0	VAPG	V-aperture correction gain adjustment 0h to Fh: ×0 to ×1	Ah	140h
	1				
	2				
	3				
	4	VAPSL	V-aperture correction slice level setting Max. (7h): max., Min. (0h): off	2h	
	5				
	6				
7	VLMHOFF	VAPLMHTH enabled/disabled 0h: on, 1h: off	0h		
3	0	VLMVOFF	VAPLMVTH enabled/disabled 0h: on, 1h: off	0h	141h
	1	VAPLMHTH	Luminance threshold value of vertical aperture correction limiter 0h: 75%, 1h: 81%, 2h: 88%, 3h: 94%	2h	
	2				
	3	VAPLMVTH	Up/down signal luminance difference threshold value of vertical aperture correction limiter 0h: 28%, 1h: 31%, 2h: 38%, 3h: 41%	1h	
	4				
	5	VAPLMON	Vertical aperture correction limiter 0h: off, 1h: on	0h	
	6	Fix	"0h" fix	0h	
7					
4	0	Fix	"0h" fix	0h	142h
	1				
	2	VHAPG	V-aperture correction gain adjustment 0h to Fh: ×0 to ×2	6h	
	3				
	4				
	5				
	6	HLAPG	Highlight aperture correction total gain 0h: ×0, 1h: ×1/4, 2h: ×1/2, 3h: ×1	2h	
7					

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
5	0	VHAPSL	Aperture correction slice level setting 0h to Fh: min. to max.	4h	143h
	1				
	2				
	3	HLAPTHLV	Highlight aperture correction added threshold level selection 0h: >50%, 1h: >37.5%, >25%, >12.5%	3h	
	4				
	5	HLAPPG	Highlight aperture correction +Gain	3h	
	6				
7					
6	0	HLAPMG	Highlight aperture correction -Gain	3h	144h
	1				
	2	HLAPDS	Highlight aperture correction reference level detection point selection. 0h: before gamma, 1h: after gamma	0h	
	3	HLAPSL	Highlight aperture correction slice level setting	4h	
	4				
	5				
	6				
7	YGAMSON	Luminance signal gamma low level signal compression 0h: off, 1h: on	0h		
7	0	YGAMSLV	Luminance signal gamma low level signal compression level switching 0h: ×1, 1h: ×1/2	0h	145h
	1	YGAMSMTH	Gamma curve smooth processing 0h: off, 1h: on	0h	
	2	YGAMSEL	Luminance signal variable gamma setting	4h	
	3				
	4				
	5	YKNEESEL	Luminance signal variable knee setting	3h	
	6				
7					
8	0	YGAIN	Luminance signal gain setting 0h to FFh: ×0 to ×2	80h	146h
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
9	0	YSPRSTH	Highlight suppression threshold value selection 0h: 100 IRE, 1h: 105 IRE, 2h: 111 IRE, 3h: 116 IRE	0h	147h
	1				
	2	YSPRSLV	Gain control value with suppression 0h: $\times 1$, 1h: $\times 1/2$, 2h: $\times 1/4$, 3h: $\times 0$	0h	
	3				
	4	WCLIP	White clip level setting 0h: 78%, 1h: 89%, 2h: 100%, 3h: 105%, 4h: 111%, 5h: 116%, 6h: 122%, 7h: 153% (Max.)	6h	
	5				
	6				
7	Fix	"0h" fix	0h		
10	0	SETUP	Setup level setting 0h to 3Fh: 0 IRE to 39.5 IRE (6-bit gradation) (Ch: 7.5 IRE)	Ch	148h
	1				
	2				
	3				
	4				
	5				
	6	POSNEG	Positive/negative inversion 0h: positive, 1h: negative	0h	
7	DEON	Detail enhancer 0h: off, 1h: on	1h		
11	0	DELVSEL	Emphasis level of detail enhancer 0h: weak, 1h: strong	0h	149h
	1	YDLY	System delay adjustment of Y main signal (0 to 15)	5h	
	2				
	3				
	4				
	5	Const	"1h" fix	1h	
	6				
7					
12	0	RETGAIN	Gain setting of aperture correction for high resolution mode 0h to 7h: 0 to Max.	1h	14Ah
	1				
	2				
	3	REON	ON/OFF of high resolution mode 0h: off, 1h: on	0h	
	4	Const	"1h" fix	1h	
	5				
	6	Const	"3h" fix	3h	
7					

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
13	0	Const	"1h" fix	1h	14Bh
	1				
	2				
	3				
	4	Const	"1h" fix	1h	
	5				
	6				
	7				
14	0	Const	"4h" fix	4h	14Ch
	1				
	2				
	3				
	4	Const	"Fh" fix	Fh	
	5				
	6				
	7				
15	0	Const	"Fh" fix	Fh	14Dh
	1				
	2				
	3				
	4				
	5				
	6	Const	"0h" fix	0h	
	7	Fix	"0h" fix	0h	
16	0	Const	"FFh" fix	FFh	14Eh
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
17	0	Const	"3h" fix	3h	14Fh
	1				
	2	Const	"3h" fix	3h	
	3				
	4	Const	"3h" fix	3h	
	5				
	6	Const	"3h" fix	3h	
7					
18	0	Const	"0h" fix	0h	150h
	1	Const	"8h" fix	8h	
	2				
	3				
	4				
	5				
	6				
7	Const	"0h" fix	0h		
19	0	MIRRST	Mirror reset timing adjustment (Uses when MIRROR = 1h)	0h	151h
	1				
	2				
	3				
	4				
	5				
	6				
7					
20	0	CLS1RL	Complementary color pixel clip level S1R (LSB)	FFh	152h
	1				
	2				
	3				
	4				
	5				
	6				
7					

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
21	0	CLS1BL	Complementary color pixel clip level S1B (LSB)	FFh	153h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
22	0	CLS2RL	Complementary color pixel clip level S2R (LSB)	FFh	154h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
23	0	CLS2BL	Complementary color pixel clip level S2B (LSB)	FFh	155h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
24	0	HLEDLEVL	Highlight edge color correction (Edge detection threshold value parameter) (LSB)	0h	156h
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
25	0	CLS1RM	Complementary color pixel clip level S1R (MSB)	7h	157h
	1				
	2				
	3	Fix	"0h" fix	0h	
	4	CLS1BM	Complementary color pixel clip level S1B (MSB)	7h	
	5				
	6				
	7	Fix	"0h" fix	0h	
26	0	CLS2RM	Complementary color pixel clip level S2R (MSB)	7h	158h
	1				
	2				
	3	Fix	"0h" fix	0h	
	4	CLS2BM	Complementary color pixel clip level S2B (MSB)	7h	
	5				
	6				
	7	Fix	"0h" fix	0h	
27	0	HLEDLEVM	Highlight edge color correction (Edge detection threshold value parameter) (MSB)	0h	159h
	1				
	2				
	3	Const	"0h" fix	0h	
	4	HLEDDL13	Highlight edge color correction DL13 line horizontal edge detection 0h: off, 1h: on	0h	
	5	HLEDDL2	Highlight edge color correction DL2 line horizontal edge detection 0h: off, 1h: on	0h	
	6	HLEDV	Highlight edge color correction (V-edge detection) 0h: off, 1h: on	0h	
	7	HLESICH	Correction data selection 0h: achromaticity, 1h: CCD data	0h	
28	0	Fix	"0h" fix	0h	15Ah
	1				
	2				
	3				
	4	Const	"0h" fix	0h	
	5	CDLY	Chroma signal system delay adjustment 0h to 3h: 0 to +3 pixels delay, 4h to 7h: -4 to -1 pixels delay	0h	
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
29	0	HLEPASE	Highlight edge color correction (Detection signal delay setting) 0h to 2h: 0 to 2 pixels delay	0h	15Bh
	1				
	2	HLEWID	Highlight edge color correction (Detection signal width setting) 0h to 7h: 1 to 8 pixels	0h	
	3				
	4				
	5	CLSOFF	Complementary color pixel clip function OFF 0h: on, 1h: off	1h	
	6	CKNEE2	Chroma knee area high luminance side bending (Effective when CKNEE = 3h) 0h: without bending, 1h: with bending	1h	
7	RBQUADON	Four-quadrant independent control switching of GAIN/HUE 0h: simultaneous four quadrants, 1h: independent	0h		
30	0	CGAMMA	Chroma variable gamma parameter 0h to 7h: gamma small (line) to gamma large	4h	15Ch
	1				
	2				
	3	CKNCLIP	Chroma knee area clip level setting 0h: no clipping, 1h: large, 2h: middle, 3h: small	0h	
	4				
	5	CKNEE	Chroma variable knee parameter	3h	
	6				
7					
31	0	RMATY	Primary color separation matrix constant for Red (complementary color mosaic) $\times -0.5$ (80h) to $\times 0.0$ (00) to $\times 0.5$ (7Fh)	21h	15Dh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
32	0	RMATC	Primary color separation matrix constant for Red (complementary color mosaic) $\times -0.5$ (80h) to $\times 0.0$ (00) to $\times 0.5$ (7Fh)	F9h	15Eh
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
33	0	BMATY	Primary color separation matrix constant for Blue (complementary color mosaic) ×-0.5 (80h) to ×0.0 (00) to ×0.5 (7Fh)	2Ch	15Fh
	1				
	2				
	3				
	4				
	5				
	6				
34	0	BMATC	Primary color separation matrix constant for Blue (complementary color mosaic) ×-0.5 (80h) to ×0.0 (00) to ×0.5 (7Fh)	E0h	160h
	1				
	2				
	3				
	4				
	5				
	6				
35	0	GMATCR	Primary color separation matrix constant for Green (complementary color mosaic) 0h to FFh: ×0.0 to ×2.0	80h	161h
	1				
	2				
	3				
	4				
	5				
	6				
36	0	GMATCB	Primary color separation matrix constant for Green (complementary color mosaic) 0h to FFh: ×0.0 to ×2.0	80h	162h
	1				
	2				
	3				
	4				
	5				
	6				
7					

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
37	0	RYGAIN1	First quadrant, R-Y gain x-1 (80h) to x0 (00) to x1 (7Fh)	20h	163h
	1				
	2				
	3				
	4				
	5				
	6				
38	0	BYGAIN1	First quadrant, B-Y gain x-1 (80h) to x0 (00) to x1 (7Fh)	11h	164h
	1				
	2				
	3				
	4				
	5				
	6				
39	0	RYHUE1	First quadrant, R-Y hue x-1 (80h) to x0 (00) to x1 (7Fh)	D0h	165h
	1				
	2				
	3				
	4				
	5				
	6				
40	0	BYHUE1	First quadrant, B-Y hue x-1 (80h) to x0 (00) to x1 (7Fh)	FFh	166h
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
41	0	RYGAIN2	Second quadrant, R-Y gain x-1 (80h) to x0 (00) to x1 (7Fh)	20h	167h
	1				
	2				
	3				
	4				
	5				
	6				
42	0	BYGAIN2	Second quadrant, B-Y gain x-1 (80h) to x0 (00) to x1 (7Fh)	11h	168h
	1				
	2				
	3				
	4				
	5				
	6				
43	0	RYHUE2	Second quadrant, R-Y hue x-1 (80h) to x0 (00) to x1 (7Fh)	D0h	169h
	1				
	2				
	3				
	4				
	5				
	6				
44	0	BYHUE2	Second quadrant, B-Y hue x-1 (80h) to x0 (00) to x1 (7Fh)	F8h	16Ah
	1				
	2				
	3				
	4				
	5				
	6				
7					

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
45	0	RYGAIN3	Third quadrant, R-Y gain x-1 (80h) to x0 (00) to x1 (7Fh)	20h	16Bh
	1				
	2				
	3				
	4				
	5				
	6				
46	0	BYGAIN3	Third quadrant, B-Y gain x-1 (80h) to x0 (00) to x1 (7Fh)	11h	16Ch
	1				
	2				
	3				
	4				
	5				
	6				
47	0	RYHUE3	Third quadrant, R-Y hue x-1 (80h) to x0 (00) to x1 (7Fh)	D0h	16Dh
	1				
	2				
	3				
	4				
	5				
	6				
48	0	BYHUE3	Third quadrant, B-Y hue x-1 (80h) to x0 (00) to x1 (7Fh)	F8h	16Eh
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
49	0	RYGAIN4	Fourth quadrant, R-Y gain x-1 (80h) to x0 (00) to x1 (7Fh)	20h	16Fh
	1				
	2				
	3				
	4				
	5				
	6				
50	0	BYGAIN4	Fourth quadrant, B-Y gain x-1 (80h) to x0 (00) to x1 (7Fh)	11h	170h
	1				
	2				
	3				
	4				
	5				
	6				
51	0	RYHUE4	Fourth quadrant, R-Y hue x-1 (80h) to x0 (00) to x1 (7Fh)	D0h	171h
	1				
	2				
	3				
	4				
	5				
	6				
52	0	BYHUE4	Fourth quadrant, B-Y hue x-1 (80h) to x0 (00) to x1 (7Fh)	F8h	172h
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
53	0	CSVLV	Chroma suppress level by V-aperture correction detection 0: no suppress, 1: 50% suppress, 2: 75% suppress, 3: 100% suppress	0h	173h
	1				
	2	CSVTH	Threshold value of CSVLV (detection level for D range of V-aperture) 0h: 25%, 1h: 44%, 2h: 63%, 3h: 81%	1h	
	3				
	4	CSHLV	Chroma suppress level by luminance detection 0: no suppress, 1: 50% suppress, 2: 75% suppress, 3: 100% suppress	0h	
	5				
	6	CSHTH	Threshold value of CSHLV (detection level for A/D converter input range) 0h: 75%, 1h: 81%, 2h: 88%, 3h: 94%	2h	
7					
54	0	6DBDWN	6dB minus gain	0h	174h
	1	LYLVB	CCD carrier level balance ON for Y main signal 0h: off, 1h: on	0h	
	2	Const	"0h" fix	0h	
	3	YLBON	Luminance reproduction level balance ON switch 0h: off, 1h: on	0h	
	4	YLBPLY	Delay adjustment for luminance reproduction level balance 0h: -2, 1h: -1, 2h: 0, 3h: +1	2h	
	5				
	6	Fix	"0h" fix	0h	
7					
55	0	BLACKS1	Digital clamp black level setting (S1)	80h	175h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
56	0	BLACKS2	Digital clamp black level setting (S2)	80h	176h
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
57	0	LGS1R	S1R gain for CCD carrier level balance ($\times 0.0$ to $\times 4.0$) 0h to FFh: $\times 0.0$ to $\times 4.0$	40h	177h
	1				
	2				
	3				
	4				
	5				
	6				
58	0	LGS2R	S2R gain for CCD carrier level balance ($\times 0.0$ to $\times 4.0$) 0h to FFh: $\times 0.0$ to $\times 4.0$	40h	178h
	1				
	2				
	3				
	4				
	5				
	6				
59	0	LGS1B	S1B gain for CCD carrier level balance ($\times 0.0$ to $\times 4.0$) 0h to FFh: $\times 0.0$ to $\times 4.0$	40h	179h
	1				
	2				
	3				
	4				
	5				
	6				
60	0	LGS2B	S2B gain for CCD carrier level balance ($\times 0.0$ to $\times 4.0$) 0h to FFh: $\times 0.0$ to $\times 4.0$	40h	17Ah
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
61	0	YLGS1R	S1R gain for luminance reproduction level balance ($\times -4.0$ to $\times 4.0$)	0h	17Bh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
62	0	YLGS2R	S2R gain for luminance reproduction level balance ($\times -4.0$ to $\times 4.0$)	0h	17Ch
	1				
	2				
	3				
	4				
	5				
	6				
	7				
63	0	YLGS1B	S1B gain for luminance reproduction level balance ($\times -4.0$ to $\times 4.0$)	0h	17Dh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
64	0	YLGS2B	S2B gain for luminance reproduction level balance ($\times -4.0$ to $\times 4.0$)	0h	17Eh
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Category 3: AE1

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
1	0	Fix	"Fh" fix	0h	17Fh
	1				
	2				
	3				
	4	Fix	"0h" fix	0h	
	5				
	6				
	7				
2	0	Fix	"0h" fix	0h	180h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
3	0	Fix	"0h" fix	0h	181h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
4	0	Fix	"0h" fix	0h	182h
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
5	0	Fix	"0h" fix	0h	183h
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Category 4: AWB1

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
1	0	WBR	White balance gain R	37h	184h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
2	0	WBG	White balance gain G	26h	185h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
3	0	WBB	White balance gain B	39h	186h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
4	0	WBGAINX2	White balance gain is doubled 0h: normal, 1h: double	0h	187h
	1	Fix	"0h" fix	0h	
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
5	0	Fix	"0h" fix	0h	188h
	1				
	2	AWBWON	AWB detection frame selection 0h: Window0 to 4, 1h: AWB target window	0h	
	3	Fix	"0h" fix	0h	
	4				
	5				
	6				
7					
6	0	WBYUP	Luminance specific integration range (upper limit) 0h to FFh	0h	189h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
7	0	WBYDWN	Luminance specific integration range (lower limit) 0h to FFh	0h	18Ah
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Category 5: OPDWND1

The parameters in CAT5 (OPDWND1) is not opened.

* The parameters in CAT5 (OPDWND1) is overwritten by those in CAT16 (OPDWND2).

Category 6: TG

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
1	0	Fix	"0h" fix	0h	18Bh
	1	SHUT	Shutter pulse on/off 0h: off, 1h: on	1h	
	2	Fix	"0h" fix	0h	
	3				
	4				
	5				
	6				
	7				
2	0	ODSGV	SG position V direction line setting [ODD]	2h	18Ch
	1				
	2				
	3				
	4	EVSGV	SG position V direction line setting [EVEN]	2h	
	5				
	6				
	7				
3	0	ODSGH	SG position H direction setting [ODD]	0h	18Dh
	1				
	2				
	3				
	4				
	5				
	6				
	7	Fix	"0h" fix	0h	
4	0	EVSGH	SG position H direction setting [EVEN]	0h	18Eh
	1				
	2				
	3				
	4				
	5				
	6				
	7	Fix	"0h" fix	0h	

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
5	0	Fix	"0h" fix	0h	18Fh
	1				
	2				
	3				
	4				
	5				
	6				
	7	Fix	"0h" fix	0h	
6	0	ODSUBVL	Sub output V direction line setting [ODD] (LSB)	0h	190h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
7	0	EVSUBVL	Sub output V direction line setting [EVEN] (LSB)	0h	191h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
8	0	ODSUBVM	Sub output V direction line setting [ODD] (MSB)	0h	192h
	1	EVSUBVM	Sub output V direction line setting [EVEN] (MSB)	0h	
	2				
	3	Fix	"0h" fix	0h	
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
9	0	DEH1	H1 pulse delay adjustment 0h to 7h: 0ns to +7ns, 8h to Fh: -8ns to -1ns	3h	193h
	1				
	2				
	3	DEH2	H2 pulse delay adjustment 0h to 7h: 0ns to +7ns, 8h to Fh: -8ns to -1ns	3h	
	4				
	5				
	6				
10	7	DERG	RG pulse delay adjustment 0h to 7h: 0ns to +7ns, 8h to Fh: -8ns to -1ns	Eh	194h
	0				
	1				
	2	DESHP	XSHP pulse delay adjustment 0h to 7h: 0ns to +7ns, 8h to Fh: -8ns to -1ns	7h	
	3				
	4				
	5				
11	6	DESHD	XSHD pulse delay adjustment 0h to 7h: 0ns to +7ns, 8h to Fh: -8ns to -1ns	Ah	195h
	7				
	0				
	1	DERS	XRS pulse delay adjustment 0h to 7h: 0ns to +7ns, 8h to Fh: -8ns to -1ns	Ah	
	2				
	3				
	4				
12	5	DUH1	H1 pulse duty adjustment 0h to 7h: 0ns to +7ns (falling edge), 8h to Fh: 0ns to +7ns (rising edge)	Ah	196h
	6				
	7				
	0	DUH2	H2 pulse duty adjustment 0h to 7h: 0ns to +7ns (falling edge), 8h to Fh: 0ns to +7ns (rising edge)	Ah	
	1				
	2				
	3				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
13	0	DURG	RG pulse duty adjustment 0h to 7h: 0ns to +7ns (falling edge), 8h to Fh: 0ns to +7ns (rising edge)	8h	197h
	1				
	2				
	3	DUSHP	XSHP pulse duty adjustment 0h to 7h: 0ns to +7ns (falling edge), 8h to Fh: 0ns to +7ns (rising edge)	Ch	
	4				
	5				
	6				
14	7	DUSHD	XSHD pulse duty adjustment 0h to 7h: 0ns to +7ns (falling edge), 8h to Fh: 0ns to +7ns (rising edge)	Dh	198h
	0				
	1				
	2	DURS	XRS pulse duty adjustment 0h to 7h: 0ns to +7ns (falling edge), 8h to Fh: 0ns to +7ns (rising edge)	0h	
	3				
	4				
	5				
15	6	Fix	"0h" fix	0h	199h
	7				
	0				
	1	DRBH1	H1 drive capability adjustment 0h to 6h: max. to min., 7h: off	5h	
	2				
	3				
	16	4	DRBRG	RG drive capability adjustment 0h to 6h: max. to min., 7h: off	
5					
6					
7		Fix	"0h" fix	0h	

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
17	0	DRBSHD	XSHD drive capability adjustment 0h to 6h: max. to min., 7h: off	6h	19Bh
	1				
	2				
	3	DRBSHP	XSHP drive capability adjustment 0h to 6h: max. to min., 7h: off	6h	
	4				
	5				
	6	Fix	"0h" fix	0h	
7					
18	0	DRBXRS	XRS drive capability adjustment 0h to 6h: max. to min., 7h: off	6h	19Ch
	1				
	2				
	3	Fix	"0h" fix	0h	
	4				
	5				
	6				
	7				

Category 7: EXTSYNC1

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
1	0	INTEXT	SGMODE control parameter	0h	19Dh
	1	HVPLL	SGMODE control: H/V-PLL selector (Equivalent to EXSTAT signal)	0h	
	2	SYNCEX	SGMODE control parameter	0h	
	3	HRSTON	SGMODE control parameter	0h	
	4	VRSTON	SGMODE control parameter	0h	
	5	BCMPON	SGMODE control parameter	0h	
	6	INT60	SGMODE control parameter	0h	
	7	SG27	MODESEL control parameter	0h	
2	0	HDETOFF	Invalidates external horizontal sync signal detection 0h: validated, 1h: invalidated	0h	19Eh
	1	VDETOFF	Invalidates external vertical sync signal detection 0h: validated, 1h: invalidated	0h	
	2	SDETOFF	Invalidates external sync signal detection 0h: validated, 1h: invalidated	0h	
	3	BDETOFF	Invalidates burst signal detection 0h: validated, 1h: invalidated	0h	
	4	PCMPINV	Phase comparator input ref/var inverted 0h: normal, 1h: inverted	0h	
	5	Fix	"0h" fix	0h	
	6	WBSTON	Wide burst function ON 0h: off, 1h: on	0h	
	7	Fix	"0h" fix	0h	
3	0	SFTHL	Horizontal direction phase adjustment value/counter load value setting SFTH (LSB)	0h	19Fh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
4	0	SFTVL	Vertical direction phase adjustment value/counter load value setting SFTV (LSB)	E0h	1A0h
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address	
5	0	SFTHM	SFTH (MSB)	0h	1A1h	
	1					
	2	SFTVM	SFTV (MSB)			1h
	3					
	4	Fix	"0h" fix			0h
	5					
	6					
7	Fix	"1h" fix	1h			
6	0	DZHWID	H reset dead band width setting (0 to $\pm 15\text{clk}$)	1h	1A2h	
	1					
	2					
	3					
	4	DZVWID	V reset dead band width setting (0 to $\pm 7.5\text{H}$)			
	5					
	6					
	7					
7	0	WBHSETL	Horizontal start position of wide burst WBHSET (LSB)	0h	1A3h	
	1					
	2					
	3					
	4					
	5					
	6					
	7					
8	0	WBHRSTL	Horizontal end position of wide burst WBHRST (LSB)	0h	1A4h	
	1					
	2					
	3					
	4					
	5					
	6					
	7					

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
9	0	WBHSETM	WBHSET (MSB)	0h	1A5h
	1				
	2	WBHRSTM	WBHRST (MSB)	0h	
	3				
	4	BSGSFTST	Gate pulse start position adjustment for VBS (-7 to +8)	7h	
	5				
	6				
7					
10	0	BSGSFTED	Gate pulse end position adjustment for VBS (-7 to +8)	7h	1A6h
	1				
	2				
	3				
	4	SCGWCTRL	Gate width adjustment for VBS (-7 to +8)	3h	
	5				
	6				
7					
11	0	PCMPSEP	SGMODE control parameter	0h	1A7h
	1	SSEPEXT	SGMODE control parameter	0h	
	2	Fix	"0h" fix	0h	
	3				
	4				
	5				
	6				
	7				

Category 8: FEADJ (EVRI)

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
1	0	Fix	"0h" fix	0h	1A8h
	1				
	2				
	3				
	4				
	5				
	6	ADJUST	Camera adjustment signal output	0h	
7	DISPMIX	Y, R-Y and B-Y sampling point display (outputs to YOUTL/M, RYOUTL/M, BYOUTL/M)	0h		
2	0	SYSDLY	Horizontal direction delay value adjustment 510H: Dh, 760H: Fh	Bh	1A9h
	1				
	2				
	3				
	4	ADJSTMK	AD sampling point display (outputs to AJSTOUTL/M)	0h	
	5	EVR0STB	EVR0 standby control 0h: Normal, 1h: standby	0h	
	6	EVR1STB	EVR1 standby control 0h: Normal, 1h: standby	0h	
	7	EVR2STB	EVR2 standby control 0h: Normal, 1h: standby	0h	
3	0	AGCCNT	AGG gain control (EVR0) (User release when AEHOLD = 1h)	1Eh	1AAh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
4	0	IRISVCNT	MIRIS control (EVR1) (User release when MIRIS = 0h or AEHOLD = 1h)	FFh	1ABh
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
5	0	EVR2CNT	Output voltage setting in EVR2	FFh	1ACh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
6	0	Fix	"0h" fix	0h	1ADh
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Category 9: MASKPG

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
1	0	MSK0HSET	Horizontal start position of MASK 0 (4-pixel units) It also serves as sampling horizontal position.	2h	1AEh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
2	0	MSK1HSET	Horizontal start position of MASK 1 (4-pixel units)	2h	1AFh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
3	0	MSK2HSET	Horizontal start position of MASK 2 (4-pixel units)	2h	1B0h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
4	0	MSK3HSET	Horizontal start position of MASK 3 (4-pixel units)	2h	1B1h
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
5	0	MSK4HSET	Horizontal start position of MASK 4 (4-pixel units)	2h	1B2h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
6	0	MSK5HSET	Horizontal start position of MASK 5 (4-pixel units)	2h	1B3h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
7	0	MSK6HSET	Horizontal start position of MASK 6 (4-pixel units)	2h	1B4h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
8	0	MSK7HSET	Horizontal start position of MASK 7 (4-pixel units)	2h	1B5h
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
9	0	MSK0HRST	Horizontal end position of MASK 0 (4-pixel units)	2h	1B6h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
10	0	MSK1HRST	Horizontal end position of MASK 1 (4-pixel units)	2h	1B7h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
11	0	MSK2HRST	Horizontal end position of MASK 2 (4-pixel units)	2h	1B8h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
12	0	MSK3HRST	Horizontal end position of MASK 3 (4-pixel units)	2h	1B9h
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
13	0	MSK4HRST	Horizontal end position of MASK 4 (4-pixel units)	2h	1BAh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
14	0	MSK5HRST	Horizontal end position of MASK 5 (4-pixel units)	2h	1BBh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
15	0	MSK6HRST	Horizontal end position of MASK 6 (4-pixel units)	2h	1BCh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
16	0	MSK7HRST	Horizontal end position of MASK 7 (4-pixel units)	2h	1BDh
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
17	0	MSK0VSET	Vertical start position of MASK 0 (4 line units) It also serves as sampling vertical position.	0h	1BEh
	1				
	2				
	3				
	4				
	5				
	6				
18	0	MSK1VSET	Vertical start position of MASK 1 (4 line units)	0h	1BFh
	1				
	2				
	3				
	4				
	5				
	6				
19	0	MSK2VSET	Vertical start position of MASK 2 (4 line units)	0h	1C0h
	1				
	2				
	3				
	4				
	5				
	6				
20	0	MSK3VSET	Vertical start position of MASK 3 (4 line units)	0h	1C1h
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
21	0	MSK4VSET	Vertical start position of MASK 4 (4 line units)	0h	1C2h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
22	0	MSK5VSET	Vertical start position of MASK 5 (4 line units)	0h	1C3h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
23	0	MSK6VSET	Vertical start position of MASK 6 (4 line units)	0h	1C4h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
24	0	MSK7VSET	Vertical start position of MASK 7 (4 line units)	0h	1C5h
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
25	0	MSK0VRST	Vertical end position of MASK 0 (4 line units)	0h	1C6h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
26	0	MSK1VRST	Vertical end position of MASK 1 (4 line units)	0h	1C7h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
27	0	MSK2VRST	Vertical end position of MASK 2 (4 line units)	0h	1C8h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
28	0	MSK3VRST	Vertical end position of MASK 3 (4 line units)	0h	1C9h
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
29	0	MSK4VRST	Vertical end position of MASK 4 (4 line units)	0h	1CAh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
30	0	MSK5VRST	Vertical end position of MASK 5 (4 line units)	0h	1CBh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
31	0	MSK6VRST	Vertical end position of MASK 6 (4 line units)	0h	1CCh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
32	0	MSK7VRST	Vertical end position of MASK 7 (4 line units)	0h	1CDh
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
33	0	MSKBYLV	B-Y setting value for MASK color -128 to +127	0h	1CEh
	1				
	2				
	3				
	4				
	5				
	6				
34	0	MSKRYLV	R-Y setting value for MASK color -128 to +127	0h	1CFh
	1				
	2				
	3				
	4				
	5				
	6				
35	0	MSKYLVL	Luminance level setting of MASK (LSB)	0h	1D0h
	1				
	2				
	3				
	4				
	5				
	6				
36	0	MSKYLVM	Luminance level setting of MASK (MSB)	0h	1D1h
	1	MSKON	MASK function switch 0h: off, 1h: on	1h	
	2	MSKHLD	Uses background color as master color 0h: off, 1h: on	0h	
	3	PGON	Test pattern generator switching 0h: off, 1h: on	0h	
	4	Fix	"0h" fix	0h	
	5	Fix	"1h" fix	1h	
	6	PGGAIN	PG data gain adjustment 0h: ×0, 1h: ×1, 2h: ×2, 3h: ×4	2h	
7					

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
37	0	PGPAT	PG pattern switching 0h: color bar, 1h: raster, 2h: impulse, 3h: serial setting	0h	1D2h
	1				
	2	PGHV	PG pattern direction setting 0h: horizontal, 1h: vertical	0h	
	3	PGRION	Ramp/inversion of impulse effect addition for PG pattern 0h: normal, 1h: addition	0h	
	4	PGRWMIX	Outputs CCD signal except PG area 0h: full-screen PG, 1h: CCD signal output	0h	
	5	PGCOLOR	Color specification of raster setting 0h: white, 1h: yellow, 2h: cyan, 3h: green, 4h: magenta, 5h: red, 6h: blue, 7h: simple horizontal ramp	0h	
	6				
7					
38	0	MSKDLY	Delay adjustment of Y mask signal 0h to 7h: 0 to 7, 8h to Fh: -8 to -1	7h	1D3h
	1				
	2				
	3				
	4	MSKDLC	Delay adjustment of chroma mask signal 0h to 7h: 0 to 7, 8h to Fh: -8 to -1	8h	
	5				
	6				
7					
39	0	PGSDCRS2	Mg + Ye data	0h	1D4h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
40	0	PGSDCRS1	G + Cy data	0h	1D5h
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
41	0	PGSDCBS2	G + Ye data	0h	1D6h
	1				
	2				
	3				
	4				
	5				
	6				
42	0	PGSDCBS1	Mg + Cy data	0h	1D7h
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Category 10: DIF

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
1	0	RECOUT	Digital output switching 0h: straight, 1h: ITU-REC output	0h	1D8h
	1	DIF27	MODESEL control parameter	0h	
	2	DIFOUT	Digital format output switching 0h: output conforming to REC601, 1h: output conforming to REC656	0h	
	3	BLCKLV	Luminance black level variable 0h to 1Fh	10h	
	4				
	5				
	7				
2	0	EAVSTAL	EAV start position (LSB)	0h	1D9h
	1				
	2				
	3				
	4				
	5				
	7				
3	0	EAVSTAM	EAV start position (MSB)	0h	1DAh
	1				
	2				
	3	Fix	"0h" fix	0h	
	4				
	5				
	7				
4	0	SAVSTAL	SAV start position (LSB)	BBh	1DBh
	1				
	2				
	3				
	4				
	5				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
5	0	SAVSTAM	SAV start position (MSB)	0h	1DCh
	1				
	2				
	3	Fix	"0h" fix	0h	
	4				
	5				
	6				
7					
6	0	FLD1FSTA	F bit start position of ODD (Field1)	3h	1DDh
	1				
	2				
	3	FLD1VSTA	V bit start position of ODD (Field1)	12h	
	4				
	5				
	6				
7					
7	0	FLD2FSTA	F bit start position of EVEN (Field2)	3h	1DEh
	1				
	2				
	3	FLD2VSTA	V bit start position of EVEN (Field2)	13h	
	4				
	5				
	6				
7					
8	0	RECYGAIN	Digital Y gain setting 0h to FFh: ×0 to ×1.99	80h	1DFh
	1				
	2				
	3				
	4				
	5				
	6				
7					

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
9	0	RECRYGAIN	Digital R-Y gain setting 0h to FFh: ×0 to ×3.99	80h	1E0h
	1				
	2				
	3				
	4				
	5				
	6				
10	0	RECBYGAIN	Digital B-Y gain setting 0h to FFh: ×0 to ×3.99	80h	1E1h
	1				
	2				
	3				
	4				
	5				
	6				
11	0	Fix	"0h" fix	0h	1E2h
	1	TRCOFF	TRC code off 0h: on 1h: off (0 output only from EAV and SAV)	0h	
	2	BLKOFF	Blank code off 0h: on 1h: off (0 output)	0h	
	3	Fix	"0h" fix	0h	
	4				
	5				
	6				
	7				
12	0	Fix	"11h" fix	11h	1E3h
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address			
13	0	Fix	"FEh" fix	FEh	1E4h			
	1							
	2							
	3							
	4							
	5							
	6							
14	0	Fix	"0h" fix	0h	1E5h			
	1	Fix	"1h" fix	1h				
	2							
	3	DCKINV	DCK inversion control 0h: normal, 1h: inverted	0h				
	4							
	5					DCKDL	DCK delay adjustment 0h: 0ns, 1h: 5ns, 2h: 10ns, 3h: 15ns	0h
	6							
7	DIFON	DCK output ON/OFF control and digital signal processing operation 0h: off, 1h: operation (output)	0h					

Category 11: BLMDETS1

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
1	0	DEFPG	Quasi-blemish generation switching 0h: off, 1h: on	0h	1E9h
	1	DEFMK	Display of blemish marker 0h: off, 1h: on	0h	
	2	Fix	"0h" fix	0h	
	3	LARGEON	Large blemish detection mode 0h: off, 1h: on	0h	
	4	AREA	Display of blemish detection area 0h: off, 1h: on	0h	
	5	DETSPEC	Condition selection of Dynamic blemish detection 0h: all 10 pixels, 1h: 1 of 10 pixels isn't determined, 2h: 2 of 10 pixels aren't determined, 3h: 3 of 10 pixels aren't determined, 4h: 8 pixels from right/left or up/down, 5h: 1 of 8 pixels from right/left or up/down isn't determined, 6h: 4 pixels from right/left or up/down, 7h: horizontal 4 pixels	0h	
	6				
7					
2	0	DEFPGVLV	Quasi-blemish level setting (LSB)	0h	1EAh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
3	0	DEFPGVM	Quasi-blemish level setting (MSB)	0h	1EBh
	1	DEFNUM	Setting the number of compensation and marker 0h to 1Fh: 1 to 32	1Fh	
	2				
	3				
	4				
	5				
	6	dummy	"1h" fix	1h	
4	0	FLDWAIT	Number of waiting field in Dynamic detection (even field only) 2h to FFh: 2FLD to 254FLD	0h	1ECh
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
5	0	DETREFL	Threshold level of normal blemish detection (LSB) (common use of Dynamic and Static)	0h	1EDh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
6	0	DELREFL	Cancel level of Dynamic detection (LSB)	0h	1EEh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
7	0	LARGREFL	Threshold level of large blemish detection (LSB) (common use of Dynamic and Static)	0h	1EFh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
8	0	DARKREFL	Reference dark level of Dynamic detection (LSB)	0h	1F0h
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
9	0	DETREFM	Threshold level of normal blemish detection (MSB) (common use of Dynamic and Static)	0h	1F1h
	1				
	2	DELREFM	Cancel level of Dynamic detection (MSB)	0h	
	3				
	4	LARGREFM	Threshold level of large blemish detection (MSB) (common use of Dynamic and Static)	0h	
	5				
	6	DARKREFM	Reference dark level of Dynamic detection (MSB)	0h	
7					
10	0	IBLKS1L	S1 black level for Static detection (LSB)	0h	1F2h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
11	0	IBLKS2L	S2 black level for Static detection (LSB)	0h	1F3h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
12	0	IBLKS1M	S1 black level for Static detection (MSB)	0h	1F4h
	1	IBLKS2M	S2 black level for Static detection (MSB)	0h	
	2	DETACCT	Number of accumulation field in Static detection (even number only) 2h to 3Eh: 2FLD to 62FLD	0h	
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
13	0	HSRTL	Blemish detection area, horizontal start position setting (LSB)	0h	1F5h
	1				
	2				
	3				
	4				
	5				
	6				
14	0	VSRTL	Blemish detection area, vertical start position setting (LSB)	0h	1F6h
	1				
	2				
	3				
	4				
	5				
	6				
15	0	HWIDTHL	Blemish detection area, horizontal width setting (LSB)	FFh	1F7h
	1				
	2				
	3				
	4				
	5				
	6				
16	0	VWIDTHL	Blemish detection area, vertical width setting (LSB)	FFh	1F8h
	1				
	2				
	3				
	4				
	5				
	6				
7					

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
17	0	HSRTM	Blemish detection area, horizontal start position setting (MSB)	0h	1F9h
	1				
	2	VSRTM	Blemish detection area, vertical start position setting (MSB)	0h	
	3				
	4	HWIDTHM	Blemish detection area, horizontal width setting (MSB)	3h	
	5				
	6	VWIDTHM	Blemish detection area, vertical width setting (MSB)	3h	
7					
18	0	ADDRRST	Blemish address reset 1h: reset	0h	1FAh
	1	Fix	"0h" fix	0h	
	2				
	3				
	4				
	5				
	6				
	7				
19	0				Fix
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
20	0	DETLV00	Blemish level	0h	1FCh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
21	0	HCNT00	Horizontal address of blemish	0h	1FDh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
22	0	VCNT00	Vertical address of blemish	0h	1FEh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
23	0	LARGE00	Type of blemish 0h: normal, 1h: large blemish	0h	1FFh
	1				
	2				
	3				
	4				
	5				
	6	SD00	Type of blemish detection 0h: Static detection, 1h: Dynamic detection	0h	
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
24	0	DETLV01	Blemish level	0h	200h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
25	0	HCNT01	Horizontal address of blemish	0h	201h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
26	0	VCNT01	Vertical address of blemish	0h	202h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
27	0	VCNT01	Vertical address of blemish	0h	203h
	1				
	2				
	3				
	4				
	5				
	6	LARGE01	Type of blemish 0h: normal, 1h: large blemish	0h	
	7	SD01	Type of blemish detection 0h: Static detection, 1h: Dynamic detection	0h	

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
28	0	DETLV02	Blemish level	0h	204h
	1				
	2				
	3				
	4				
	5				
	6				
29	0	HCNT02	Horizontal address of blemish	0h	205h
	1				
	2				
	3				
	4				
	5				
	6				
30	0	VCNT02	Vertical address of blemish	0h	206h
	1				
	2				
	3				
	4				
	5				
	6				
31	0	VCNT02	Vertical address of blemish	0h	207h
	1				
	2				
	3				
	4				
	5				
	6	LARGE02	Type of blemish 0h: normal, 1h: large blemish	0h	
7	SD02	Type of blemish detection 0h: Static detection, 1h: Dynamic detection	0h		

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
32	0	DETLV03	Blemish level	0h	208h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
33	0	HCNT03	Horizontal address of blemish	0h	209h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
34	0	VCNT03	Vertical address of blemish	0h	20Ah
	1				
	2				
	3				
	4				
	5				
	6				
	7				
35	0	LARGE03	Type of blemish 0h: normal, 1h: large blemish	0h	20Bh
	1				
	2				
	3				
	4				
	5				
	6	SD03	Type of blemish detection 0h: Static detection, 1h: Dynamic detection	0h	

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
36	0	DETLV04	Blemish level	0h	20Ch
	1				
	2				
	3				
	4				
	5				
	6				
	7				
37	0	HCNT04	Horizontal address of blemish	0h	20Dh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
38	0	VCNT04	Vertical address of blemish	0h	20Eh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
39	0	LARGE04	Type of blemish 0h: normal, 1h: large blemish	0h	20Fh
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
40	0	DETLV05	Blemish level	0h	210h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
41	0	HCNT05	Horizontal address of blemish	0h	211h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
42	0	VCNT05	Vertical address of blemish	0h	212h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
43	0	LARGE05	Type of blemish 0h: normal, 1h: large blemish	0h	213h
	1				
	2				
	3				
	4				
	5				
	6	SD05	Type of blemish detection 0h: Static detection, 1h: Dynamic detection	0h	
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
44	0	DETLV06	Blemish level	0h	214h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
45	0	HCNT06	Horizontal address of blemish	0h	215h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
46	0	VCNT06	Vertical address of blemish	0h	216h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
47	0	VCNT06	Vertical address of blemish	0h	217h
	1				
	2				
	3				
	4				
	5				
	6	LARGE06	Type of blemish 0h: normal, 1h: large blemish	0h	
	7	SD06	Type of blemish detection 0h: Static detection, 1h: Dynamic detection	0h	

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
48	0	DETLV07	Blemish level	0h	218h
	1				
	2				
	3				
	4				
	5				
	6				
49	0	HCNT07	Horizontal address of blemish	0h	219h
	1				
	2				
	3				
	4				
	5				
	6				
50	0	VCNT07	Vertical address of blemish	0h	21Ah
	1				
	2				
	3				
	4				
	5				
	6				
51	0	VCNT07	Vertical address of blemish	0h	21Bh
	1				
	2				
	3				
	4				
	5				
	6	LARGE07	Type of blemish 0h: normal, 1h: large blemish	0h	
7	SD07	Type of blemish detection 0h: Static detection, 1h: Dynamic detection	0h		

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
52	0	DETLV08	Blemish level	0h	21Ch
	1				
	2				
	3				
	4				
	5				
	6				
	7				
53	0	HCNT08	Horizontal address of blemish	0h	21Dh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
54	0	VCNT08	Vertical address of blemish	0h	21Eh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
55	0	VCNT08	Vertical address of blemish	0h	21Fh
	1				
	2				
	3				
	4				
	5				
	6	LARGE08	Type of blemish 0h: normal, 1h: large blemish	0h	21Fh
	7	SD08	Type of blemish detection 0h: Static detection, 1h: Dynamic detection	0h	

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
56	0	DETLV09	Blemish level	0h	220h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
57	0	HCNT09	Horizontal address of blemish	0h	221h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
58	0	VCNT09	Vertical address of blemish	0h	222h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
59	0	VCNT09	Vertical address of blemish	0h	223h
	1				
	2				
	3				
	4				
	5				
	6	LARGE09	Type of blemish 0h: normal, 1h: large blemish	0h	
	7	SD09	Type of blemish detection 0h: Static detection, 1h: Dynamic detection	0h	

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
60	0	DETLV10	Blemish level	0h	224h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
61	0	HCNT10	Horizontal address of blemish	0h	225h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
62	0	VCNT10	Vertical address of blemish	0h	226h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
63	0	LARGE10	Type of blemish 0h: normal, 1h: large blemish	0h	227h
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
64	0	DETLV11	Blemish level	0h	228h
	1				
	2				
	3				
	4				
	5				
	6				
65	0	HCNT11	Horizontal address of blemish	0h	229h
	1				
	2				
	3				
	4				
	5				
	6				
66	0	VCNT11	Vertical address of blemish	0h	22Ah
	1				
	2				
	3				
	4				
	5				
	6				
67	0	VCNT11	Vertical address of blemish	0h	22Bh
	1				
	2				
	3				
	4				
	5				
	6	LARGE11	Type of blemish 0h: normal, 1h: large blemish	0h	
7	SD11	Type of blemish detection 0h: Static detection, 1h: Dynamic detection	0h		

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
68	0	DETLV12	Blemish level	0h	22Ch
	1				
	2				
	3				
	4				
	5				
	6				
69	0	HCNT12	Horizontal address of blemish	0h	22Dh
	1				
	2				
	3				
	4				
	5				
	6				
70	0	VCNT12	Vertical address of blemish	0h	22Eh
	1				
	2				
	3				
	4				
	5				
	6				
71	0	VCNT12	Vertical address of blemish	0h	22Fh
	1				
	2				
	3				
	4				
	5				
	6	LARGE12	Type of blemish 0h: normal, 1h: large blemish	0h	
7	SD12	Type of blemish detection 0h: Static detection, 1h: Dynamic detection	0h		

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
72	0	DETLV13	Blemish level	0h	230h
	1				
	2				
	3				
	4				
	5				
	6				
73	0	HCNT13	Horizontal address of blemish	0h	231h
	1				
	2				
	3				
	4				
	5				
	6				
74	0	VCNT13	Vertical address of blemish	0h	232h
	1				
	2				
	3				
	4				
	5				
	6				
75	0	LARGE13	Type of blemish 0h: normal, 1h: large blemish	0h	233h
	1				
	2				
	3				
	4				
	5				
	6				
75	6	SD13	Type of blemish detection 0h: Static detection, 1h: Dynamic detection	0h	
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
76	0	DETLV14	Blemish level	0h	234h
	1				
	2				
	3				
	4				
	5				
	6				
77	0	HCNT14	Horizontal address of blemish	0h	235h
	1				
	2				
	3				
	4				
	5				
	6				
78	0	VCNT14	Vertical address of blemish	0h	236h
	1				
	2				
	3				
	4				
	5				
	6				
79	0	VCNT14	Vertical address of blemish	0h	237h
	1				
	2				
	3				
	4				
	5				
	6	LARGE14	Type of blemish 0h: normal, 1h: large blemish	0h	
7	SD14	Type of blemish detection 0h: Static detection, 1h: Dynamic detection	0h		

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
80	0	DETLV15	Blemish level	0h	238h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
81	0	HCNT15	Horizontal address of blemish	0h	239h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
82	0	VCNT15	Vertical address of blemish	0h	23Ah
	1				
	2				
	3				
	4				
	5				
	6				
	7				
83	0	VCNT15	Vertical address of blemish	0h	23Bh
	1				
	2				
	3				
	4				
	5				
	6	LARGE15	Type of blemish 0h: normal, 1h: large blemish	0h	
	7	SD15	Type of blemish detection 0h: Static detection, 1h: Dynamic detection	0h	

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
84	0	DETLV16	Blemish level	0h	23Ch
	1				
	2				
	3				
	4				
	5				
	6				
	7				
85	0	HCNT16	Horizontal address of blemish	0h	23Dh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
86	0	VCNT16	Vertical address of blemish	0h	23Eh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
87	0	VCNT16	Vertical address of blemish	0h	23Fh
	1				
	2				
	3				
	4				
	5				
	6	LARGE16	Type of blemish 0h: normal, 1h: large blemish	0h	
	7	SD16	Type of blemish detection 0h: Static detection, 1h: Dynamic detection	0h	

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
88	0	DETLV17	Blemish level	0h	240h
	1				
	2				
	3				
	4				
	5				
	6				
89	0	HCNT17	Horizontal address of blemish	0h	241h
	1				
	2				
	3				
	4				
	5				
	6				
90	0	VCNT17	Vertical address of blemish	0h	242h
	1				
	2				
	3				
	4				
	5				
	6				
91	0	VCNT17	Vertical address of blemish	0h	243h
	1				
	2				
	3				
	4				
	5				
	6	LARGE17	Type of blemish 0h: normal, 1h: large blemish	0h	
7	SD17	Type of blemish detection 0h: Static detection, 1h: Dynamic detection	0h		

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
92	0	DETLV18	Blemish level	0h	244h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
93	0	HCNT18	Horizontal address of blemish	0h	245h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
94	0	VCNT18	Vertical address of blemish	0h	246h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
95	0	VCNT18	Vertical address of blemish	0h	247h
	1				
	2				
	3				
	4				
	5				
	6	LARGE18	Type of blemish 0h: normal, 1h: large blemish	0h	
	7	SD18	Type of blemish detection 0h: Static detection, 1h: Dynamic detection	0h	

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
96	0	DETLV19	Blemish level	0h	248h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
97	0	HCNT19	Horizontal address of blemish	0h	249h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
98	0	VCNT19	Vertical address of blemish	0h	24Ah
	1				
	2				
	3				
	4				
	5				
	6				
	7				
99	0	VCNT19	Vertical address of blemish	0h	24Bh
	1				
	2				
	3				
	4				
	5				
	6	LARGE19	Type of blemish 0h: normal, 1h: large blemish	0h	
7	SD19	Type of blemish detection 0h: Static detection, 1h: Dynamic detection	0h		

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
100	0	DETLV20	Blemish level	0h	24Ch
	1				
	2				
	3				
	4				
	5				
	6				
	7				
101	0	HCNT20	Horizontal address of blemish	0h	24Dh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
102	0	VCNT20	Vertical address of blemish	0h	24Eh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
103	0	VCNT20	Vertical address of blemish	0h	24Fh
	1				
	2				
	3				
	4				
	5				
	6	LARGE20	Type of blemish 0h: normal, 1h: large blemish	0h	
7	SD20	Type of blemish detection 0h: Static detection, 1h: Dynamic detection	0h		

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
104	0	DETLV21	Blemish level	0h	250h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
105	0	HCNT21	Horizontal address of blemish	0h	251h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
106	0	VCNT21	Vertical address of blemish	0h	252h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
107	0	LARGE21	Type of blemish 0h: normal, 1h: large blemish	0h	253h
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
108	0	DETLV22	Blemish level	0h	254h
	1				
	2				
	3				
	4				
	5				
	6				
109	0	HCNT22	Horizontal address of blemish	0h	255h
	1				
	2				
	3				
	4				
	5				
	6				
110	0	VCNT22	Vertical address of blemish	0h	256h
	1				
	2				
	3				
	4				
	5				
	6				
111	0	VCNT22	Vertical address of blemish	0h	257h
	1				
	2				
	3				
	4				
	5				
	6	LARGE22	Type of blemish 0h: normal, 1h: large blemish	0h	
7	SD22	Type of blemish detection 0h: Static detection, 1h: Dynamic detection	0h		

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
112	0	DETLV23	Blemish level	0h	258h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
113	0	HCNT23	Horizontal address of blemish	0h	259h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
114	0	VCNT23	Vertical address of blemish	0h	25Ah
	1				
	2				
	3				
	4				
	5				
	6				
	7				
115	0	LARGE23	Type of blemish 0h: normal, 1h: large blemish	0h	25Bh
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
116	0	DETLV24	Blemish level	0h	25Ch
	1				
	2				
	3				
	4				
	5				
	6				
117	0	HCNT24	Horizontal address of blemish	0h	25Dh
	1				
	2				
	3				
	4				
	5				
	6				
118	0	VCNT24	Vertical address of blemish	0h	25Eh
	1				
	2				
	3				
	4				
	5				
	6				
119	0	VCNT24	Vertical address of blemish	0h	25Fh
	1				
	2				
	3				
	4				
	5				
	6	LARGE24	Type of blemish 0h: normal, 1h: large blemish	0h	
7	SD24	Type of blemish detection 0h: Static detection, 1h: Dynamic detection	0h		

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
120	0	DETLV25	Blemish level	0h	260h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
121	0	HCNT25	Horizontal address of blemish	0h	261h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
122	0	VCNT25	Vertical address of blemish	0h	262h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
123	0	LARGE25	Type of blemish 0h: normal, 1h: large blemish	0h	263h
	1				
	2				
	3				
	4				
	5				
	6	SD25	Type of blemish detection 0h: Static detection, 1h: Dynamic detection	0h	
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
124	0	DETLV26	Blemish level	0h	264h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
125	0	HCNT26	Horizontal address of blemish	0h	265h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
126	0	VCNT26	Vertical address of blemish	0h	266h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
127	0	VCNT26	Vertical address of blemish	0h	267h
	1				
	2				
	3				
	4				
	5				
	6	LARGE26	Type of blemish 0h: normal, 1h: large blemish	0h	
	7	SD26	Type of blemish detection 0h: Static detection, 1h: Dynamic detection	0h	

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
128	0	DETLV27	Blemish level	0h	268h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
129	0	HCNT27	Horizontal address of blemish	0h	269h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
130	0	VCNT27	Vertical address of blemish	0h	26Ah
	1				
	2				
	3				
	4				
	5				
	6				
	7				
131	0	VCNT27	Vertical address of blemish	0h	26Bh
	1				
	2				
	3				
	4				
	5				
	6	LARGE27	Type of blemish 0h: normal, 1h: large blemish	0h	
	7	SD27	Type of blemish detection 0h: Static detection, 1h: Dynamic detection	0h	

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
132	0	DETLV28	Blemish level	0h	26Ch
	1				
	2				
	3				
	4				
	5				
	6				
	7				
133	0	HCNT28	Horizontal address of blemish	0h	26Dh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
134	0	VCNT28	Vertical address of blemish	0h	26Eh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
135	0	LARGE28	Type of blemish 0h: normal, 1h: large blemish	0h	26Fh
	1				
	2				
	3				
	4				
	5				
	6				
7	SD28	Type of blemish detection 0h: Static detection, 1h: Dynamic detection	0h		

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
136	0	DETLV29	Blemish level	0h	270h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
137	0	HCNT29	Horizontal address of blemish	0h	271h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
138	0	VCNT29	Vertical address of blemish	0h	272h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
139	0	VCNT29	Vertical address of blemish	0h	273h
	1				
	2				
	3				
	4				
	5				
	6	LARGE29	Type of blemish 0h: normal, 1h: large blemish	0h	
	7	SD29	Type of blemish detection 0h: Static detection, 1h: Dynamic detection	0h	

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
140	0	DETLV30	Blemish level	0h	274h
	1				
	2				
	3				
	4				
	5				
	6				
141	0	HCNT30	Horizontal address of blemish	0h	275h
	1				
	2				
	3				
	4				
	5				
	6				
142	0	VCNT30	Vertical address of blemish	0h	276h
	1				
	2				
	3				
	4				
	5				
	6				
143	0	VCNT30	Vertical address of blemish	0h	277h
	1				
	2				
	3				
	4				
	5				
	6	LARGE30	Type of blemish 0h: normal, 1h: large blemish	0h	
7	SD30	Type of blemish detection 0h: Static detection, 1h: Dynamic detection	0h		

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
144	0	DETLV31	Blemish level	0h	278h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
145	0	HCNT31	Horizontal address of blemish	0h	279h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
146	0	VCNT31	Vertical address of blemish	0h	27Ah
	1				
	2				
	3				
	4				
	5				
	6				
	7				
147	0	VCNT31	Vertical address of blemish	0h	27Bh
	1				
	2				
	3				
	4				
	5				
	6	LARGE31	Type of blemish 0h: normal, 1h: large blemish	0h	
	7	SD31	Type of blemish detection 0h: Static detection, 1h: Dynamic detection	0h	

Category 12: CPU

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
1	0	MODESEL	DSP operation mode 0h, 3h, 6h and 9h: analog/digital output, 1h, 4h, Ah: 8fsc analog output, 2h, 5h, 8h and Bh: 27M master MCK PLL	0h	2h
	1				
	2				
	3				
	4	Fix	"0h" fix	0h	
	5				
	6				
	7				
2	0	ADJMODE	Adjustment mode 0h: normal mode, 20h: AGCMIN auto adjustment mode, 21h: Pre-WB adjustment mode, 22h: blemish detection and compensation mode, 31h: AWB monitor mode, 32h: AWB co-process mode	0h	3h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
3	0	PREWBMODE	Pre-WB adjustment mode (Effective when ADJMODE = 21h) 0h: no operation, 1h: 3200K adjustment, 2h: low color temperature adjustment, 3h: high color temperature adjustment	0h	4h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
4	0	CCDLEV	AGCMIN auto adjustment parameter	0h	5h
	1	Fix	"0h" fix	0h	
	2				
	3				
	4				
	5	AGCMINFIN	End flag of automatic AGCMIN adjustment 0h: not end, 1h: end	0h	
	6	BLMDETFIN	End flag of Static detection 0h: not end, 1h: end	0h	
	7	Fix	"0h" fix	0h	

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
5	0	CPUHOLD	CPU hold 0h: operation, 1h: stop	0h	6h
	1	AWBHOLD	AWB hold 0h: operation, 1h: stop	0h	
	2	AEHOLD	AE hold 0h: operation, 1h: stop	0h	
	3	CLMPHOLD	Clamp hold 0h: operation, 1h: stop	0h	
	4	SGHOLD	SG hold 0h: operation, 1h: stop	0h	
	5	PDRHOLD	Port driver hold 0h: operation, 1h: stop	0h	
	6	Fix	"0h" fix	0h	
7					
6	0	Fix	"0h" fix	0h	7h
	1	OUTGAIN	OUTGAIN 0h: off, 1h: on	0h	
	2	SPRS	Suppress 0h: off, 1h: on	1h	
	3	ASPR	Aperture correction suppress 0h: off, 1h: on (Effective when SPRS = 1h)	1h	
	4	CSPR	Chroma suppress 0h: off, 1h: on (Effective when SPRS = 1h)	1h	
	5	LVB	CCD carrier level balance 0h: off, 1h: on	0h	
	6	YLB	Luminance reproduction level balance 0h: off, 1h: on	0h	
7	Fix	"0h" fix	0h		
7	0	GAMSEL	Gamma parameter selection 0h: EEPROM value, 1h: user setting value (CAT13 setting value)	0h	8h
	1	Fix	"0h" fix	0h	
	2	Fix	"0h" fix	0h	
	3				
	4				
	5				
	6				
7					
8	0	VHAPGCTL	External control of VH aperture correction gain 0h: impossible, 1h: possible	0h	9h
	1	RGBMATCTL	External control of primary color separation matrix 0h: impossible, 1h: possible	0h	
	2	CRGAINCTL	External control of chroma gain 0h: impossible, 1h: possible	0h	
	3	YGAINCTL	External control of YGAIN 0h: impossible, 1h: possible	0h	
	4	HUECTL	External control of HUE 0h: impossible, 1h: possible	0h	
	5	LVBGCTL	CCD carrier level balance gain external control 0h: impossible, 1h: possible	0h	
	6	YLBGCTL	Luminance reproduction level balance gain external control 0h: impossible, 1h: possible	0h	
7	SETUPCTL	External control of SETUP level 0h: impossible, 1h: possible	0h		

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
9	0	GAMCTL	External control of GAMMA parameter 0h: impossible, 1h: possible	0h	Ah
	1	Fix	"0h" fix	0h	
	2				
	3				
	4				
	5				
	6				
	7				
10	0	BPSEL	RS-232C bit rate selection 0h: 9600bps, 1h: 19200bps	1h	Bh
	1	CSEVRSEL	EVR chip select signal output selection 0h: S4 pin, 1h: P3 pin	0h	
	2	Fix	"0h" fix	0h	
	3				
	4				
	5				
	6				
	7				
11	0	CRLESSON	Anti color-rolling mode 0h: off, 1h: on	0h	Ch
	1	CRLESSAWB	Anti color-rolling AWB (effective only when CRLESSON = 1h) 0h: off, 1h: on	1h	
	2	U6DBDWN	6dB minus gain	0h	
	3	Fix	"0h" fix	0h	
	4				
	5				
	6				
	7				
12	0	PLLSTBOFF	PLLSTB control selection 0h: FW control, 1h: EEPROM value	0h	Dh
	1	Fix	"0h" fix	0h	
	2	Fix	"1h" fix	1h	
	3	Fix	"0h" fix	0h	
	4				
	5	SSELOFF	S pin control by SGMODE 0h: on, 1h: off	0h	
	6	Fix	"0h" fix	0h	
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
13	0	YDLYOFF	YDLY control 0h: on, 1h: off	1h	Eh
	1	SYSSELON	System condiguration switching function 0h: off, 1h: on	0h	
	2	SYSSELFLG	System selection flag	0h	
	3	CROFFCTLON	Low illuminance chroma OFF control 0h: control off, 1h: control on	0h	
	4	CROFFJUDG	Low illuminance chroma OFF judgment result flag 0h: chroma on, 1h :chroma off	0h	
	5	Fix	"0h" fix	0h	
	6				
7					
14	0	YDLY0	YDLY when YDACCKSEL = 0h (Effective only when YDLYOFF = 0h)	0h	Fh
	1				
	2				
	3				
	4	Fix	"0h" fix	0h	
	5				
	6				
7					
15	0	YDLY1	YDLY when YDACCKSEL = 1h (Effective only when YDLYOFF = 0h)	0h	10h
	1				
	2				
	3				
	4	Fix	"0h" fix	0h	
	5				
	6				
7					
16	0	MODESELO	MODESEL when SYSSELFLG = 0h (Efective only when SYSSELON = 1h)	0h	11h
	1				
	2				
	3				
	4				
	5				
	6				
7					

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
17	0	MODESEL1	MODESEL when SYSSELFLG = 1h (Effective only when SYSSELON = 1h)	0h	12h
	1				
	2				
	3				
	4				
	5				
	6				
18	0	SGMODE0	SGMODE when SYSSELFLG = 0h (Effective only when SYSSELON = 1h)	0h	13h
	1				
	2				
	3				
	4				
	5				
	6				
19	0	SGMODE1	SGMODE when SYSSELFLG = 1h (Effective only when SYSSELON = 1h)	0h	14h
	1				
	2				
	3				
	4				
	5				
	6				
20	0	EVR20	EVR2 output when SYSSELFLG = 0h (Effective only when SYSSELON = 1h)	0h	15h
	1				
	2				
	3				
	4				
	5				
	6				
7					

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
21	0	EVR21	EVR2 output when SYSSELF LG = 1h (Effective only when SYSSELON = 1h)	0h	16h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
22	0	Const	"1h" fix	1h	17h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
23	0	Const	"Bh" fix	Bh	18h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
24	0	Const	"Ah" fix	Ah	19h
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
25	0	Const	"Ah" fix	Ah	1Ah
	1				
	2				
	3				
	4				
	5				
	6				
	7				
26	0	Const	"0h" fix	0h	1Bh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
27	0	Const	"0h" fix	0h	1Ch
	1				
	2				
	3				
	4				
	5				
	6				
	7				
28	0	Const	"Ah" fix	Ah	1Dh
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
29	0	Const	"Ah" fix	Ah	1Eh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
30	0	Const	"Ah" fix	Ah	1Fh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
31	0	Const	"Ah" fix	Ah	20h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
32	0	Const	"Ah" fix	Ah	21h
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
33	0	Const	"Ah" fix	Ah	22h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
34	0	Const	"0h" fix	0h	23h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
35	0	Const	"Ah" fix	Ah	24h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
36	0	Const	"Ah" fix	Ah	25h
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
37	0	Const	"Ah" fix	Ah	26h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
38	0	Const	"Ah" fix	Ah	27h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
39	0	Const	"0h" fix	0h	28h
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Category 13: PICT2

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
1	0	Const	"68h" fix	68h	29h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
2	0	Const	"F0h" fix	F0h	2Ah
	1				
	2				
	3				
	4				
	5				
	6				
	7				
3	0	Const	"2Ah" fix	2Ah	2Bh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
4	0	Const	"D4h" fix	D4h	2Ch
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
5	0	Const	"80h" fix	80h	2Dh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
6	0	Const	"80h" fix	80h	2Eh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
7	0	Const	"FFh" fix	FFh	2Fh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
8	0	Const	"FEh" fix	FEh	30h
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
9	0	Const	"2Fh" fix	2Fh	31h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
10	0	Const	"1Ch" fix	1Ch	32h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
11	0	Const	"6h" fix	6h	33h
	1				
	2				
	3				
	4	Fix	"0h" fix	0h	
	5				
	6				
	7				
12	0	OUTGAINMAX	OUTGAIN setting value (OUTGAIN = 1h)	2h	34h
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
13	0	RYGAINRATE	PYGAIN coefficient (OUTGAIN = 1h) RYGAIN ← RYGAIN × OUTGAINMAX × RYGAINRATE/FFh	C0h	35h
	1				
	2				
	3				
	4				
	5				
	6				
14	0	BYGAINRATE	BYGAIN coefficient (OUTGAIN = 1h) BYGAIN ← BYGAIN × OUTGAINMAX × BYGAINRATE/FFh	C0h	36h
	1				
	2				
	3				
	4				
	5				
	6				
15	0	YGAINRATE	YGAIN coefficient (OUTGAIN = 1h) YGAIN ← YGAIN × OUTGAINMAX × YGAINRATE/FFh	C0h	37h
	1				
	2				
	3				
	4				
	5				
	6				
16	0	ASPRSTA	Aperture correction suppress start AGCCNT (Set value as ASPRSTA < ASPREND)	A0h	38h
	1				
	2				
	3				
	4				
	5				
	6				
7					

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
17	0	ASPREND	Aperture correction suppress end AGCCNT (Set value as ASPRSTA < ASPREND)	D0h	39h
	1				
	2				
	3				
	4				
	5				
	6				
18	0	ASPRMIN	Aperture correction suppress, end suppress level	0h	3Ah
	1				
	2				
	3				
	4				
	5				
	6				
19	0	CSPRSTA	Chroma suppress start AGCCNT (Set value as CSPRSTA < CSPREND)	A0h	3Bh
	1				
	2				
	3				
	4				
	5				
	6				
20	0	CSPREND	Chroma suppress end AGCCNT (Set value as CSPRSTA < CSPREND)	D0h	3Ch
	1				
	2				
	3				
	4				
	5				
	6				
7					

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
21	0	CSPRMIN	Chroma suppress, end suppress level	8Ah	3Dh
	1				
	2				
	3				
	4				
	5				
	6				
22	0	LVBMODE	CCD carrier level balance mode 0h: auto, 1h: fixed value of low color temperature, 2h: fixed value of 3200K, 3h: fixed value of high color temperature	0h	3Eh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
23	0	LVBS1RL	CCD carrier level balance, S1R gain of low color temperature	43h	3Fh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
24	0	LVBS2RL	CCD carrier level balance, S2R gain of low color temperature	3Ch	40h
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
25	0	LVBS1BL	CCD carrier level balance, S1B gain of low color temperature	50h	41h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
26	0	LVBS2BL	CCD carrier level balance, S2B gain of low color temperature	34h	42h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
27	0	LVBS1RM	CCD carrier level balance, S1R gain of 3200K	40h	43h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
28	0	LVBS2RM	CCD carrier level balance, S2R gain of 3200K	3Fh	44h
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
29	0	LVBS1BM	CCD carrier level balance, S1B gain of 3200K	49h	45h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
30	0	LVBS2BM	CCD carrier level balance, S2B gain of 3200K	38h	46h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
31	0	LVBS1RH	CCD carrier level balance, S1R gain of high color temperature	36h	47h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
32	0	LVBS2RH	CCD carrier level balance, S2R gain of high color temperature	4Ch	48h
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
33	0	LVBS1BH	CCD carrier level balance, S1B gain of high color temperature	40h	49h
	1				
	2				
	3				
	4				
	5				
	6				
34	0	LVBS2BH	CCD carrier level balance, S2B gain of high color temperature	3Fh	4Ah
	1				
	2				
	3				
	4				
	5				
	6				
35	0	YLBMODE	Luminance reproduction level balance mode 0h: auto, 1h: fixed value of low color temperature, 2h: fixed value of 3200K, 3h: fixed value of high color temperature	0h	4Bh
	1				
	2				
	3				
	4				
	5				
	6				
36	0	YLS1RL	Luminance reproduction level balance, S1R gain of low color temperature	1h	4Ch
	1				
	2				
	3				
	4				
	5				
	6				
7					

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
37	0	YLBS2RL	Luminance reproduction level balance, S2R gain of low color temperature	FEh	4Dh
	1				
	2				
	3				
	4				
	5				
	6				
38	0	YLBS1BL	Luminance reproduction level balance, S1B gain of low color temperature	EEh	4Eh
	1				
	2				
	3				
	4				
	5				
	6				
39	0	YLBS2BL	Luminance reproduction level balance, S2B gain of low color temperature	11h	4Fh
	1				
	2				
	3				
	4				
	5				
	6				
40	0	YLBS1RM	Luminance reproduction level balance, S1R gain of 3200K	1h	50h
	1				
	2				
	3				
	4				
	5				
	6				
7					

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
41	0	YLBS2RM	Luminance reproduction level balance, S2R gain of 3200K	FEh	51h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
42	0	YLBS1BM	Luminance reproduction level balance, S1B gain of 3200K	EEh	52h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
43	0	YLBS2BM	Luminance reproduction level balance, S2B gain of 3200K	11h	53h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
44	0	YLBS1RH	Luminance reproduction level balance, S1R gain of high color temperature	F1h	54h
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
45	0	YLBS2RH	Luminance reproduction level balance, S2R gain of high color temperature	0Eh	55h
	1				
	2				
	3				
	4				
	5				
	6				
46	0	YLBS1BH	Luminance reproduction level balance, S1B gain of high color temperature	ECh	56h
	1				
	2				
	3				
	4				
	5				
	6				
47	0	YLBS2BH	Luminance reproduction level balance, S2B gain of high color temperature	13h	57h
	1				
	2				
	3				
	4				
	5				
	6				
48	0	Fix	"A0h" fix	A0h	58h
	1				
	2				
	3				
	4				
	5				
	6				
7					

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
49	0	Fix	"D0h" fix	D0h	59h
	1				
	2				
	3				
	4				
	5				
	6				
50	0	Fix	"Dh" fix	Dh	5Ah
	1				
	2				
	3				
	4				
	5				
	6	Fix	"0h" fix	0h	
51	0	Fix	"1Ah" fix	1Ah	5Bh
	1				
	2				
	3				
	4				
	5				
	6	Fix	"0h" fix	0h	
52	0	UYGAMSLV	Luminance signal gamma low level signal compression level switching (Effective when GAMSEL = 1h)	0h	5Ch
	1	UYGAMSMTH	Gamma curve smooth processing (Effective when GAMSEL = 1h)	0h	
	2	UYGAMSEL	Luminance signal variable gamma setting (Effective when GAMSEL = 1h)	0h	
	3				
	4				
	5	UYKNEESEL	Luminance signal variable knee setting (Effective when GAMSEL = 1h)	5h	
	6				
7					

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
53	0	UCGAMMA	Chroma variable gamma parameter (Effective when GAMSEL = 1h)	0h	5Dh
	1				
	2				
	3	UCKNCLIP	Chroma knee area clip level (Effective when GAMSEL = 1h)	0h	
	4				
	5	UCKNEE	Chroma variable knee parameter (Effective when GAMSEL = 1h)	5h	
	6				
7					
54	0	UCKNEE2	Chroma knee area high luminance side bending (Effective when GAMSEL = 1h) 0h: with bending, 1h: without bending	0h	5Eh
	1	UYGAMSON	Luminance signal gamma low level signal compression (Effective when GAMSEL = 1h) 0h: off, 1h: on	0h	
	2	Fix	"0h" fix	0h	
	3				
	4				
	5				
	6				
7					
55	0	CRONPOINT	Low luminance chroma OFF function Chroma OFF → ON transition point setting (Set as CRONPOINT < CROFFPOINT.)	C0h	5Fh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
56	0	CROFFPOINT	Low luminance chroma OFF function Chroma ON → OFF transition point setting (Set as CRONPOINT < CROFFPOINT.)	D0h	60h
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Category 14: AE2

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
1	0	AEME	AE/ME 0h: auto exposure, 1h: manual exposure	0h	61h
	1	MIRIS	Mechanical IRIS 0h: off, 1h: on	0h	
	2	BLCOFF	Backlight compensation OFF 0h: on, 1h: off	0h	
	3	AEREF	AE reference user mode 0h: off, 1h: on	0h	
	4	AGCMAX	AGC MAX gain select 0h: AGCMAXL, 1h: AGCMAXH	0h	
	5	Fix	"0h" fix	0h	
	6	AESHUT	AE SHUT mode on 0h: off, 1h: on	0h	
	7	AGCOFF	AGC off mode 0h: AGCON, 1h: AGCMIN fix	0h	
2	0	NORMFLC	Flickerless by electronic shutter and AGC gain control 0h: off, 1h: on	1h	62h
	1	LLFLC	Flickerless by low-speed side shutter limiter 0h: off, 1h: on	0h	
	2	FIXSHTFLC	Flickerless by fixed electronic shutter 0h: off, 1h: on	0h	
	3	AGCFLOF	Flickerless by AGG gain modulation OFF 0h: on, 1h: off	0h	
	4	SHUTFLOF	Flickerless by electronic shutter modulation OFF 0h: on, 1h: off	0h	
	5	NORMFLCF	Flickerless by modulation on 0h: with decision, 1h: on	0h	
	6	Fix	"0h" fix	0h	
	7				
3	0	VIDEOAE	Video AE mode 0h: off, 1h: on	0h	63h
	1	Fix	"0h" fix	0h	
	2	SHTSEL	Electronic shutter speed selector 0h to 7h: slow to fast	0h	
	3				
	4				
	5	Fix	"0h" fix	0h	
	6				
	7				
4	0	BLCSEL	BLC mode selection 0h: weighted average mode, 1h: compensation gain fixed mode	0h	64h
	1	FBLCGAINSEL	BLC compensation gain fixed mode range selection 0h: ×0 to ×4, 1h: ×1 to ×1024	0h	
	2	Fix	"0h" fix	0h	
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
5	0	FBLCGAIN	BLC compensation gain fixed mode gain adjustment	C0h	65h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
6	0	AEW0	AE Window0 weight	1h	66h
	1				
	2				
	3				
	4	AEW1	AE Window1 weight	1h	
	5				
	6				
	7				
7	0	AEW2	AE Window2 weight	3h	67h
	1				
	2				
	3				
	4	AEW3	AE Window3 weight	3h	
	5				
	6				
	7				
8	0	AEW4	AE Window4 weight	Fh	68h
	1				
	2				
	3				
	4	Fix	"0h" fix	0h	
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
9	0	AESTAB	Dead band width when tracing into hysteresis loop	0h	69h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
10	0	AEHYST	Dead band width when tracing out of hysteresis loop	0h	6Ah
	1				
	2				
	3				
	4				
	5				
	6				
	7				
11	0	AEWAIT	Counter value beyond AEHYST allowable range	0h	6Bh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
12	0	AESPEED	AE speed	6h	6Ch
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
13	0	AEUSR	AE USR setting level (Effective when AEREF = 1h)	0h	6Dh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
14	0	REFSFT	The amount of shifts of AE reference	0h	6Eh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
15	0	AGCMAXL	AE AGC MAX Low (Effective when AGCMAX = 0h)	C0h	6Fh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
16	0	AGCMAXH	AE AGC MAX High (Effective when AGCMAX = 1h)	FFh	70h
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
17	0	SHUTMAX	Upper limit of shutter speed (Maximum shutter speed)	FFh	71h
	1				
	2				
	3				
	4				
	5				
	6				
18	0	SHUTMIN	Lower limit of shutter speed (Minimum shutter speed)	0h	72h
	1				
	2				
	3				
	4				
	5				
	6				
19	0	IRISVMAX	Maximum value of IRISVCNT	FFh	73h
	1				
	2				
	3				
	4				
	5				
	6				
20	0	IRISVMIN	Minimum value of IRISVCNT	0h	74h
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
21	0	VCAM12	IRISVCNT setting value (-12dB)	0h	75h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
22	0	VCAM6	IRISVCNT setting value (-6dB)	13h	76h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
23	0	VCA0	IRISVCNT setting value (0dB)	4Dh	77h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
24	0	VCAP6	IRISVCNT setting value (6dB)	87h	78h
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
25	0	VCAP12	IRISVCNT setting value (12dB)	E8h	79h
	1				
	2				
	3				
	4				
	5				
	6				
26	0	TRANSREFLV	AGC area/shutter area reference offset value 0h to EFh: 0 to large	0h	7Ah
	1				
	2				
	3				
	4				
	5				
	6				
7	Fix	"0h" fix	0h		
27	0	AEDEADBAND	Dead band width adjustment	10h	7Bh
	1				
	2				
	3				
	4				
	5				
	6				
7					
28	0	Const	"20h" fix	20h	7Ch
	1				
	2				
	3				
	4				
	5				
	6				
7					

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
29	0	Const	"1h" fix	1h	7Dh
	1	Fix	"0h" fix	0h	
	2				
	3				
	4				
	5				
	6				
	7				

Category 15: AWB2

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
1	0	AWBMODE	AWB mode selection 0h: ATW, 1h: user fixed value 1, 2h: Push, 3h: user fixed value 3, 4h: MWB, 5h: user fixed value 2, 6h: HOLD, 7h: user fixed value 4	0h	7Eh
	1				
	2				
	3	Fix	"0h" fix	0h	
	4	Fix	"0h" fix	0h	
	5				
	6				
	7				
2	0	AWBSEPOF	Luminance specific integration 0h: on, 1h: off	0h	7Fh
	1	AWBTRG	Push lock system selection 0h: conventional, 1h: trigger	0h	
	2	AWBSFT	Convergence system selection 0h: frame setting system, 1h: point setting system	1h	
	3	Fix	"0h" fix	0h	
	4				
	5				
	6				
	7				
3	0	ATWFRAMOF	ATW operation frame 0h: on, 1h: all cancel	0h	80h
	1	ATWFRM1OF	ATW operation frame 1 0h: on, 1h: cancel	0h	
	2	ATWFRM2OF	ATW operation frame 2 0h: on, 1h: cancel	0h	
	3	ATWFRM3OF	ATW operation frame 3 0h: on, 1h: cancel	0h	
	4	ATWLARGFRM	ATW operation frame expansion 0h: normal, 1h: enlarged	0h	
	5	Fix	"0h" fix	0h	
	6				
	7				
4	0	GGAIN	G gain setting value (WBG initial value) "26h" is recommended.	26h	81h
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
5	0	AWBSPED	ATW speed adjustment 0h to FFh: fast to slow	2h	82h
	1				
	2				
	3				
	4				
	5				
	6				
6	0	WBDLY	ATW pull-in WAIT 0h to FFh: fast to slow	10h	83h
	1				
	2				
	3				
	4				
	5				
	6				
7	0	ATWSTEP	Pull-in step width adjustment for ATW 0h to FFh: step width wide to step width narrow	8h	84h
	1				
	2				
	3				
	4				
	5				
	6				
8	0	ATWGSFT1	ATW convergence frame G direction (Effective when AWBSFT = 0h)	2h	85h
	1				
	2				
	3				
	4				
	5				
	6				
7					

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
9	0	ATWRSFT1	ATW convergence frame R direction (Effective when AWBSFT = 0h)	2h	86h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
10	0	ATWBSFT1	ATW convergence frame B direction (Effective when AWBSFT = 0h)	2h	87h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
11	0	ATWMSFT1	ATW convergence frame Mg direction (Effective when AWBSFT = 0h)	2h	88h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
12	0	ATWRSFT2	ATW convergence point, shift amount of R direction (Effective when AWBSFT = 1h)	0h	89h
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
13	0	ATWBSFT2	ATW convergence point, shift amount of B direction (Effective when AWBSFT = 1h)	0h	8Ah
	1				
	2				
	3				
	4				
	5				
	6				
14	0	WBDBAND	Judgment frame for completion of convergence operation	10h	8Bh
	1				
	2				
	3				
	4				
	5				
	6				
15	0	DBANDR	Judgment frame for start of repull-in, R direction	12h	8Ch
	1				
	2				
	3				
	4				
	5				
	6				
16	0	DBANDB	Judgment frame for start of repull-in, B direction	12h	8Dh
	1				
	2				
	3				
	4				
	5				
	6				
7					

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
17	0	DBANDM	Judgment frame for start of repull-in, Mg direction	12h	8Eh
	1				
	2				
	3				
	4				
	5				
	6				
18	0	DBANDG	Judgment frame for start of repull-in, G direction	12h	8Fh
	1				
	2				
	3				
	4				
	5				
	6				
19	0	FRAMRMIN	ATW operation frame 2, R/G	2Bh	90h
	1				
	2				
	3				
	4				
	5				
	6				
20	0	FRAMBMIN	ATW operation frame 2, B/G	2Bh	91h
	1				
	2				
	3				
	4				
	5				
	6				
7					

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
21	0	FRAMRMAX	ATW operation frame 3, R/G	10h	92h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
22	0	FRAMMG	ATW operation frame 3, Mg direction	2Bh	93h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
23	0	LARGFRMR0	For large frame, R direction expansion of low color temperature side	10h	94h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
24	0	LARGFRMB1	For large frame, B direction expansion of high color temperature side	10h	95h
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
25	0	FRAMFL	ATW operation frame 1, fluorescent light frame	20h	96h
	1				
	2				
	3				
	4				
	5				
	6				
26	0	AWBRBWE	ATW operation frame 1, R and B directions frame expansion (Effective when ATWFRM3OF = 1h)	0h	97h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
27	0	AWBMGWE	ATW operation frame 1, Mg and G directions frame expansion (Effective when ATWFRM3OF = 1h)	0h	98h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
28	0	UWBYREFL	Integration range (lower luminance limit)	4h	99h
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
29	0	INTSLICE	Luminance specific integration, integration range slice level	80h	9Ah
	1				
	2				
	3				
	4				
	5				
	6				
30	0	UWBYREFH	Integration range (upper luminance limit)	D0h	9Bh
	1				
	2				
	3				
	4				
	5				
	6				
31	0	HLCUT	Luminance specific integration, high luminance block area setting	80h	9Ch
	1				
	2				
	3				
	4				
	5				
	6				
32	0	ALLSTEP	Pull-in speed adjustment for all pull-in 0h to FFh: fast to slow	2h	9Dh
	1				
	2				
	3				
	4				
	5				
	6				
7					

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
33	0	PLRGAIN	Push lock/MWB R gain	3Eh	9Eh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
34	0	PLBGAIN	Push lock/MWB B gain	38h	9Fh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
35	0	WBUSRR1	User R gain 1 (WBR ← WBUSRR1 when AWBMODE = 1h)	76h	A0h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
36	0	WBUSRB1	User B gain 1 (WBB ← WBUSRB1 when AWBMODE = 1h)	34h	A1h
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
37	0	WBUSRR2	User R gain 2 (WBR ← WBUSRR2 when AWBMODE = 5h)	53h	A2h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
38	0	WBUSRB2	User B gain 2 (WBB ← WBUSRB2 when AWBMODE = 5h)	44h	A3h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
39	0	WBUSRR3	User R gain 3 (WBR ← WBUSRR3 when AWBMODE = 3h)	65h	A4h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
40	0	WBUSRB3	User B gain 3 (WBB ← WBUSRB3 when AWBMODE = 3h)	39h	A5h
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
41	0	WBUSRR4	User R gain 4 (WBR ← WBUSRR4 when AWBMODE = 7h)	8Fh	A6h
	1				
	2				
	3				
	4				
	5				
	6				
42	0	WBUSRB4	User B gain 4 (WBB ← WBUSRB4 when AWBMODE = 7h)	2Ah	A7h
	1				
	2				
	3				
	4				
	5				
	6				
43	0	Fix	"0h" fix	0h	A8h
	1				
	2				
	3				
	4				
	5				
	6				
44	0	CRFRMOFF	AWB convergence frame for anti-color rolling 0h: on, 1h: all cancel	0h	A9h
	1	CRFRM1OFF	AWB convergence frame for anti-color rolling 1 0h: on, 1h: cancel	1h	
	2	CRFRM2OFF	AWB convergence frame for anti-color rolling 2 0h: on, 1h: cancel	0h	
	3	Fix	"1h" fix	0h	
	4	Fix	"0h" fix	0h	
	5				
	6				
7					

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
45	0	CRRGMAXL	Anti-color rolling AWB pull-in frame, R/G maximum value (LSB)	0h	AAh
	1				
	2				
	3				
	4				
	5				
	6				
46	0	CRRGMAXH	Anti-color rolling AWB pull-in frame, R/G maximum value (MSB)	15h	ABh
	1				
	2				
	3				
	4				
	5				
	6				
47	0	CRRGMINL	Anti-color rolling AWB pull-in frame, R/G minimum value (LSB)	0h	ACh
	1				
	2				
	3				
	4				
	5				
	6				
48	0	CRRGMINH	Anti-color rolling AWB pull-in frame, R/G minimum value (MSB)	7h	ADh
	1				
	2				
	3				
	4				
	5				
	6				
7					

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
49	0	CRBGMAXL	Anti-color rolling AWB pull-in frame, B/G maximum value (LSB)	0h	AEh
	1				
	2				
	3				
	4				
	5				
	6				
50	0	CRBGMAXH	Anti-color rolling AWB pull-in frame, B/G maximum value (MSB)	1Eh	AFh
	1				
	2				
	3				
	4				
	5				
	6				
51	0	CRBGMINL	Anti-color rolling AWB pull-in frame, B/G minimum value (LSB)	0h	B0h
	1				
	2				
	3				
	4				
	5				
	6				
52	0	CRBGMINH	Anti-color rolling AWB pull-in frame, B/G minimum value (MSB)	3h	B1h
	1				
	2				
	3				
	4				
	5				
	6				
7					

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
53	0	CRRSFT	Anti-color rolling AWB Convergence point R shift	0h	B2h
	1				
	2				
	3				
	4				
	5				
	6				
54	0	CRBSFT	Anti-color rolling AWB Convergence point B shift	0h	B3h
	1				
	2				
	3				
	4				
	5				
	6				
55	0	CRDBANDR	Anti-color rolling AWB Dead band width of R direction for determination of convergence start	10h	B4h
	1				
	2				
	3				
	4				
	5				
	6				
56	0	CRDBANDB	Anti-color rolling AWB Dead band width of B direction for determination of convergence start	10h	B5h
	1				
	2				
	3				
	4				
	5				
	6				
7					

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
57	0	CRDBANDM	Anti-color rolling AWB Dead band width of Mg direction for determination of convergence start	10h	B6h
	1				
	2				
	3				
	4				
	5				
	6				
58	0	CRDBANDG	Anti-color rolling AWB Dead band width of G direction for determination of convergence start	10h	B7h
	1				
	2				
	3				
	4				
	5				
	6				
59	0	CRLESSSTEP	Parameter for adjustment of anti-color rolling AWB convergence speed 0h to FFh: fast to slow	1h	B8h
	1				
	2				
	3				
	4				
	5				
	6				
60	0	CRLESSDLY	Parameter for adjustment of anti-color rolling AWB response speed 0h to FFh: fast to slow	0h	B9h
	1				
	2				
	3				
	4				
	5				
	6				
7					

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
61	0	Fix	"8h" fix	8h	BAh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
62	0	MASKCNT	Timing setting when ATW starts up	7h	BBh
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Category 16: OPDWND2

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
1	0	UOPDWHST	Horizontal reference position of OPD frame (4-pixel step) (Normal)	0h	BCh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
2	0	UOPDWHSTM	Horizontal reference position of OPD frame (4-pixel step) (Mirror)	5h	BDh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
3	0	UOPDWWST	Vertical reference position of OPD frame (2 lines/FLD) (Normal/Mirror)	0h	BEh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
4	0	UOPDWHGW	OPD frame horizontal grid width setting (4-pixel step) (Normal/Mirror)	8h	BFh
	1				
	2				
	3				
	4	UOPDWWGW	OPD frame vertical grid width setting (2 lines/FLD) (Normal/Mirror)	8h	
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
5	0	UOPDW4HST	4-frame horizontal start position (grid step) (Normal)	5h	C0h
	1				
	2				
	3				
	4	UOPDW4VST	4-frame vertical start position (grid step) (Normal)	5h	
	5				
	6				
	7				
6	0	UOPDW4HSTM	4-frame horizontal start position (grid step) (Mirror)	5h	C1h
	1				
	2				
	3				
	4	UOPDW4VSTM	4-frame vertical start position (grid step) (Mirror)	5h	
	5				
	6				
	7				
7	0	UOPDW4HW	4-frame horizontal width setting (grid step) (Normal/Mirror)	5h	C2h
	1				
	2				
	3				
	4	UOPDW4VW	4-frame vertical width setting (grid step) (Normal/Mirror)	5h	
	5				
	6				
	7				
8	0	UOPDWMK	Window display 0h: off, 1h: on	0h	C3h
	1	UOPDDISP	Window selection 0h: none, 1h: Window0, 2h: Window1, 3h: Window2, 4h: Window3, 5h: Window4, 7h: all windows, 8h: AWB window, 9h: AWB integral pixels (luminance specific integration)	0h	
	2				
	3				
	4				
	5	Fix	"0h" fix	0h	
	6				
	7	UWINDOUT	Window pulse (Port 4 output) 0h: off, 1h: on	0h	

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
9	0	UAWBW0	AWB weight coefficient in window frame 0 0h: ×0, 1h: ×1/16, 2h: ×1/4, 3h: ×1	3h	C4h
	1				
	2	UAWBW1	AWB weight coefficient in window frame 1 0h: ×0, 1h: ×1/16, 2h: ×1/4, 3h: ×1	3h	
	3				
	4	UAWBW2	AWB weight coefficient in window frame 2 0h: ×0, 1h: ×1/16, 2h: ×1/4, 3h: ×1	3h	
	5				
	6	UAWBW3	AWB weight coefficient in window frame 3 0h: ×0, 1h: ×1/16, 2h: ×1/4, 3h: ×1	3h	
7					
10	0	UAWBW4	AWB weight coefficient in window frame 4 0h: ×0, 1h: ×1/16, 2h: ×1/4, 3h: ×1	3h	C5h
	1				
	2	Fix	"0h" fix	0h	
	3				
	4				
	5				
	6				
	7				

Category 17: EXTSYNC2

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
1	0	CTRLSFTHL	Shifter-related parameters (LSB) (Saves SFTH on key release)	0h	C6h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
2	0	CTRLSFTHM	Shifter-related parameters (MSB) (Saves SFTH on key release)	0h	C7h
	1				
	2	Fix	"0h" fix	0h	
	3				
	4				
	5				
	6				
	7				
3	0	CTRLSFTVL	Shifter-related parameters (LSB) (Saves SFTV on key release)	0h	C8h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
4	0	CTRLSFTVM	Shifter-related parameters (MSB) (Saves SFTV on key release)	0h	C9h
	1				
	2	Fix	"0h" fix	0h	
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
5	0	FIXRSTHL	SFTH setting parameter in VRHR mode (LSB)	0h	CAh
	1				
	2				
	3				
	4				
	5				
	6				
6	0	FIXRSTHM	SFTH setting parameter in VRHR mode (MSB)	0h	CBh
	1	Fix	"0h" fix	0h	
	2				
	3				
	4				
	5				
	6				
	7				
7	0	FIXRSTVL	SFTV setting parameter in VRHR mode (LSB)	0h	CCh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
8	0	FIXRSTVM	SFTV setting parameter in VRHR mode (MSB)	0h	CDh
	1	Fix	"0h" fix	0h	
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
9	0	FIXPHL	SFTH setting parameter when it is not the target for shifter operation (LSB)	0h	CEh
	1				
	2				
	3				
	4				
	5				
	6				
10	0	FIXPHM	SFTH setting parameter when it is not the target for shifter operation (MSB)	0h	CFh
	1	Fix	"0h" fix	0h	
	2				
	3				
	4				
	5				
	6				
11	0	FIXPVL	SFTV setting parameter when it is not the target for shifter operation (LSB)	0h	D0h
	1				
	2				
	3				
	4				
	5				
	6				
12	0	FIXPVM	SFTV setting parameter when it is not the target for shifter operation (MSB)	0h	D1h
	1	Fix	"0h" fix	0h	
	2				
	3				
	4				
	5				
	6				
7					

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
13	0	PRSTSFTHL	Pre-set value for shifter operation (SFTH) (LSB)	0h	D2h
	1				
	2				
	3				
	4				
	5				
	6				
14	0	PRSTSFTHM	Pre-set value for shifter operation (SFTH) (MSB)	0h	D3h
	1	Fix	"0h" fix	0h	
	2				
	3				
	4				
	5				
	6				
	7				
15	0	PRSTSFTVL	Pre-set value for shifter operation (SFTV) (LSB)	0h	D4h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
16	0	PRSTSFTVM	Pre-set value for shifter operation (SFTV) (MSB)	0h	D5h
	1	Fix	"0h" fix	0h	
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
17	0	SGMODE	External sync mode setting 0h: INT, 1h: LL, 2h: VSL, 3h: VBSLHP, 4h: VBSLHR, 5h: VRHR, Ah: VSL-S, Fh: VSL-D	0h	D6h
	1				
	2				
	3				
	4	Fix	"0h" fix	0h	
	5	ATMODEON	External sync auto mode 0h: off, 1h: on	0h	
	6	Fix	"0h" fix	0h	
7					
18	0	SFTUP	Key operation, increment	0h	D7h
	1	SFTDWN	Key operation, decrement	0h	
	2	Fix	"0h" fix	0h	
	3				
	4				
	5				
	6				
	7				
19	0	KEYINFLD	Key processing interval of initial period 1h to FFh: 1FLD to 255FLD (0h: 256FLD)	1Eh	D8h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
20	0	KEYINCNT	Key processing count of initial period 1h to FFh: 1 to 255 times (0h: 256 times)	2h	D9h
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
21	0	KEYRPFLD	Key processing interval of continuous period 1h to FFh: 1FLD to 255FLD (0h: 256FLD)	Ah	DAh
	1				
	2				
	3				
	4				
	5				
	6				
22	0	SFTSTEP	Change width setting for shift value 1h to 1Bh (1Ch to 1Fh are setting prohibited.)	1h	DBh
	1				
	2				
	3				
	4	Fix	"0h" fix	0h	
	5				
	6				
23	7	Fix	"3h" fix	3h	DCh
	0				
	1				
	2				
	3				
	4				
	5				
	6				
7					

Category 18: FEADJ (EVREXT)

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
1	0	Fix	"0h" fix	0h	DDh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
2	0	Fix	"0h" fix	0h	DEh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
3	0	Fix	"0h" fix	0h	DFh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
4	0	Fix	"0h" fix	0h	E0h
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
5	0	Fix	"0h" fix	0h	E1h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
6	0	Fix	"0h" fix	0h	E2h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
7	0	Fix	"0h" fix	0h	E3h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
8	0	EVREUSRO	External EVR output voltage setting CH1	FFh	E4h
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
9	0	EVREUSR1	External EVR output voltage setting CH2	E0h	E5h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
10	0	EVREUSR2	External EVR output voltage setting CH3	C0h	E6h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
11	0	EVREUSR3	External EVR output voltage setting CH4	A0h	E7h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
12	0	EVREUSR4	External EVR output voltage setting CH5	80h	E8h
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
13	0	EVREUSR5	External EVR output voltage setting CH6	60h	E9h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
14	0	EVREUSR6	External EVR output voltage setting CH7	40h	EAh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
15	0	EVREUSR7	External EVR output voltage setting CH8	20h	EBh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
16	0	Fix	"80h" fix	80h	ECh
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Category 19: PREADJ

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
1	0	AGCMIN	AGG minimum value	28h	EDh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
2	0	PRER0	R gain in low color temperature	3Dh	EEh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
3	0	AWBPRER	R gain in 3200K	53h	EFh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
4	0	PRER1	R gain in high color temperature	C7h	F0h
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
5	0	PREB0	B gain in low color temperature	58h	F1h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
6	0	AWBPRES	B gain in 3200K	44h	F2h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
7	0	PREB1	B gain in high color temperature	22h	F3h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
8	0	ATWSTARTR	R gain of ATW starting	65h	F4h
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
9	0	ATWSTARTB	B gain of ATW starting	39h	F5h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
10	0	MWBPRESETR	Preset R gain for MWB	53h	F6h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
11	0	MWBPRESETB	Preset B gain for MWB	44h	F7h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
12	0	PRERDIVG1L	R/G Low in high color temperature	F6h	F8h
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
13	0	PRERDIVG1H	R/G High in high color temperature	6h	F9h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
14	0	PREBDIVG1L	B/G Low in high color temperature	B5h	FAh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
15	0	PREBDIVG1H	B/G High in high color temperature	1Fh	FBh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
16	0	PRERDIVG0L	R/G Low in low color temperature	10h	FCh
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
17	0	PRERDIVG0H	R/G High in low color temperature	16h	FDh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
18	0	PREBDIVG0L	B/G Low in low color temperature	Bh	FEh
	1				
	2				
	3				
	4				
	5				
	6				
	7				
19	0	PREBDIVG0H	B/G High in low color temperature	Ch	FFh
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Category 20: PORT

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
1	0	P0ADJ	Input port setting: coefficient setting, output port setting: unused	0h	100h
	1				
	2				
	3				
	4	P0WID	Bit width specification 0h to 15h: bit width (1 bit width to 16 bits width)	0h	
	5				
	6				
	7				
2	0	P0BYTE	Byte number specification 0h: unused, 1h to 127h: Byte number	1h	101h
	1				
	2				
	3				
	4				
	5				
	6				
7	P0IOSEL	Port I/O selection 0h: Input port setting, 1h: Output port setting	0h		
3	0	P0CAT	Category number specification 0h: port unused, 1h to 24h: category number	Fh	102h
	1				
	2				
	3				
	4				
	5	P0LSB	Input port setting 0h to 7h: LSB specification of the data (Max. 16 bits) Output port setting 0h to 7h: Output bit specification	0h	
	6				
7					
4	0	P1ADJ	Input port setting: coefficient setting, output port setting: unused	0h	103h
	1				
	2				
	3				
	4	P1WID	Bit width specification 0h to 15h: bit width (1 bit width to 16 bits width)	0h	
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
5	0	P1BYTE	Byte number specification 0h: unused, 1h to 127h: Byte number	1h	104h
	1				
	2				
	3				
	4				
	5				
	6				
	7	P1IOSEL	Port I/O selection 0h: Input port setting, 1h: Output port setting	0h	
6	0	P1CAT	Category number specification 0h: port unused, 1h to 24h: category number	Fh	105h
	1				
	2				
	3				
	4				
	5	P1LSB	Input port setting 0h to 7h: LSB specification of the data (Max. 16 bits) Output port setting 0h to 7h: Output bit specification	1h	
	6				
7					
7	0	P2ADJ	Input port setting: coefficient setting, output port setting: unused	0h	106h
	1				
	2				
	3				
	4	P2WID	Bit width specification 0h to 15h: bit width (1 bit width to 16 bits width)	0h	
	5				
	6				
7					
8	0	P2BYTE	Byte number specification 0h: unused, 1h to 127h: Byte number	1h	107h
	1				
	2				
	3				
	4				
	5				
	6				
	7	P2IOSEL	Port I/O selection 0h: Input port setting, 1h: Output port setting	0h	

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
9	0	P2CAT	Category number specification 0h: port unused, 1h to 24h: category number	Fh	108h
	1				
	2				
	3				
	4	P2LSB	Input port setting 0h to 7h: LSB specification of the data (Max. 16 bits) Output port setting 0h to 7h: Output bit specification	2h	
	5				
	6				
10	7	P3ADJ	Input port setting: coefficient setting, output port setting: unused	0h	109h
	0				
	1				
	2				
	3	P3WID	Bit width specification 0h to 15h: bit width (1 bit width to 16 bits width)	0h	
	4				
	5				
6					
11	7	P3BYTE	Byte number specification 0h: unused, 1h to 127h: Byte number	Bh	10Ah
	0				
	1				
	2				
	3				
	4				
	5				
6	P3IOSEL	Port I/O selection 0h: Input port setting, 1h: Output port setting	0h		
12	0	P3CAT	Category number specification 0h: port unused, 1h to 24h: category number	Ch	10Bh
	1				
	2				
	3				
	4	P3LSB	Input port setting 0h to 7h: LSB specification of the data (Max. 16 bits) Output port setting 0h to 7h: Output bit specification	0h	
	5				
	6				
7					

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
13	0	P4ADJ	Input port setting: coefficient setting, output port setting: unused	0h	10Ch
	1				
	2				
	3	P4WID	Bit width specification 0h to 15h: bit width (1 bit width to 16 bits width)	0h	
	4				
	5				
	6				
7	14	P4BYTE	Byte number specification 0h: unused, 1h to 127h: Byte number	1h	
0					
1					
2					
3					
4					
5		P4IOSEL	Port I/O selection 0h: Input port setting, 1h: Output port setting	0h	
15	0	P4CAT	Category number specification 0h: port unused, 1h to 24h: category number	Eh	10Eh
	1				
	2				
	3				
	4	P4LSB	Input port setting 0h to 7h: LSB specification of the data (Max. 16 bits) Output port setting 0h to 7h: Output bit specification	2h	
	5				
	6				
16	0	P5ADJ	Input port setting: coefficient setting, output port setting: unused	0h	10Fh
	1				
	2				
	3	P5WID	Bit width specification 0h to 15h: bit width (1 bit width to 16 bits width)	0h	
	4				
	5				
	6				
7					

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
17	0	P5BYTE	Byte number specification 0h: unused, 1h to 127h: Byte number	1h	110h
	1				
	2				
	3				
	4				
	5				
	6				
	7	P5IOSEL	Port I/O selection 0h: Input port setting, 1h: Output port setting	0h	
18	0	P5CAT	Category number specification 0h: port unused, 1h to 24h: category number	Eh	111h
	1				
	2				
	3				
	4				
	5	P5LSB	Input port setting 0h to 7h: LSB specification of the data (Max. 16 bits) Output port setting 0h to 7h: Output bit specification	3h	
	6				
7					
19	0	P6ADJ	Input port setting: coefficient setting, output port setting: unused	0h	112h
	1				
	2				
	3				
	4	P6WID	Bit width specification 0h to 15h: bit width (1 bit width to 16 bits width)	0h	
	5				
	6				
7					
20	0	P6BYTE	Byte number specification 0h: unused, 1h to 127h: Byte number	2h	113h
	1				
	2				
	3				
	4				
	5				
	6				
	7	P6IOSEL	Port I/O selection 0h: Input port setting, 1h: Output port setting	0h	

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
21	0	P6CAT	Category number specification 0h: port unused, 1h to 24h: category number	Eh	114h
	1				
	2				
	3				
	4	P6LSB	Input port setting 0h to 7h: LSB specification of the data (Max. 16 bits) Output port setting 0h to 7h: Output bit specification	0h	
	5				
	6				
22	7	P7ADJ	Input port setting: coefficient setting, output port setting: unused	0h	115h
	0				
	1				
	2				
	3	P7WID	Bit width specification 0h to 15h: bit width (1 bit width to 16 bits width)	0h	
	4				
	5				
6					
23	7	P7IOSEL	Port I/O selection 0h: Input port setting, 1h: Output port setting	0h	116h
	0				
	1				
	2				
	3				
	4				
	5				
24	6	P7CAT	Category number specification 0h: port unused, 1h to 24h: category number	Eh	117h
	7				
	0				
	1				
	2	P7LSB	Input port setting 0h to 7h: LSB specification of the data (Max. 16 bits) Output port setting 0h to 7h: Output bit specification	4h	
	3				
	4				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
25	0	P8ADJ	Input port setting: coefficient setting, output port setting: unused	0h	118h
	1				
	2				
	3	P8WID	Bit width specification 0h to 15h: bit width (1 bit width to 16 bits width)	0h	
	4				
	5				
	6				
7	26	P8BYTE	Byte number specification 0h: unused, 1h to 127h: Byte number	1h	
0					
1					
2					
3					
4					
5		P8IOSEL	Port I/O selection 0h: Input port setting, 1h: Output port setting	0h	
27	0	P8CAT	Category number specification 0h: port unused, 1h to 24h: category number	Eh	11Ah
	1				
	2				
	3				
	4	P8LSB	Input port setting 0h to 7h: LSB specification of the data (Max. 16 bits) Output port setting 0h to 7h: Output bit specification	0h	
	5				
	6				
7	28	P9ADJ	Input port setting: coefficient setting, output port setting: unused	0h	
0					
1					
2		P9WID	Bit width specification 0h to 15h: bit width (1 bit width to 16 bits width)	0h	
3					
4					
5					
6					
7					

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
29	0	P9BYTE	Byte number specification 0h: unused, 1h to 127h: Byte number	1h	11Ch
	1				
	2				
	3				
	4				
	5				
	6				
	7	P9IOSEL	Port I/O selection 0h: Input port setting, 1h: Output port setting	0h	
30	0	P9CAT	Category number specification 0h: port unused, 1h to 24h: category number	Eh	11Dh
	1				
	2				
	3				
	4				
	5	P9LSB	Input port setting 0h to 7h: LSB specification of the data (Max. 16 bits) Output port setting 0h to 7h: Output bit specification	6h	
	6				
7					
31	0	P10ADJ	Input port setting: coefficient setting, output port setting: unused	0h	11Eh
	1				
	2				
	3				
	4	P10WID	Bit width specification 0h to 15h: bit width (1 bit width to 16 bits width)	0h	
	5				
	6				
	7				
32	0	P10BYTE	Byte number specification 0h: unused, 1h to 127h: Byte number	11h	11Fh
	1				
	2				
	3				
	4				
	5				
	6				
		7	P10IOSEL	Port I/O selection 0h: Input port setting, 1h: Output port setting	

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
33	0	P10CAT	Category number specification 0h: port unused, 1h to 24h: category number	11h	120h
	1				
	2				
	3				
	4	P10LSB	Input port setting 0h to 7h: LSB specification of the data (Max. 16 bits) Output port setting 0h to 7h: Output bit specification	0h	
	5				
	6				
34	7	P11ADJ	Input port setting: coefficient setting, output port setting: unused	0h	121h
	0				
	1				
	2				
	3	P11WID	Bit width specification 0h to 15h: bit width (1 bit width to 16 bits width)	0h	
	4				
	5				
6					
35	7	P11IOSEL	Port I/O selection 0h: Input port setting, 1h: Output port setting	0h	122h
	0				
	1				
	2				
	3				
	4				
	5				
36	6	P11CAT	Category number specification 0h: port unused, 1h to 24h: category number	Ch	123h
	7				
	0				
	1				
	2	P11LSB	Input port setting 0h to 7h: LSB specification of the data (Max. 16 bits) Output port setting 0h to 7h: Output bit specification	0h	
	3				
	4				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
37	0	P12ADJ	Input port setting: coefficient setting, output port setting: unused	0h	124h
	1				
	2				
	3	P12WID	Bit width specification 0h to 15h: bit width (1 bit width to 16 bits width)	0h	
	4				
	5				
	6				
38	7	P12BYTE	Byte number specification 0h: unused, 1h to 127h: Byte number	1h	
	0				
	1				
	2				
	3				
	4				
	5				
6	P12IOSEL	Port I/O selection 0h: Input port setting, 1h: Output port setting	0h		
39	0	P12CAT	Category number specification 0h: port unused, 1h to 24h: category number	Ch	126h
	1				
	2				
	3				
	4	P12LSB	Input port setting 0h to 7h: LSB specification of the data (Max. 16 bits) Output port setting 0h to 7h: Output bit specification	0h	
	5				
	6				
40	7	P13ADJ	Input port setting: coefficient setting, output port setting: unused	0h	127h
	0				
	1				
	2	P13WID	Bit width specification 0h to 15h: bit width (1 bit width to 16 bits width)	0h	
	3				
	4				
	5				
6					
7					

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
41	0	P13BYTE	Byte number specification 0h: unused, 1h to 127h: Byte number	1h	128h
	1				
	2				
	3				
	4				
	5				
	6				
	7	P13IOSEL	Port I/O selection 0h: Input port setting, 1h: Output port setting	0h	
42	0	P13CAT	Category number specification 0h: port unused, 1h to 24h: category number	Ch	129h
	1				
	2				
	3				
	4				
	5	P13LSB	Input port setting 0h to 7h: LSB specification of the data (Max. 16 bits) Output port setting 0h to 7h: Output bit specification	1h	
	6				
7					
43	0	P14ADJ	Input port setting: coefficient setting, output port setting: unused	0h	12Ah
	1				
	2				
	3				
	4	P14WID	Bit width specification 0h to 15h: bit width (1 bit width to 16 bits width)	0h	
	5				
	6				
7					
44	0	P14BYTE	Byte number specification 0h: unused, 1h to 127h: Byte number	1h	12Bh
	1				
	2				
	3				
	4				
	5				
	6				
	7	P14IOSEL	Port I/O selection 0h: Input port setting, 1h: Output port setting	0h	

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
45	0	P14CAT	Category number specification 0h: port unused, 1h to 24h: category number	Ch	12Ch
	1				
	2				
	3				
	4	P14LSB	Input port setting 0h to 7h: LSB specification of the data (Max. 16 bits) Output port setting 0h to 7h: Output bit specification	2h	
	5				
	6				
46	7	P15ADJ	Input port setting: coefficient setting, output port setting: unused	0h	12Dh
	0				
	1				
	2				
	3	P15WID	Bit width specification 0h to 15h: bit width (1 bit width to 16 bits width)	0h	
	4				
	5				
6					
47	7	P15IOSEL	Port I/O selection 0h: Input port setting, 1h: Output port setting	0h	12Eh
	0				
	1				
	2				
	3				
	4				
	5				
48	6	P15CAT	Category number specification 0h: port unused, 1h to 24h: category number	Ch	12Fh
	7				
	0				
	1				
	2	P15LSB	Input port setting 0h to 7h: LSB specification of the data (Max. 16 bits) Output port setting 0h to 7h: Output bit specification	3h	
	3				
	4				

Category 21: BLMDDET2

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
1	0	BLMDDETAGC	AGC gain setting in Static detection operation	0h	130h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
2	0	OPSHBLMDDET	Static auto EEPROM write function ON/OFF 0h: off, 1h: on	0h	131h
	1	Fix	"0h" fix	0h	
	2				
	3				
	4				
	5				
	6				
	7				

Category 22: SOUT1

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
1	0	EXHDET	Ext-HD input detection flag 1h: input detected	0h	—
	1	EXVDET	Ext-VD input detection flag 1h: input detected	0h	
	2	EXSDET	Ext-Sync input detection flag 1h: input detected	0h	
	3	EXBDET	Ext-Burst input detection flag 1h: input detected	0h	
	4	Fix	"0h" fix	0h	
	5				
	6				
	7				
2	0	AJSTOUTL	AD sampling data output (LSB)	0h	—
	1				
	2				
	3				
	4				
	5				
	6				
	7				
3	0	AJSTOUTM	AD sampling data output (MSB)	0h	—
	1				
	2	dummy	"1h" fix	1h	
	3	Fix	"0h" fix	0h	
	4				
	5				
	6				
	7				
4	0	OBLKS1L	Black clamp signal (S1) integration value (LSB)	0h	—
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
5	0	OBLKS2L	Black clamp signal (S2) integration value (LSB)	0h	—
	1				
	2				
	3				
	4				
	5				
	6				
6	0	OBLKS1M	Black clamp signal (S1) integration value (MSB)	0h	—
	1				
	2				
	3				
	4	OBLKS2M	Black clamp signal (S2) integration value (MSB)	0h	
	5				
	6				
	7				
7	0	INTY0L	Luminance integration value of the window 0 (LSB)	0h	—
	1				
	2				
	3				
	4				
	5				
	6				
	7				
8	0	INTY0M	Luminance integration value of the window 0 (MSB)	0h	—
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
9	0	INTY1L	Luminance integration value of the window 1 (LSB)	0h	—
	1				
	2				
	3				
	4				
	5				
	6				
	7				
10	0	INTY1M	Luminance integration value of the window 1 (MSB)	0h	—
	1				
	2				
	3				
	4				
	5				
	6				
	7				
11	0	INTY2L	Luminance integration value of the window 2 (LSB)	0h	—
	1				
	2				
	3				
	4				
	5				
	6				
	7				
12	0	INTY2M	Luminance integration value of the window 2 (MSB)	0h	—
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
13	0	INTY3L	Luminance integration value of the window 3 (LSB)	0h	—
	1				
	2				
	3				
	4				
	5				
	6				
14	0	INTY3M	Luminance integration value of the window 3 (MSB)	0h	—
	1				
	2				
	3				
	4				
	5				
	6				
15	0	INTY4L	Luminance integration value of the window 4 (LSB)	0h	—
	1				
	2				
	3				
	4				
	5				
	6				
16	0	INTY4M	Luminance integration value of the window 4 (MSB)	0h	—
	1				
	2				
	3				
	4				
	5				
	6				
7					

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
17	0	Fix	"0h" fix	0h	—
	1				
	2				
	3				
	4				
	5				
	6				
	7				
18	0	Fix	"0h" fix	0h	—
	1				
	2				
	3				
	4				
	5				
	6				
	7				
19	0	Fix	"0h" fix	0h	—
	1				
	2				
	3				
	4				
	5				
	6				
	7				
20	0	Fix	"0h" fix	0h	—
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
21	0	Fix	"0h" fix	0h	—
	1				
	2				
	3				
	4				
	5				
	6				
	7				
22	0	Fix	"0h" fix	0h	—
	1				
	2				
	3				
	4				
	5				
	6				
	7				
23	0	Fix	"0h" fix	0h	—
	1				
	2				
	3				
	4				
	5				
	6				
	7				
24	0	Fix	"0h" fix	0h	—
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
25	0	Fix	"0h" fix	0h	—
	1				
	2				
	3				
	4				
	5				
	6				
	7				
26	0	Fix	"0h" fix	0h	—
	1				
	2				
	3				
	4				
	5				
	6				
	7				
27	0	Fix	"0h" fix	0h	—
	1				
	2				
	3				
	4				
	5				
	6				
	7				
28	0	Fix	"0h" fix	0h	—
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
29	0	Fix	"0h" fix	0h	—
	1				
	2				
	3				
	4				
	5				
	6				
	7				
30	0	Fix	"0h" fix	0h	—
	1				
	2				
	3				
	4				
	5				
	6				
	7				
31	0	Fix	"0h" fix	0h	—
	1				
	2				
	3				
	4				
	5				
	6				
	7				
32	0	Fix	"0h" fix	0h	—
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
33	0	Fix	"0h" fix	0h	—
	1				
	2				
	3				
	4				
	5				
	6				
	7				
34	0	Fix	"0h" fix	0h	—
	1				
	2				
	3				
	4				
	5				
	6				
	7				
35	0	Fix	"0h" fix	0h	—
	1				
	2				
	3				
	4				
	5				
	6				
	7				
36	0	Fix	"0h" fix	0h	—
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
37	0	Fix	"0h" fix	0h	—
	1				
	2				
	3				
	4				
	5				
	6				
	7				
38	0	Fix	"0h" fix	0h	—
	1				
	2				
	3				
	4				
	5				
	6				
	7				
39	0	Fix	"0h" fix	0h	—
	1				
	2				
	3				
	4				
	5				
	6				
	7				
40	0	Fix	"0h" fix	0h	—
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
41	0	Fix	"0h" fix	0h	—
	1				
	2				
	3				
	4				
	5				
	6				
	7				
42	0	Fix	"0h" fix	0h	—
	1				
	2				
	3				
	4				
	5				
	6				
	7				
43	0	INTRL	Red integration value (LSB)	0h	—
	1				
	2				
	3				
	4				
	5				
	6				
	7				
44	0	INTR	Red integration value (MID)	0h	—
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
45	0	INTRM	Red integration value (MSB)	0h	—
	1				
	2				
	3				
	4				
	5				
	6				
46	0	INTGL	Green integration value (LSB)	0h	—
	1				
	2				
	3				
	4				
	5				
	6				
	7				
47	0	INTG	Green integration value (MID)	0h	—
	1				
	2				
	3				
	4				
	5				
	6				
	7				
48	0	INTGM	Green integration value (MSB)	0h	—
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
49	0	INTBL	Blue integration value (LSB)	0h	—
	1				
	2				
	3				
	4				
	5				
	6				
	7				
50	0	INTB	Blue integration value (MID)	0h	—
	1				
	2				
	3				
	4				
	5				
	6				
	7				
51	0	INTBM	Blue integration value (MSB)	0h	—
	1				
	2				
	3				
	4				
	5				
	6				
	7				
52	0	AWBCNTL	Integration value of pixels (LSB)	0h	—
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
53	0	AWBCNTM	Integration value of pixels (MSB)	0h	—
	1				
	2				
	3				
	4				
	5				
	6				
54	0	YOUTL	Y sampling data output (LSB)	0h	—
	1				
	2				
	3				
	4				
	5				
	6				
	7				
55	0	RYOUTL	R-Y sampling data output (LSB)	0h	—
	1				
	2				
	3				
	4				
	5				
	6				
	7				
56	0	BYOUTL	B-Y sampling data output (LSB)	0h	—
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
57	0	YOUTM	Y sampling data output (MSB)	0h	—
	1	RYOUTM	R-Y sampling data output (MSB)	0h	
	2	BYOUTM	B-Y sampling data output (MSB)	0h	
	3	Fix	"0h" fix	0h	
	4				
	5				
	6				
7					

Category 23: SOUT2

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
1	0	AWBOUT1	AWB adjustment data (monitor mode: WBR, co-process mode: R-G) (LSB)	0h	—
	1				
	2				
	3				
	4				
	5				
	6				
	7				
2	0	AWBOUT2	AWB adjustment data (monitor mode: WBG, co-process mode: R-G) (MSB)	0h	—
	1				
	2				
	3				
	4				
	5				
	6				
	7				
3	0	AWBOUT3	AWB adjustment data (monitor mode: WBG, co-process mode: B-G) (LSB)	0h	—
	1				
	2				
	3				
	4				
	5				
	6				
	7				
4	0	AWBOUT4	AWB adjustment data (monitor mode: —, co-process mode: B-G) (MSB)	0h	—
	1				
	2				
	3				
	4				
	5				
	6				
	7				

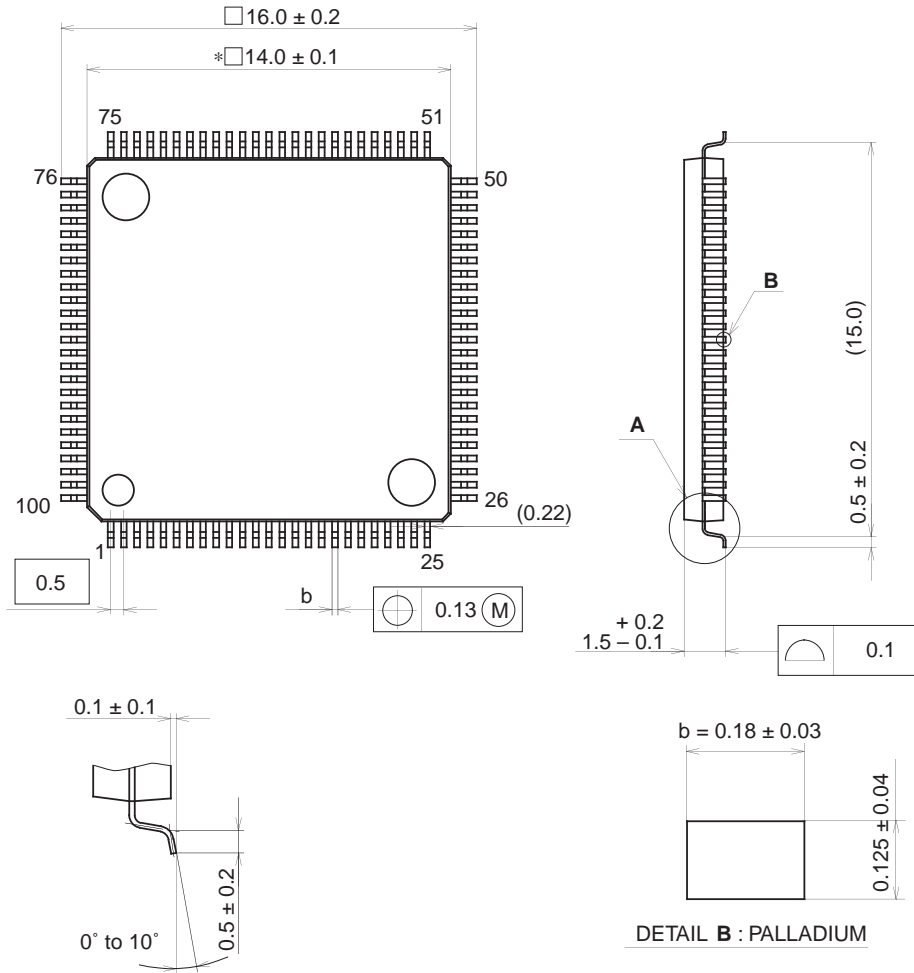
Byte	Bit	Parameter name	Description	Initial value	EEPROM address
5	0	AESCLL	AE SCALE (LSB)	0h	—
	1				
	2				
	3				
	4				
	5				
	6				
	7				
6	0	AESCLM	AE SCALE (MSB)	0h	—
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Category 24: TEST

Byte	Bit	Parameter name	Description	Initial value	EEPROM address
1	0	Fix	"0h" fix	0h	1E6h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
2	0	Fix	"0h" fix	0h	1E7h
	1				
	2				
	3				
	4				
	5				
	6				
	7				
3	0	Fix	"0h" fix	0h	1E8h
	1				
	2				
	3				
	4				
	5				
	6				
	7				

Package Outline Unit: mm

100PIN LQFP (PLASTIC)



NOTE: Dimension "*" does not include mold protrusion.

DETAIL A

DETAIL B : PALLADIUM

PACKAGE STRUCTURE

SONY CODE	LQFP-100P-L01
EIAJ CODE	P-LQFP100-14x14-0.5
JEDEC CODE	_____

PACKAGE MATERIAL	EPOXY RESIN
LEAD TREATMENT	PALLADIUM PLATING
LEAD MATERIAL	COPPER ALLOY
PACKAGE MASS	0.7g