

HCMOS — Logic

Digital Delay Units

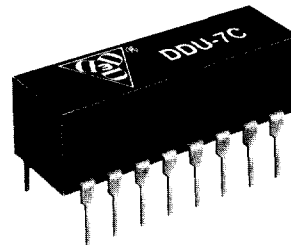
SERIES: DDU-7C

10 Outputs
16 pins DIP



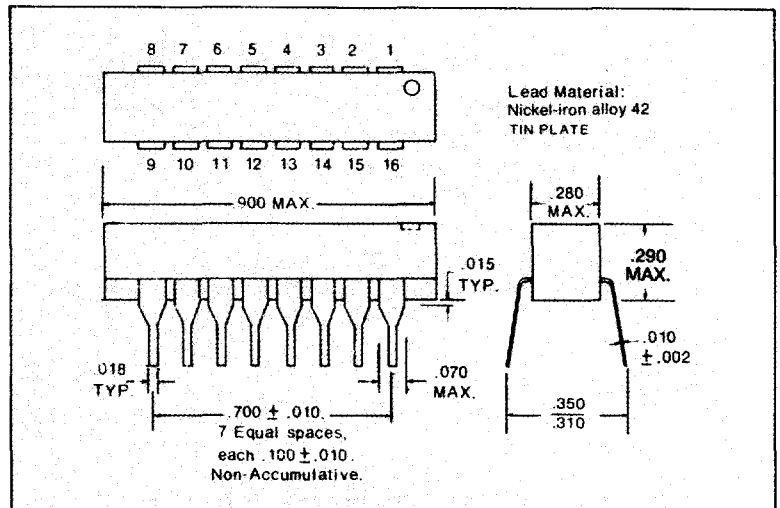
Features:

- Completely interfaced for HCMOS application
- No external components required
- P.C. board space economy achieved
- Fits standard 16 pins DIP socket
- Operates over commercial temperature range



Specifications:

- **No. Taps:** 10 equally spaced taps.
- **Total delay tolerance:** $\pm 5\%$ or 2 ns whichever is greater.
- **Rise time:** 8 ns typ.
- **Temperature coefficient:** 300 PPM/ $^{\circ}$ C.
- **Temperature range:** 0° C to $+ 70^{\circ}$ C.
- **Supply Voltage:** 5 Vdc $\pm 5\%$.
- **Supply current:**
 $I_{CCH} = 10$ ma
 $I_{CCL} = 40$ μ a
- **Fan-out:** 10 LSTTL loads min.
- **DC Parameters:** See TTL-HCMOS Logic Table on Page 6.



Part No.	Total Delay (ns)	Delay Per Tap (ns)
*DDU-7C-25	22.5	2.5 \pm .7
DDU-7C-100	100	10.0 \pm 2
DDU-7C-150	150	15.0 \pm 2
DDU-7C-200	200	20.0 \pm 2
DDU-7C-250	250	25.0 \pm 2
DDU-7C-300	300	30.0 \pm 3
DDU-7C-400	400	40.0 \pm 4
DDU-7C-500	500	50.0 \pm 5

*Time delay referenced to 1st tap.
8 ns \pm 2 ns inherent delay.

