

DIM200MHS17-A000

Half Bridge IGBT Module

Replaces issue February 2002, version DS5459-3.0

DS5459-4.0 March 2002

FEATURES

- 10µs Short Circuit Withstand
- Non Punch Through Silicon
- Isolated Copper Base

APPLICATIONS

- Inverters
- Motor Controllers
- Traction Drives

The Powerline range of modules includes half bridge, chopper, dual and single switch configurations covering voltages from 600V to 3300V and currents up to 2400A.

The DIM200MHS17-A000 is a half bridge 1700V, n channel enhancement mode, insulated gate bipolar transistor (IGBT) module. The IGBT has a wide reverse bias safe operating area (RBSOA) plus full 10 μ s short circuit withstand.

The module incorporates an electrically isolated base plate and low inductance construction enabling circuit designers to optimise circuit layouts and utilise grounded heat sinks for safety.

ORDERING INFORMATION

Order As:

DIM200MHS17-A000

Note: When ordering, please use the whole part number.

KEY PARAMETERS

V _{ces}		1700V
V _{CE(sat)} *	(typ)	2.7V
I _C	(max)	200A
I C(BK)	(max)	400A

^{*(}measured at the power busbars and not the auxiliary terminals)

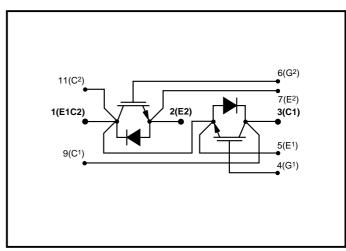


Fig. 1 Half bridge circuit diagram

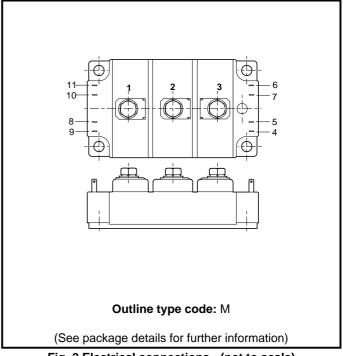


Fig. 2 Electrical connections - (not to scale)



ABSOLUTE MAXIMUM RATINGS - PER ARM

Stresses above those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture of the package. Appropriate safety precautions should always be followed. Exposure to Absolute Maximum Ratings may affect device reliability.

T_{case} = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
V _{CES}	Collector-emitter voltage	V _{GE} = 0V	1700	V
V_{GES}	Gate-emitter voltage	-	±20	V
I _c	Continuous collector current	$T_{case} = 65^{\circ}C$	200	А
I _{C(PK)}	Peak collector current	1ms, T _{case} = 110°C	400	А
P _{max}	Max. transistor power dissipation	$T_{case} = 25^{\circ}C, T_{j} = 150^{\circ}C$	1488	W
l²t	Diode I ² t value	$V_R = 0, t_p = 10 \text{ms}, T_{vj} = 125^{\circ}\text{C}$	7.5	kA ² s
V_{isol}	Isolation voltage - per module	Commoned terminals to base plate. AC RMS, 1 min, 50Hz	4000	V



THERMAL AND MECHANICAL RATINGS

Internal insulation material: Al₂O₃
Baseplate material: Cu
Creepage distance: 20mm
Clearance: 8mm
CTI (Critical Tracking Index): 175

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
R _{th(j-c)}	Thermal resistance - transistor (per arm)	Continuous dissipation -	-	-	84	°C/kW
		junction to case				
R _{th(j-c)}	Thermal resistance - diode (per arm)	Continuous dissipation -	-	-	176	°C/kW
		junction to case				
R _{th(c-h)}	Thermal resistance - case to heatsink	Mounting torque 5Nm	-	-	15	°C/kW
	(per module)	(with mounting grease)				
T _j	Junction temperature	Transistor	-	-	150	°C
		Diode	-	-	125	°C
T _{stg}	Storage temperature range	-	-40	-	125	°C
-	Screw torque	Mounting - M6	-	-	5	Nm
		Electrical connections - M6	-	-	5	Nm



ELECTRICAL CHARACTERISTICS

 $T_{case} = 25$ °C unless stated otherwise.

Symbol	Parameter	Test Conditions		Min.	Тур.	Max.	Units
I _{CES}	Collector cut-off current	$V_{GE} = 0V, V_{CE} = V_{CES}$		-	-	1	mA
		$V_{GE} = 0V, V_{CE} = V_{CES}, T_{case} = 125^{\circ}$	С	-	-	10	mA
I _{GES}	Gate leakage current	$V_{GE} = \pm 20V, V_{CE} = 0V$		-	-	1	μΑ
$V_{\text{GE(TH)}}$	Gate threshold voltage	$I_{\rm C} = 10$ mA, $V_{\rm GE} = V_{\rm CE}$		4.5	5.5	6.5	V
V _{CE(sat)} †	Collector-emitter saturation voltage	V _{GE} = 15V, I _C = 200A		-	2.7	3.2	V
		$V_{GE} = 15V, I_{C} = 200A, T_{case} = 125$	5°C	-	3.4	4.0	V
I _F	Diode forward current	DC		-	-	200	А
I _{FM}	Diode maximum forward current	t _p = 1ms		-	-	400	А
V _F †	Diode forward voltage	I _F = 200A		-	2.2	2.5	V
		I _F = 200A, T _{case} = 125°C		-	2.3	2.6	V
C _{ies}	Input capacitance	V _{CE} = 25V, V _{GE} = 0V, f = 1MHz		-	15	-	nF
L _M	Module inductance - per arm	-		-	30	-	nH
R _{INT}	Internal transistor resistance - per arm	-		-	-	-	mΩ
SC _{Data}	Short circuit. I _{SC}	$T_j = 125^{\circ}C, V_{CC} = 1000V,$	I ₁	-	900	-	Α
		$t_p \le 10 \mu s$, $V_{CE(max)} = V_{CES} - L^*$. di/dt		-	800	-	А
		IEC 60747-9					

Note:

[†] Measured at the power busbars and not the auxiliary terminals)

 $^{^{\}ast}$ L is the circuit inductance + $\rm L_{M}$



ELECTRICAL CHARACTERISTICS

T_{case} = 25°C unless stated otherwise

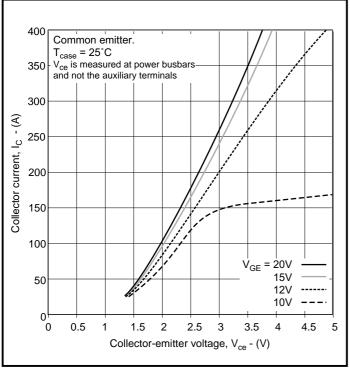
Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
t _{d(off)}	Turn-off delay time	I _C = 200A	-	590	-	ns
t _f	Fall time	$V_{GE} = \pm 15V$	-	300	-	ns
E _{OFF}	Turn-off energy loss	V _{CE} = 900V	-	40	-	mJ
t _{d(on)}	Turn-on delay time	$R_{G(ON)} = R_{G(OFF)} = 4.7\Omega$	-	320	-	ns
t _r	Rise time	L ~ 100nH	-	90	-	ns
E _{on}	Turn-on energy loss		-	50	-	mJ
Q_g	Gate charge		-	-	-	μС
Q _{rr}	Diode reverse recovery charge	I _F = 200A, V _R = 900V,	-	65	-	μС
I _{rr}	Diode reverse current	dl _F /dt = 3000A/μs	-	195	-	А
E _{REC}	Diode reverse recovery energy		-	42	-	mJ

T_{case} = 125°C unless stated otherwise

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
t _{d(off)}	Turn-off delay time	I _C = 200A	-	880	-	ns
t _f	Fall time	V _{GE} = ±15V	-	410	-	ns
E _{OFF}	Turn-off energy loss	V _{CE} = 900V	-	60	-	mJ
t _{d(on)}	Turn-on delay time	$R_{G(ON)} = R_{G(OFF)} = 4.7\Omega$	-	110	-	ns
t _r	Rise time	L ~ 100nH	-	450	-	ns
E _{on}	Turn-on energy loss		-	85	-	mJ
Q _{rr}	Diode reverse recovery charge	I _F = 200A, V _R = 900V,	-	100	-	μС
I _{rr}	Diode reverse current	dI _F /dt = 2500A/μs	-	195	-	Α
E _{rec}	Diode reverse recovery energy		-	64	-	mJ



TYPICAL CHARACTERISTICS



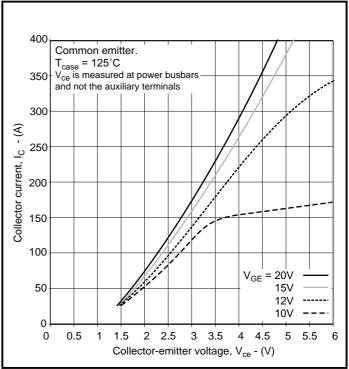
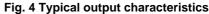


Fig. 3 Typical output characteristics



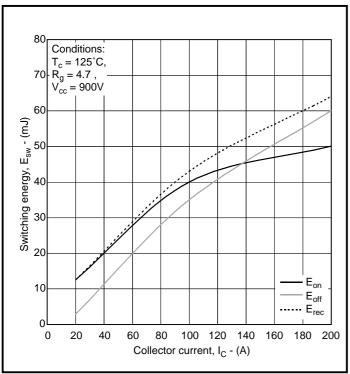


Fig. 5 Typical switching energy vs collector current

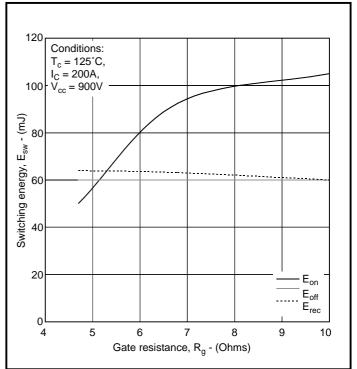
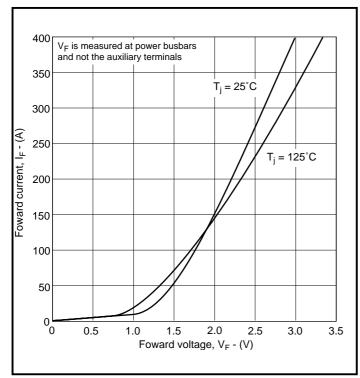


Fig. 6 Typical switching energy vs gate resistance

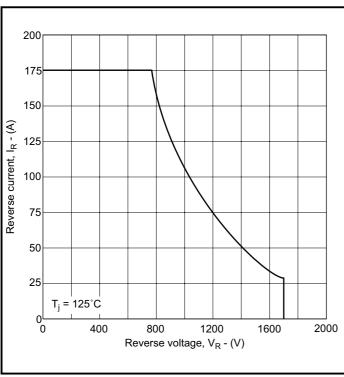


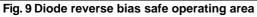


450 400 Chip 350 €300 <u>-</u>0 Module Collector current, IC 250 100 Conditions: $T_{case} = 125^{\circ}C$ $V_{ge}^{case} = 15V$ $R_{g(off)} = 4.7$ ohms 200 400 600 800 1000 1200 1400 1600 1800 Collector emitter voltage, V_{ce} - (V)

Fig. 7 Diode typical forward characteristics

Fig. 8 Reverse bias safe operating area





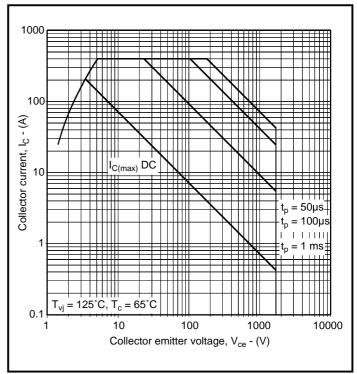
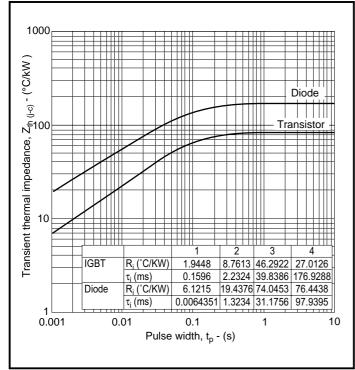


Fig. 10 Forward bias safe operating area





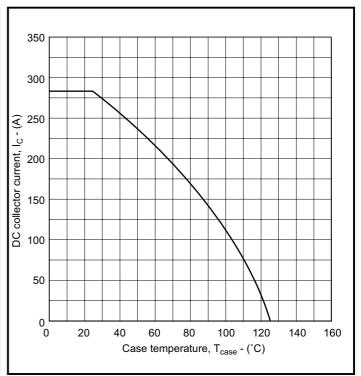


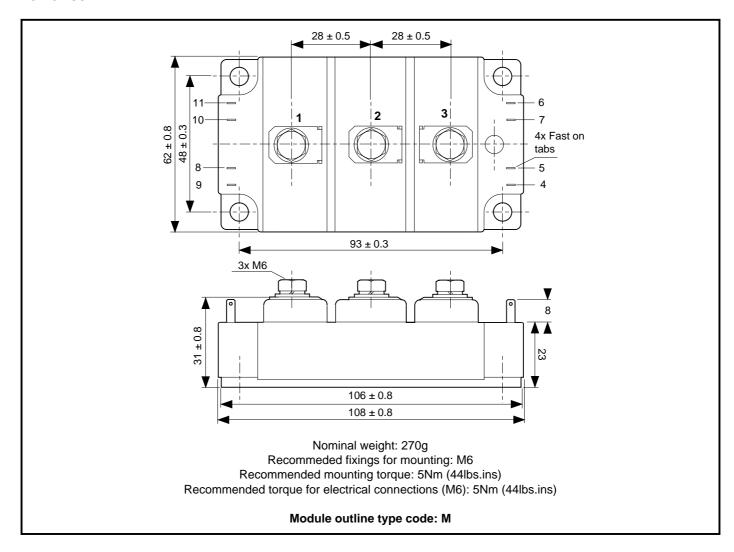
Fig. 11 Transient thermal impedance

Fig. 12 DC current rating vs case temperature



PACKAGE DETAILS

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