

September 1986 Revised February 2000

DM74ALS1005

Hex Inverting Driver with Open Collector Outputs

General Description

These devices contain six independent drivers, each of which performs the logic INVERT/Complement function. The outputs require external pull-up resistors for proper logical operation. The DM74ALS1005 is a driver version of the DM74ALS05A.

Pull-Up Resistor Equations

$$R_{MAX} = \frac{V_{CC} \left(Min\right) - V_{OH}}{N_1 \left(I_{OH}\right) + N_2 \left(I_{IH}\right)}$$

$$R_{MIN} = \frac{V_{CC} \left(Max \right) - V_{OL}}{I_{OL} - N_3 \left(I_{IL} \right)}$$

Where:

 N_1 (I_{OH}) = total maximum output high current

for all outputs tied to pull-up resistor

 N_2 (I_{IH}) = total maximum input high current for

all inputs tied to pull-up resistor

 N_3 (I_{IL}) = total maximum input low current for all inputs tied to pull-up resistor

No (lu) =

Features

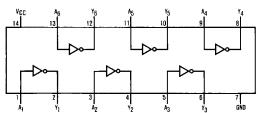
- Switching specifications at 50 pF
- \blacksquare Switching specifications guaranteed over full temperature and V_{CC} range
- Advanced oxide-isolated, ion-implanted Schottky TTL process
- Functionally and pin for pin compatible with Schottky and low power Schottky TTL counterpart
- Improved AC performance over Schottky and low power Schottky counterparts

Ordering Code:

Order Number Package Number			Package Description
	DM74ALS1005M M14A		14-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-012, 0.150 Narrow
	DM74ALS1005N	N14A	14-Lead Plastic Dual-In-Line Package (PDIP) JEDEC MS-001 0 300 Wide

Devices also available in Tape and Reel. Specify by appending the suffix letter "X" to the ordering code

Connection Diagram



Function Table

L = LOW Logic Level H = HIGH Logic Level

Absolute Maximum Ratings(Note 1)

Supply Voltage 7V
Input Voltage 7V
OFF-State Output Voltage 7V

Operating Free Air Temperature Range 0° C to $+70^{\circ}$ C Storage Temperature Range -65° C to $+150^{\circ}$ C

Typical θ_{JA}

 N Package
 76.0°C/W

 M Package
 106.5°C/W

Note 1: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the Electrical Characteristics tables are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Recommended Operating Conditions

Symbol	Parameter	Min	Nom	Max	Units
V _{CC}	Supply Voltage	4.5	5	5.5	V
V _{IH}	HIGH Level Input Voltage	2			V
V _{IL}	LOW Level Input Voltage			0.8	V
V _{OH}	HIGH Level Output Voltage			5.5	V
I _{OL}	LOW Level Output Current			24	mA
T _A	Free Air Operating Temperature	0		70	°C

Electrical Characteristics

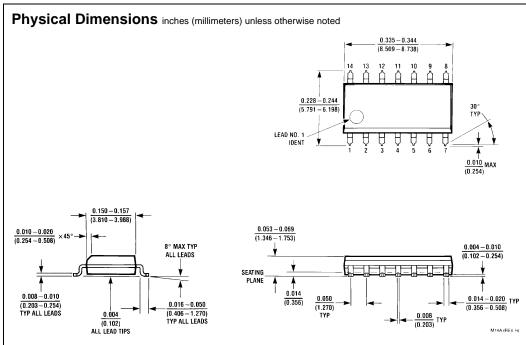
over recommended operating free air temperature range. All typical values are measured at $V_{CC} = 5V$, $T_A = 25^{\circ}C$.

Symbol	Parameter	Conditions		Min	Тур	Max	Units
V _{IK}	Input Clamp Voltage	$V_{CC} = 4.5V, I_I = -18 \text{ mA}$				-1.5	V
I _{OH}	HIGH Level Output Current	$V_{CC} = 4.5V, V_{OH} = 5.5V$				100	μΑ
V _{OL}	LOW Level	V _{CC} = 4.5V	I _{OL} = 12 mA		0.25	0.4	V
	Output Voltage		I _{OL} = 24 mA		0.35	0.5	V
I	Input Current at Maximum	V _{CC} = 5.5V, V _{IH} = 7V				0.1	mA
	Input Voltage	VCC = 3.3 v, VIH = 7 V				0.1	IIIA
I _{IH}	HIGH Level Input Current	$V_{CC} = 5.5V, V_{IH} = 2.7V$				20	μΑ
I _{IL}	LOW Level Input Current	$V_{CC} = 5.5V, V_{IL} = 0.4V$				-0.1	mA
I _{CC}	Supply Current	V _{CC} = 5.5V	Outputs HIGH		0.9	3	mA
			Outputs LOW		7	12	mA

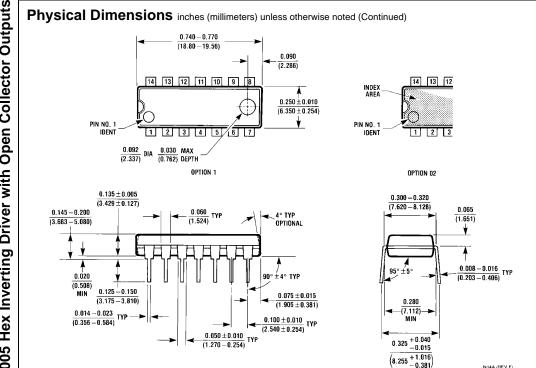
Switching Characteristics

over recommended operating free air temperature range

Symbol	Parameter	Conditions	Min	Max	Units
t _{PLH}	Propagation Delay Time	V _{CC} = 4.5V to 5.5V	5	30	ns
	LOW-to-HIGH Level Output	$R_L = 680\Omega$	3	30	115
t _{PHL}	Propagation Delay Time	$C_L = 50 \text{ pF}$	2	10	20
	HIGH-to-LOW Level Output			10	ns



14-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-012, 0.150 Narrow Package Number M14A



14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide Package Number N14A

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