

April 1984 Revised March 2000

# DM74AS157 • DM74AS158 **Quad 1 of 2 Line Data Selector/Multiplexer**

### **General Description**

These data selectors/multiplexers contain inverters and drivers to supply full on-chip data selection to the four output gates. A separate STROBE input is provided. A 4-bit word is selected from one of two sources and is routed to the four outputs. The AS157 presents true data whereas the AS158 presents inverted data to minimize propagation delay time.

### **Features**

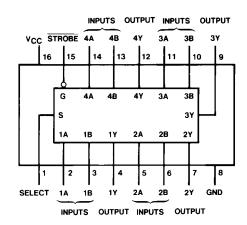
- Switching specifications at 50 pF
- Switching specifications guaranteed over full temperature and  $V_{\mbox{\footnotesize CC}}$  range
- Advanced oxide-isolated, ion-implanted Schottky TTL
- Functionally and pin for pin compatible with Schottky, low power Schottky, and advanced low power Schottky TTL counterpart
- Improved AC performance over Schottky, low power Schottky, and advanced low power Schottky counter-
- Expand any data input point
- Multiplex dual data buses
- General four functions of two variables (one variable is common)
- Source programmable counters

### **Ordering Code:**

Order Number	Package Number	Package Description
DM74AS157M	M16A	16-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-012, 0.150 Narrow
DM74AS157SJX	M16D	16-Lead Small Outline Package (SOP), EIAJ TYPE II, 5.3mm Wide
DM74AS157N	N16E	16-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide
DM74AS158M	M16A	16-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-012, 0.150 Narrow
DM74AS158N	N16E	16-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide

Devices also available in Tape and Reel. Specify by appending the suffix letter "X" to the ordering code.

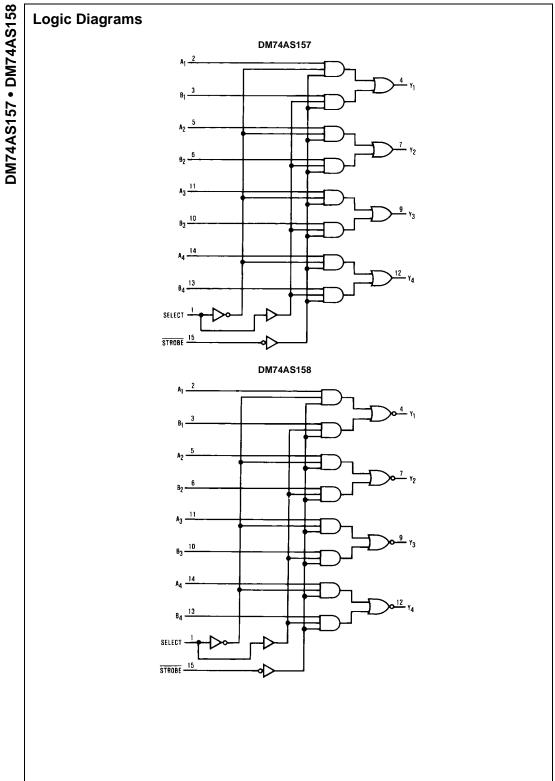
### **Connection Diagram**



### **Function Table**

Inputs				Output Y			
STROBE	Select	Α	В	DM74AS157	DM74AS158		
Н	Х	Χ	Χ	L	Н		
L	L	L	Χ	L	Н		
L	L	Н	Χ	Н	L		
L	Н	Χ	L	L	Н		
L	Н	Χ	Н	Н	L		

- H = HIGH Level
- L = LOW Level
- X = Don't Care



### **Absolute Maximum Ratings**(Note 1)

Supply Voltage 7V Input Voltage 7V Operating Free Air Temperature Range 0°C to +70°C

Storage Temperature Range -65°C to +150°C

Typical  $\theta_{JA}$ 

N Package 75.0 °C/W

Note 1: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the Electrical Characteristics tables are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

### **Recommended Operating Conditions**

Symbol	Parameter	Min	Nom	Max	Units
V <sub>CC</sub>	Supply Voltage	4.5	5	5.5	V
V <sub>IH</sub>	HIGH Level Input Voltage	2			V
V <sub>IL</sub>	LOW Level Input Voltage			0.8	V
I <sub>OH</sub>	HIGH Level Output Current			-2	mA
I <sub>OL</sub>	LOW Level Output Current			20	mA
T <sub>A</sub>	Free Air Operating Temperature	0		70	°C

### **Electrical Characteristics**

over recommended operating free air temperature range. All typical values are measured at  $V_{CC} = 5V$ ,  $T_A = 25^{\circ}C$ .

Symbol	Parameter	Conditions		Min	Тур	Max	Units		
V <sub>IK</sub>	Input Clamp Voltage	$V_{CC} = 4.5V, I_I = -18 \text{ mA}$					-1.2	V	
V <sub>OH</sub>	HIGH Level	$V_{CC} = 4.5V \text{ to } 5.$	5V		V 2			V	
	Output Voltage	$I_{OH} = -2 \text{ mA}$			V <sub>CC</sub> – 2			V	
V <sub>OL</sub>	LOW Level	$V_{CC} = 4.5V$				0.05	0.5	.,	
	Output Voltage	$I_{OL} = 20 \text{ mA}$	I <sub>OL</sub> = 20 mA			0.35	0.5	V	
I	Input Current at Max	$V_{CC} = 5.5V$	5V Select				0.2	mA	
	Input Voltage	$V_{IH} = 7V$		All Others			0.1		
I <sub>IH</sub>	HIGH Level	$V_{CC} = 5.5V$ Select $V_{IH} = 2.7V$ All Others		Select			40		
	Input Current			All Others			20	μΑ	
I <sub>IL</sub>	LOW Level	$V_{CC} = 5.5V$	V <sub>CC</sub> = 5.5V Select				-1	Λ	
	Input Current	$V_{IL} = 0.4V$		All Others			-0.5	mA	
I <sub>O</sub> (Note 2)	Output Drive Current	V <sub>CC</sub> = 5.5V, V <sub>O</sub> = 2.25V		-30		-112	mA		
I <sub>CC</sub>	Supply Current	V <sub>CC</sub> = 5.5V DM74AS157 DM74AS158		\S157		17.5	28	mA	
					15.6	22.5	mA		

Note 2: The output conditions have been chosen to produce a current that closely approximates one half of the true short circuit current, I<sub>OS</sub>.

## **DM74AS157 Switching Characteristics**

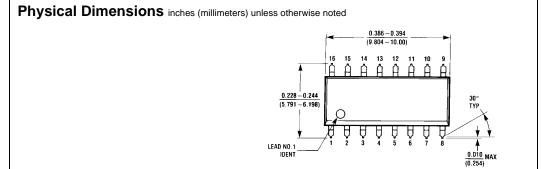
over recommended operating free air temperature range

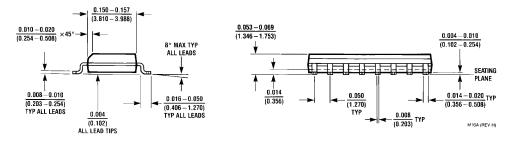
Symbol	Parameter	Conditions	From (Input)	To (Output)	Min	Max	Units
t <sub>PLH</sub>	Propagation Delay Time, LOW-to-HIGH Level Output	$V_{CC} = 4.5V$ to 5.5V, $C_L = 50$ pF,	Data	Υ	1	6	ns
t <sub>PHL</sub>	Propagation Delay Time, HIGH-to-LOW Level Output	$R_L = 500\Omega$	Data	Y	1	5.5	ns
t <sub>PLH</sub>	Propagation Delay Time, LOW-to-HIGH Level Output		STROBE	Υ	2	10.5	ns
t <sub>PHL</sub>	Propagation Delay Time, HIGH-to-LOW Level Output		STROBE	Υ	2	7.5	ns
t <sub>PLH</sub>	Propagation Delay Time, LOW-to-HIGH Level Output		Select	Y	2	11	ns
t <sub>PHL</sub>	Propagation Delay Time, HIGH-to-LOW Level Output		Select	Y	2	10	ns

### **DM74AS158 Switching Characteristics**

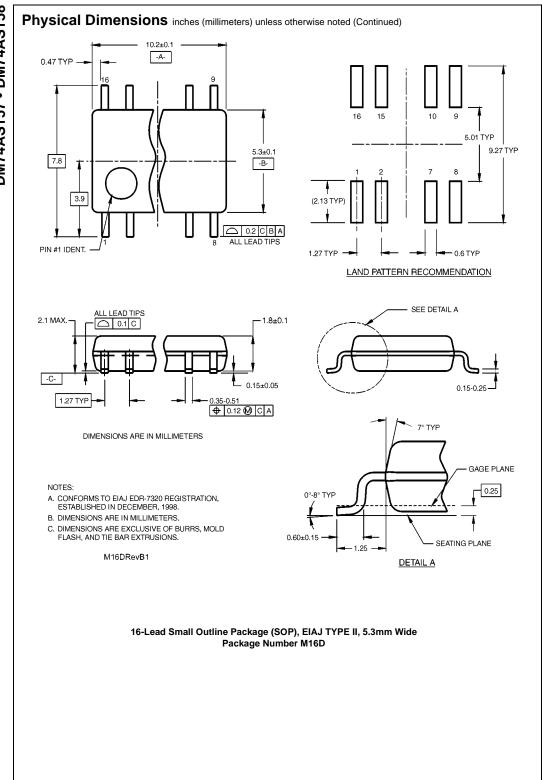
over recommended operating free air temperature range

Symbol	Parameter	Conditions	From	To (Output)	Min	Max	Units
			(Input)				
t <sub>PLH</sub>	Propagation Delay Time, LOW-to-HIGH Level Output	V <sub>CC</sub> = 4.5V to 5.5V,	Data	Υ	1	5	ns
	'	$C_L = 50 \text{ pF},$					
t <sub>PHL</sub>	Propagation Delay Time, HIGH-to-LOW Level Output	$R_L = 500\Omega$	Data	Y	1	4.5	ns
t <sub>PLH</sub>	Propagation Delay Time, LOW-to-HIGH Level Output		STROBE	Y	2	6.5	ns
t <sub>PHL</sub>	Propagation Delay Time, HIGH-to-LOW Level Output		STROBE	Y	2	10	ns
t <sub>PLH</sub>	Propagation Delay Time, LOW-to-HIGH Level Output		Select	Y	2	9.5	ns
t <sub>PHL</sub>	Propagation Delay Time, HIGH-to-LOW Level Output		Select	Υ	2	10.5	ns

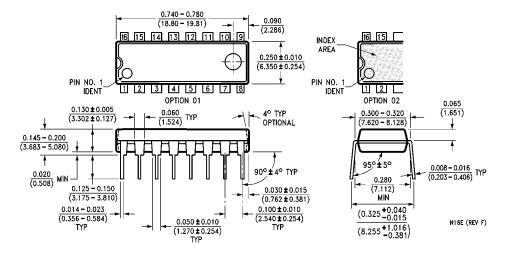




16-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-012, 0.150 Narrow Package Number M16A



### Physical Dimensions inches (millimeters) unless otherwise noted (Continued)



16-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide Package Number N16E

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