

-100mA / -50V Digital transistors

(with built-in resistors)

DTA143EM / DTA143EE / DTA143EUA / DTA143EKA

Applications

Inverter, Interface, Driver

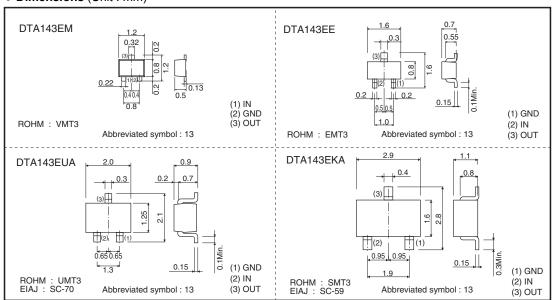
Features

- 1)Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- 2)The bias resistors consist of thin-film resistors with complete isolation to allow positive biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- 3)Only the on/off conditions need to be set for operation, making the device design easy.

Structure

PNP epitaxial planar silicon transistor (Resistor built-in type)

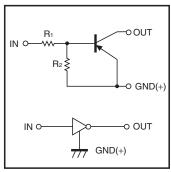
• Dimensions (Unit : mm)



Packaging specifications

	Package	VMT3	EMT3	UMT3	SMT3
	Packaging type	Taping	Taping	Taping	Taping
Code		T2L	TL	T106	T146
Part No.	Basic ordering unit (pieces)	8000	3000	3000	3000
DTA143EM		0	_	-	_
DTA143EE		-	0	-	_
DTA143EUA		-	-	0	_
DTA143EKA		_	_	_	0

• Inner circuit



 $R_1=R_2=4.7k\Omega$

● Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Lir	Unit	
		DTA143EM DTA143EE	DTA143EUA DTA143EKA	
Supply voltage	Vcc	_	V	
Input voltage	VIN	-30 t	V	
Output current	lo	-1	mA	
	IC(Max.)	-1		
Power dissipation	Po	150 200		mW
Junction temperature	Tj	1	°C	
Storage temperature	Tstg	-55 to +150		

• Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Innuit valtaga	VI(off)	-	-	-0.5	V	Vcc=-5V, Io=-100μA
Input voltage	VI(on)	-3	-	_		Vo=-0.3V, Io=-20mA
Output voltage	V _{O(on)}	-	-0.1	-0.3	V	lo/l=-10mA/-0.5mA
Input current	lı	-	-	-1.8	mA	Vi=-5V
Output current	IO(off)	-	-	-0.5	μΑ	Vcc=-50V, Vi=0V
DC current gain	Gı	30	-	-	-	Vo=-5V, Io=-10mA
Input resistance	R ₁	3.29	4.7	6.11	kΩ	_
Resistance ratio	R2/R1	0.8	1	1.2	-	_
Transition frequency	f⊤ *	-	250	-	MHz	Vce=-10V, Ie=5mA, f=100MHz

^{*} Characteristics of built-in transistor

• Electrical characteristic curves

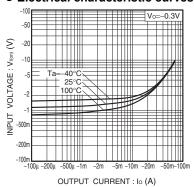


Fig.1 Input voltage vs. output current (ON characteristics)

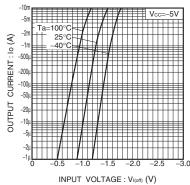


Fig.2 Output current vs. input voltage (OFF characteristics)

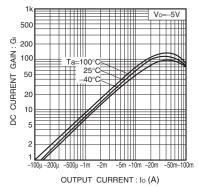


Fig.3 DC current gain vs. output current

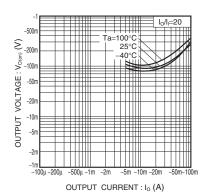


Fig.4 Output voltage vs. output current

Notes

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