Transistors

Digital transistors (built in resistor) DTB143TK / DTB143TS

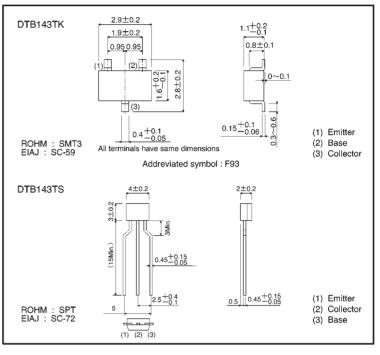
Features

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- The bias resistors consist of thinfilm resistors with complete isolation to allow positive biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- Only the on / off conditions need to be set for operation, making device design easy.

Structure PNP digital transistor

(Built-in resistor type)

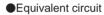


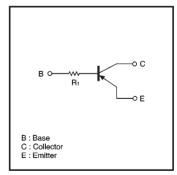


Absolute maximum ratings (Ta = 25°C)

Parameter	Symbol	Limits(D	Unit	
Falameter	Symbol	K S		
Collector-base voltage	Vсво	-5	V	
Collector-emitter voltage	VCEO	-4	V	
Emitter-base voltage	Vebo	-!	V	
Collector current	lc	-500		mA
Collector power dissipation	Pc	200	300	mW
Junction temperature	Tj	150		ĉ
Storage temperature	Tstg	-55~+150		C

ROHM





Electrical characteristics (Ta = 25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Collector-base breakdown voltage	ВУсво	-50	_	_	V	Ic=-50 μ A	
Collector-emitter breakdown voltage	BVCEO	-40	_	_	V	Ic=-1mA	
Emitter-base breakdown voltage	BVEBO	-5	_	_	V	$I_E = -50 \ \mu A$	
Collector cutoff current	Ісво	_	_	-0.5	μA	$V_{CB} = -50V$	
Emitter cutoff current	Іево	_	_	-0.5	μA	V _{EB} =-4V	
Collector-emitter saturation voltage	VCE(sat)	_	_	-0.3	V	Ic/IB=-50mA/-2.5mA	
DC current transfer ratio	hfe	100	250	600	—	$V_{CE}=-5V$, Ic=-50mA	
Input resistance	Rı	3.29	4.7	6.11	kΩ	_	
Transition frequency	f⊤		200	_	MHz	Vce=-10V, le=50mA, f=100MHz *	

* Transition frequency of the device

Packaging specifications

	Package	SMT3	SPT	
	Packaging type	Taping	Taping	
	Code	T146	TP	
Part No.	Basic ordering unit (pieces)	3000	5000	
DTB143TK		0	_	
DTB143TS		—	0	

Electrical characteristic curves

