# Digital transistors (built-in resistors) DTB143TK

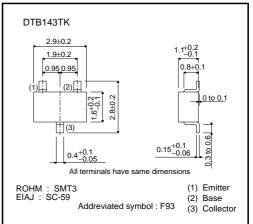
## Features

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- 2) The bias resistors consist of thin-film resistors with complete isolation to allow positive biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- Only the on / off conditions need to be set for operation, making device design easy.

## Structure

PNP digital transistor (Built-in resistor type)

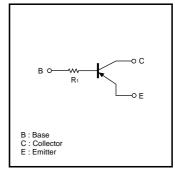
# •External dimensions (Unit : mm)



### Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit
Collector-base voltage	Vсво	-50	V
Collector-emitter voltage	Vceo	-40	V
Emitter-base voltage	Vево	-5	V
Collector current	lc	-500	mA
Collector power dissipation	Pc	200	mW
Junction temperature	Tj	150	°C
Storage temperature	Tstg	-55 to +150	°C

### Equivalent circuit



# Transistors

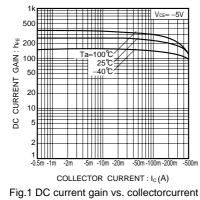
# •Electrical characteristics (Ta=25°C)

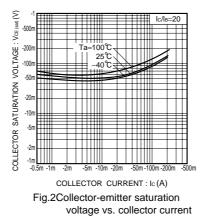
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-base breakdown voltage	ВУсво	-50	-	-	V	Ic=-50µA
Collector-emitter breakdown voltage	BVCEO	-40	_	_	V	Ic=-1mA
Emitter-base breakdown voltage	ВVево	-5	_	_	V	I <sub>E</sub> = -50μA
Collector cutoff current	Ісво	_	_	-0.5	μΑ	Vcb=-50V
Emitter cutoff current	Іево	_	_	-0.5	μA	V <sub>EB</sub> =-4V
Collector-emitter saturation voltage	VCE(sat)	-	-	-0.3	V	Ic/I <sub>B</sub> = -50mA/-2.5mA
DC current transfer ratio	hfe	100	250	600	-	Vce= -5V, Ic= -50mA
Input resistance	R1	3.29	4.7	6.11	kΩ	_
Transition frequency	f⊤	-	200	-	MHz	Vce=-10V, Ie=50mA, f=100MHz *

# Packaging specifications

	Package	SMT3
	Packaging type	Taping
	Code	T146
Part No.	Basic ordering unit (pieces)	3000
DTB143TK	0	

## •Electrical characteristic curves





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