

# SANYO Semiconductors

# DATA SHEET

An ON Semiconductor Company

N-Channel Silicon MOSFET

# **EFC4618R-P** — General-Purpose Switching Device Applications

# **Features**

- 2.5V drive
- · Best suited for LiB charging and discharging switch
- · Common-drain type
- · Protection diode in
- · Halogen free compliance

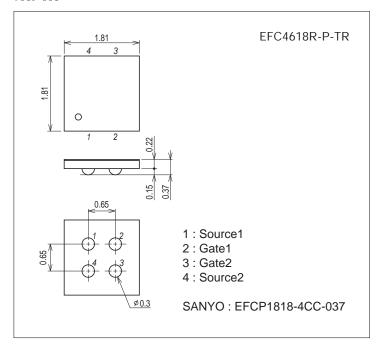
# **Specifications**

# Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Source-to-Source Voltage	V <sub>SSS</sub>		24	V
Gate-to-Source Voltage	V <sub>GSS</sub>		±12	V
Source Current (DC)	IS		6	А
Source Current (Pulse)	ISP	PW≤10μs, duty cycle≤1%	60	Α
Total Dissipation	PT	When mounted on ceramic substrate (5000mm <sup>2</sup> ×0.8mm)	1.6	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

# **Package Dimensions**

unit : mm (typ) 7069-001

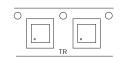


# **Product & Package Information**

Package : EFCPJEITA, JEDEC : -

• Minimum Packing Quantity : 5,000 pcs./reel

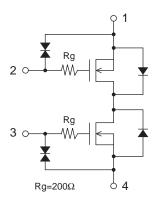
# Packing Type: TR



# Marking



# **Electrical Connection**



# Electrical Characteristics at Ta=25°C

Darameter	Cumbal	Canditions	Constitution of		Ratings		
Parameter	Symbol	Conditions		min	typ	max	Unit
Source-to-Source Breakdown Voltage	V(BR)SSS	IS=1mA, VGS=0V Test Circuit 1		24			V
Zero-Gate Voltage Source Current	ISSS	VSS=20V, VGS=0V	Test Circuit 1			1	μΑ
Gate-to-Source Leakage Current	IGSS	VGS=±8V, VSS=0V	Test Circuit 2			±10	μΑ
Cutoff Voltage	age VGS(off) VSS=10V, IS=1mA		Test Circuit 3	0.5		1.3	V
Forward Transfer Admittance	yfs	VSS=10V, IS=3A	Test Circuit 4		6.5		S
	R <sub>SS</sub> (on)1	I <sub>S</sub> =3A, V <sub>GS</sub> =4.5V	Test Circuit 5	13.5	19.8	23	mΩ
	R <sub>SS</sub> (on)2	IS=3A, VGS=4.0V	Test Circuit 5	14	20.5	24	$m\Omega$
Static Source-to-Source On-State Resistance	R <sub>SS</sub> (on)3	I <sub>S</sub> =3A, V <sub>GS</sub> =3.7V	Test Circuit 5	14.5	21	25.5	mΩ
	RSS(on)4	IS=3A, VGS=3.1V	Test Circuit 5	14.9	23	30	mΩ
	RSS(on)5	IS=3A, VGS=2.5V	Test Circuit 5	18.5	27	35	mΩ
Turn-ON Delay Time	t <sub>d</sub> (on)				200		ns
Rise Time	t <sub>r</sub>	Can appointed Toot Circuit	Toot Circuit 7		815		ns
Turn-OFF Delay Time	t <sub>d</sub> (off)	See specified Test Circuit. Test Circuit 7			1840		ns
Fall Time	tf	]			1770		ns
Total Gate Charge	Qg	V <sub>S</sub> S=10V, V <sub>G</sub> S=4.5V, I <sub>S</sub> =6A			25.4		nC
Forward Source-to-Source Voltage	V <sub>F</sub> (S-S)	I <sub>S</sub> =3A, V <sub>GS</sub> =0V	Test Circuit 6		0.76	1.2	V

# **Ordering Information**

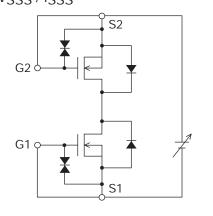
Device	Device Package		memo	
EFC4618R-P-TR EFCP		5,000pcs./reel	Pb Free and Halogen Free	

IT11565

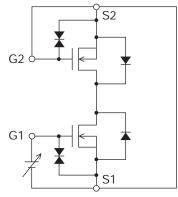
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# Test circuits are example of measuring FET1 side

Test Circuit 1 VSSS / ISSS

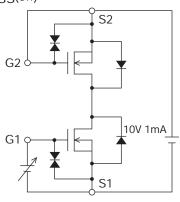


Test Circuit 2 IGSS(+) / (--)

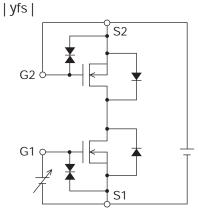


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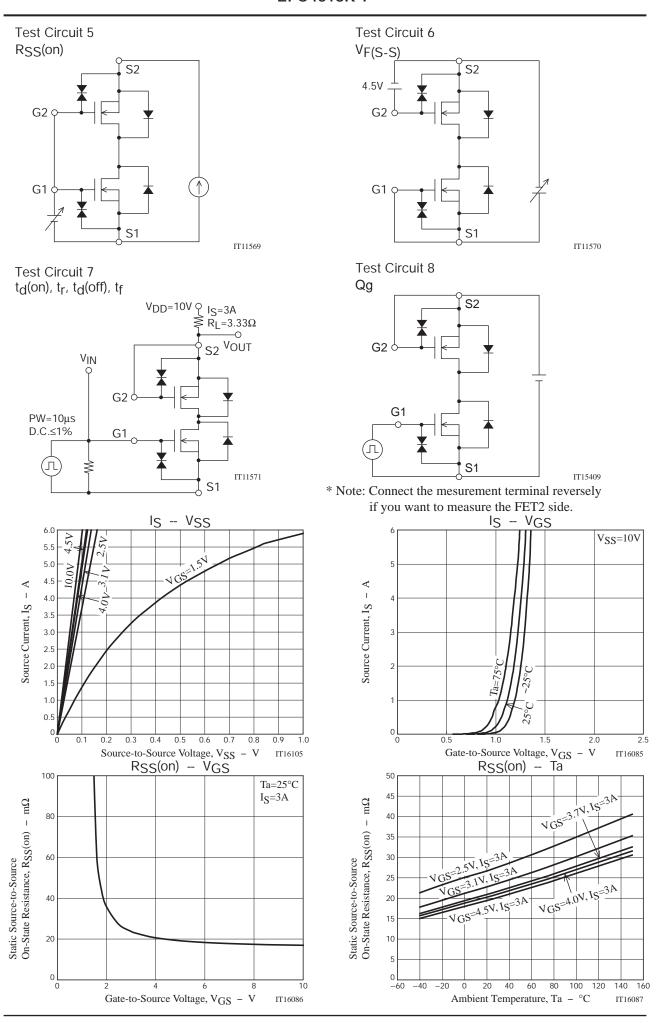
Test Circuit 3 VGS(off)

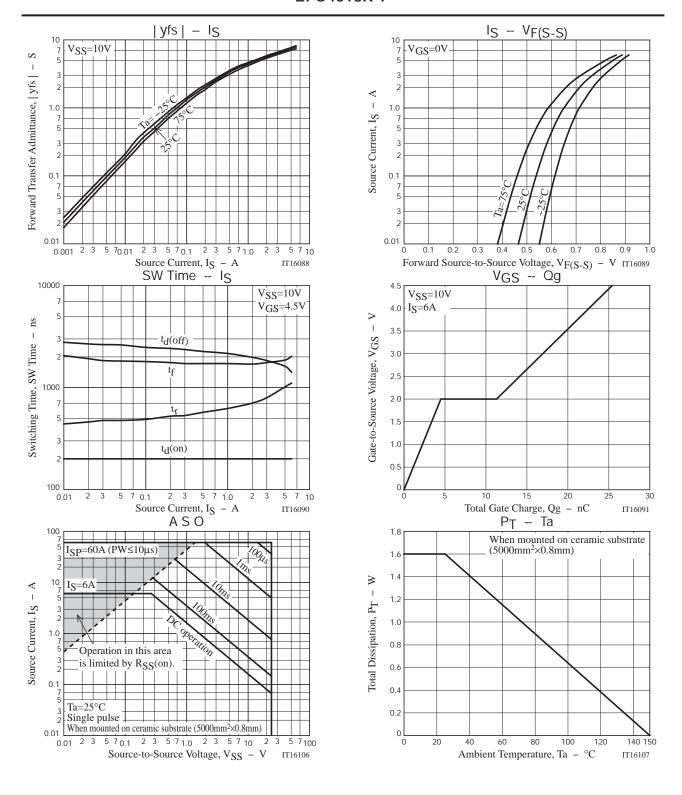


Test Circuit 4



\* Note: Connect the mesurement terminal reversely if you want to measure the FET2 side.



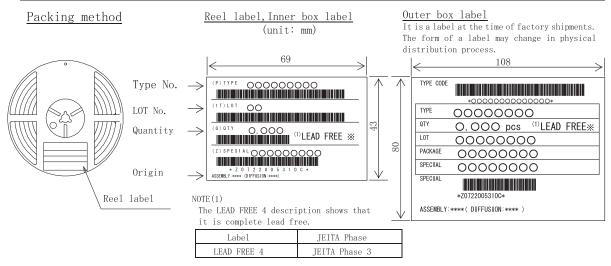


#### **Taping Specification**

# EFC4618R-P-TR

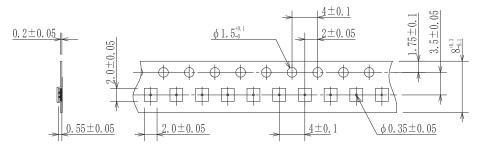
# 1. Packing Format

Package Name		Maximum Number of devices contained (pcs)			Packing format		
		Ree1	Inner box	Outer box	Inner BOX(C-1)	Outer BOX(A-7)	
EFCP1818-4CC-03	37	5,000	25, 000	150, 000	5 reels contained	6 inner boxes contained	
					Dimensions :mm(external)	Dimensions :mm(external)	
					183 X 72 X 185	440 X 195 X 210	

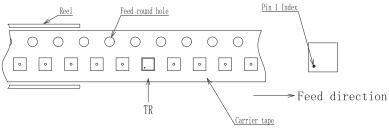


#### 2. Taping configuration

# 2-1. Carrier tape size (unit: mm)



#### 2-2. Device placement direction



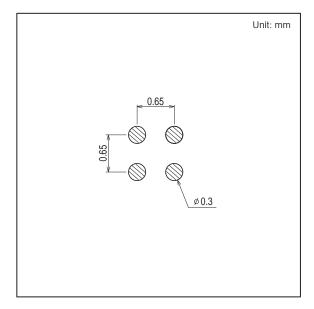
Packing type····TR

# **Outline Drawing**

EFC4618R-P-TR

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# Land Pattern Example



Note on usage: Since the EFC4618R-P is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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