

FJAF6810

High Voltage Color Display Horizontal Deflection Output

- High Collector-Base Breakdown Voltage : BV_{CBO} = 1500V
- High Switching Speed : t_F(typ.) =0.1μs
- For Color Monitor



NPN Triple Diffused Planar Silicon Transistor

Absolute Maximum Ratings T_C=25°C unless otherwise noted

Symbol	Parameter	Rating	Units
V _{CBO}	Collector-Base Voltage	1500	V
V _{CEO}	Collector-Emitter Voltage	750	V
V _{EBO}	Emitter-Base Voltage	6	V
I _C	Collector Current (DC)	10	А
I _{CP} *	Collector Current (Pulse)	20	А
P _C	Collector Dissipation	60	W
TJ	Junction Temperature	150	°C
T _{STG}	Storage Temperature	-55 ~ 150	°C

^{*} Pulse Test: Pulse Width=5ms, Duty Cycle < 10%

$\textbf{Electrical Characteristics} \ \, \textbf{T}_{\text{C}} = 25^{\circ} \text{C unless otherwise noted}$

Cut-off Current Cut-off Current ut-off Current	V_{CB} =1400V, R_{BE} =0 V_{CB} =800V, I_{E} =0 V_{EB} =4V, I_{C} =0			1 10	mA μA
ut-off Current	_ OD _ L			10	μΑ
	V _{EB} =4V, I _C =0				
				1	mA
ase Breakdown Voltage	I _E =500μA, I _C =0	6			V
nt Gain	V _{CE} =5V, I _C =1A	10			
	V_{CE} =5V, I_{C} =6A	5		8	
Emitter Saturation Voltage	I _C =6A, I _B =1.5A			3	V
itter Saturation Voltage	I _C =6A, I _B =1.5A			1.5	V
lime	V_{CC} =200V, I_{C} =6A, R_{L} =33 Ω			3	μs
	I _{B1} =1.2A, I _{B2} = - 2.4A			0.2	μs
	Emitter Saturation Voltage itter Saturation Voltage	ase Breakdown Voltage $I_E=500\mu A, I_C=0$ nt Gain $V_{CE}=5V, I_C=1A$ $V_{CE}=5V, I_C=6A$ Emitter Saturation Voltage $I_C=6A, I_B=1.5A$ itter Saturation Voltage $I_C=6A, I_B=1.5A$ $V_{CC}=200V, I_C=6A, R_L=33\Omega$ $I_{B1}=1.2A, I_{B2}=-2.4A$	$ \begin{array}{cccccccccccccccccccccccccccccccccccc$	$ \begin{array}{cccccccccccccccccccccccccccccccccccc$	$\begin{array}{cccccccccccccccccccccccccccccccccccc$

^{*} Pulse Test: PW=20µs, duty Cycle=1% Pulsed

Thermal Characteristics $T_C=25$ °C unless otherwise noted

Symbol	Parameter	Тур	Max	Units
$R_{\theta jC}$	Thermal Resistance, Junction to Case		2.08	°C/W

©2001 Fairchild Semiconductor Corporation

Typical Characteristics

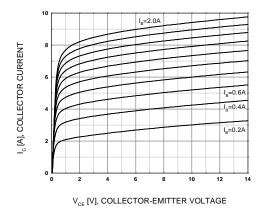


Figure 1. Static Characteristic

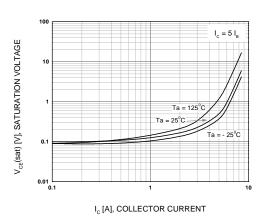


Figure 3. Collector-Emitter Saturation Voltage

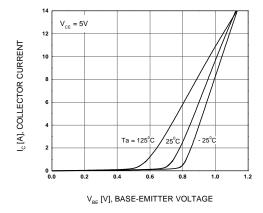


Figure 5. Base-Emitter On Voltage

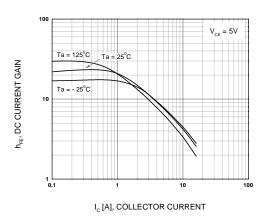


Figure 2. DC current Gain

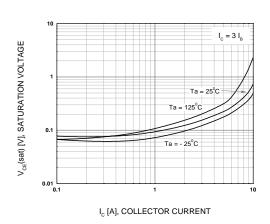


Figure 4. Collector-Emitter Saturation Voltage

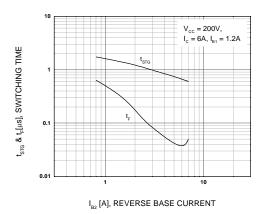


Figure 6. Resistive Load Switching Time

©2001 Fairchild Semiconductor Corporation Rev. A2, May 2001

Typical Characteristics (Continued)

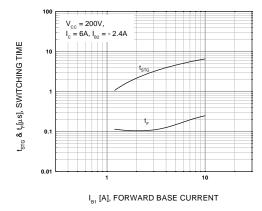


Figure 7. Resistive Load Switching Time

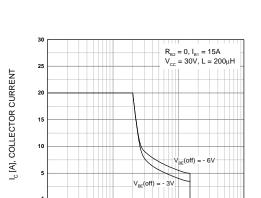


Figure 9. Reverse Bias Safe Operating Area

 $\mathbf{V}_{\mathtt{CE}}\left[\mathbf{V}\right]$, COLLECTOR-EMITTER VOLTAGE

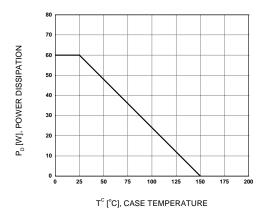


Figure 11. Power Derating

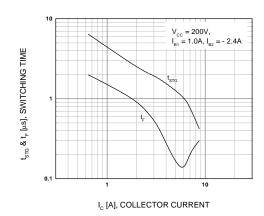


Figure 8. Resistive Load Switching Time

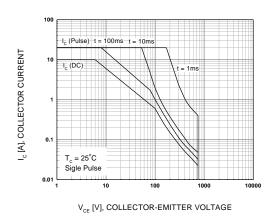


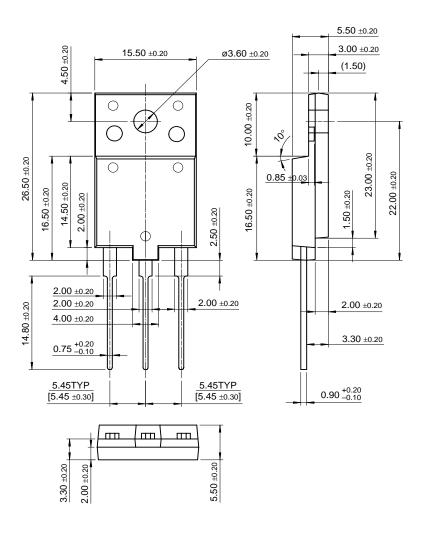
Figure 10. Forward Bias Safe Operating Area

Rev. A2, May 2001

©2001 Fairchild Semiconductor Corporation

Package Demensions

TO-3PF



TRADEMARKS

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

$ACEx^{TM}$	FAST [®]	OPTOPLANAR™	SuperSOT™-3
Bottomless™	FASTr™	PACMAN™	SuperSOT™-6
CoolFET™	FRFET™	POP™	SuperSOT™-8
CROSSVOLT™	GlobalOptoisolator™	PowerTrench [®]	SyncFET™
DenseTrench™	GTO™	QFET™	TinyLogic™
DOME™	HiSeC™	QS TM	UHC™
EcoSPARK™	ISOPLANAR™	QT Optoelectronics™	UltraFET [®]
E ² CMOS™	LittleFET™	Quiet Series™	VCX™
EnSigna™	MicroFET™	SLIENT SWITCHER®	
FACT™	MICROWIRE™	SMART START™	

DISCLAIMER

FACT Quiet Series™

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

Stealth™

OPTOLOGIC™

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

As used herein:

- 1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user.
- 2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

PRODUCT STATUS DEFINITIONS

Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.

©2001 Fairchild Semiconductor Corporation Rev. H2