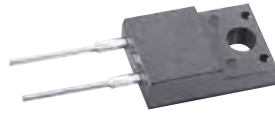


# FML-G26S

Super Fast Rectifiers

**VOLTAGE RANGE: 600 V**

**CURRENT: 10 A**



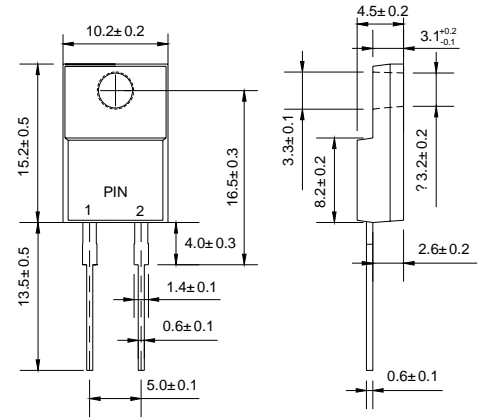
## ITO-220AC

### Features

- ◇ Metal-Semiconductor junction with guard ring
- ◇ Epitaxial construction
- ◇ Low forward voltage drop, low switching losses
- ◇ High surge capability
- ◇ For use in low voltage, high frequency inverters free wheeling, and polarity protection applications
- ◇ The plastic material carries U/L recognition 94V-0

### Mechanical Data

- ◇ Case: JEDEC ITO-220AC
- ◇ Polarity: As marked
- ◇ Weight: 0.056 ounces, 1.587 gram
- ◇ Mounting position: Any



Dimensions in millimeters

### MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Ratings at 25°C ambient temperature unless otherwise specified.

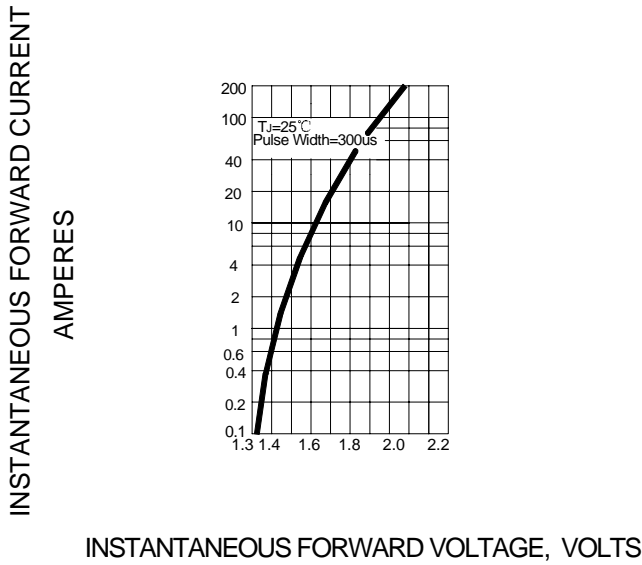
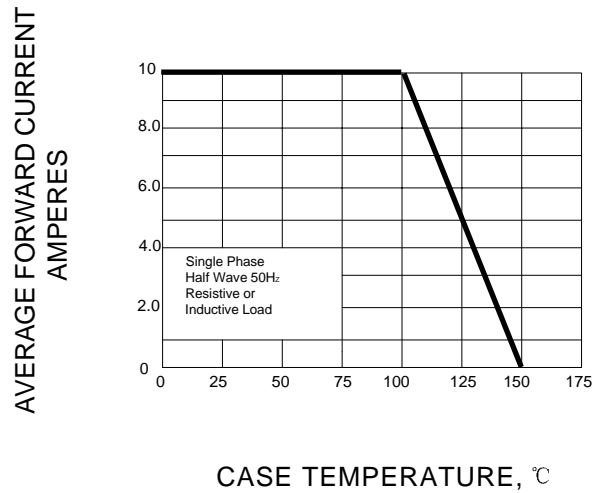
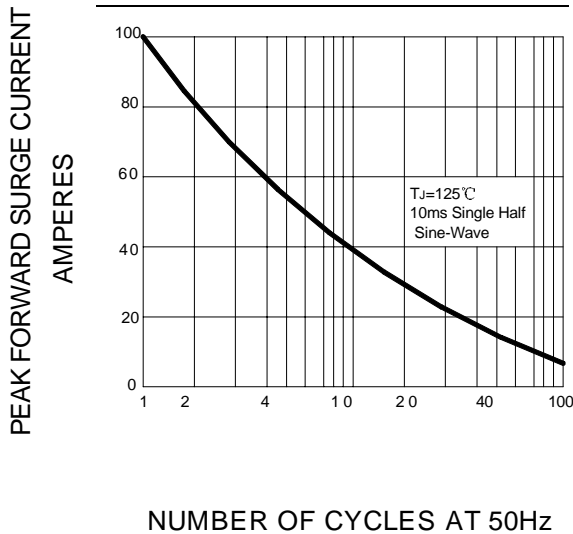
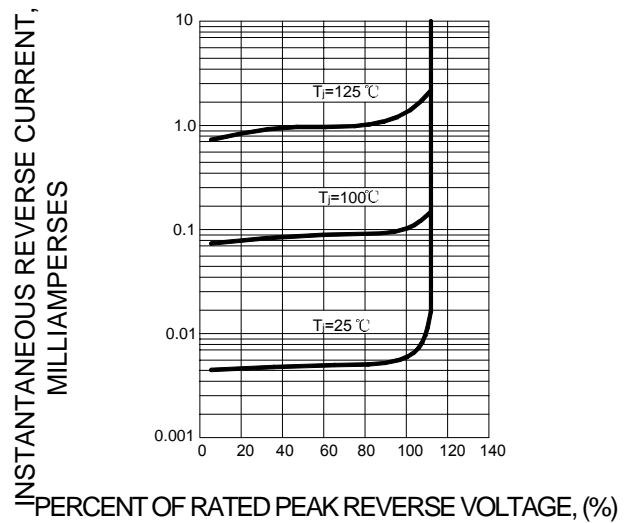
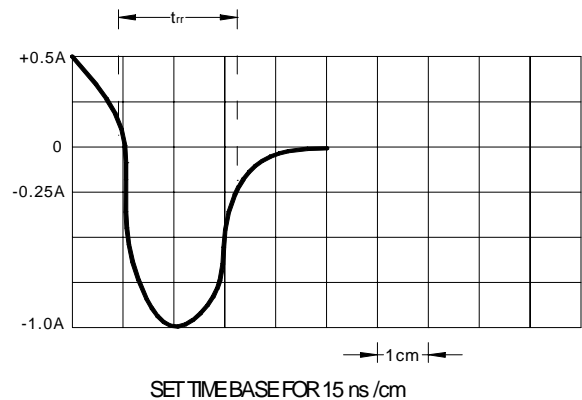
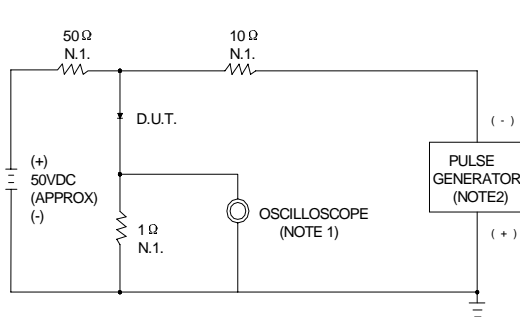
Single phase, half wave, 50 Hz, resistive or inductive load. For capacitive load, derate by 20%.

		FML- G26S	UNITS
Maximum recurrent peak reverse voltage	$V_{RRM}$	600	V
Maximum RMS voltage	$V_{RMS}$	420	V
Maximum DC blocking voltage	$V_{DC}$	600	V
Maximum average forward rectified current @ $T_C=100^\circ C$	$I_{F(AV)}$	10	A
Peak forward surge current 10ms single half-sine-wave superimposed on rated load	$I_{FSM}$	100	A
Maximum instantaneous forward voltage ( $I_F=10A$ )	$V_F$	1.7	V
Maximum reverse current @ $T_J=25^\circ C$ at rated DC blocking voltage @ $T_J=100^\circ C$	$I_R$	0.1 0.3	mA
Maximum reverse recovery time (Note1)	$t_{rr}$	40	ns
Typical thermal resistance (Note2)	$R_{\theta JC}$	4.0	$^\circ C/W$
Operating junction temperature range	$T_J$	- 55 ---- + 150	$^\circ C$
Storage temperature range	$T_{STG}$	- 55 ---- + 150	$^\circ C$

NOTE: 1. Measured with  $I_F=0.5A$ ,  $I_R=1A$ ,  $I_{rr}=0.25A$ .

2. Thermal resistance junction to case.

## Ratings AND Characteristic Curves

**FIG.1 – TYPICAL FORWARD CHARACTERISTIC**

**FIG.2- FORWARD DERATING CURVE**

**FIG.3- PEAK FORWARD SURGE CURRENT**

**FIG.4 – TYPICAL REVERSE CHARACTERISTICS**

**FIG.5 – REVERSE RECOVERY TIME CHARACTERISTIC AND TEST CIRCUIT DIAGRAM**


NOTES: 1. RISE TIME = 7ns MAX. INPUT IMPEDANCE = 1MΩ, 22pF  
 2. RISE TIME = 10ns MAX. SOURCE IMPEDANCE = 50Ω