FTS2012

## Ultrahigh-Speed Switching Applications

## Features

- Low ON resistance.
- 4V drive.
- Mounting height 1.1 mm .


## Specifications

## Absolute Maximum Ratings at $\mathrm{Ta}=25^{\circ} \mathrm{C}$

| Parameter | Symbol |  | Conditions | Ratings |
| :--- | :---: | :---: | ---: | ---: |
| Drain-to-Source Voltage | $\mathrm{V}_{\mathrm{DSS}}$ |  | Unit |  |
| Gate-to-Source Voltage | $\mathrm{V}_{\text {GSS }}$ |  | V |  |
| Drain Current (DC) | $\mathrm{I}_{\mathrm{D}}$ |  | $\pm 20$ | V |
| Drain Current (pulse) | $\mathrm{I}_{\mathrm{DP}}$ | PW $\leq 10 \mu \mathrm{~s}$, duty cycle $\leq 1 \%$ | 8 | A |
| Allowable Power Dissipation | $\mathrm{P}_{\mathrm{D}}$ | Mounted on a ceramic board (1000mm $\left.{ }^{2} \times 0.8 \mathrm{~mm}\right)$ | 32 | A |
| Channel Temperature | Tch |  | 1.3 | W |
| Storage Temperature | Tstg |  | 150 | ${ }^{\circ} \mathrm{C}$ |

Electrical Characteristics at $\mathrm{Ta}=25^{\circ} \mathrm{C}$

| Parameter | Symbol | Conditions | Ratings |  |  | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | min | typ | max |  |
| Drain-to-Source Breakdown Voltage | $\mathrm{V}_{\text {(BR) }}$ DSS | $\mathrm{I}_{\mathrm{D}}=1 \mathrm{~mA}, \mathrm{~V}_{\mathrm{GS}}=0$ | 30 |  |  | V |
| Zero-Gate Voltage Drain Current | IDSS | $\mathrm{V}_{\mathrm{DS}}=30 \mathrm{~V}, \mathrm{~V}_{\mathrm{GS}}=0$ |  |  | 1 | $\mu \mathrm{A}$ |
| Gate-to-Source Leakage Current | IGSS | $\mathrm{V}_{\mathrm{GS}}= \pm 16 \mathrm{~V}, \mathrm{~V}_{\mathrm{DS}}=0$ |  |  | $\pm 10$ | $\mu \mathrm{A}$ |
| Cutoff Voltage | $\mathrm{V}_{\mathrm{GS}}$ (off) | $\mathrm{V}_{\mathrm{DS}}=10 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=1 \mathrm{~mA}$ | 1.0 |  | 2.4 | V |
| Forward Transfer Admittance | \| yfs | | $\mathrm{V}_{\mathrm{DS}}=10 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=8 \mathrm{~A}$ | 9.8 | 14 |  | S |
| Static Drain-to-Source On-State Resistance | $\mathrm{R}_{\mathrm{DS}}(\mathrm{on}) 1$ | ${ }^{1} \mathrm{D}=8 \mathrm{~A}, \mathrm{~V}_{\mathrm{GS}}=10 \mathrm{~V}$ |  | 14 | 19 | $\mathrm{m} \Omega$ |
|  | $\mathrm{R}_{\mathrm{DS}}(\mathrm{on}) 2$ | ${ }^{\mathrm{I}} \mathrm{D}=4 \mathrm{~A}, \mathrm{~V}_{\mathrm{GS}}=4 \mathrm{~V}$ |  | 22 | 31 | $\mathrm{m} \Omega$ |
| Input Capacitance | Ciss | $\mathrm{V}_{\mathrm{DS}}=10 \mathrm{~V}, \mathrm{f}=1 \mathrm{MHz}$ |  | 1550 |  | pF |
| Output Capacitance | Coss | $\mathrm{V}_{\mathrm{DS}}=10 \mathrm{~V}, \mathrm{f}=1 \mathrm{MHz}$ |  | 350 |  | pF |
| Reverse Transfer Capacitance | Crss | $\mathrm{V}_{\mathrm{DS}}=10 \mathrm{~V}, \mathrm{f}=1 \mathrm{MHz}$ |  | 220 |  | pF |

Marking : S2012

Package Dimensions
unit:mm
2147A


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| Parameter | Symbol | Conditions | Ratings |  |  | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | min | typ | max |  |
| Turn-ON Delay Time | $\mathrm{t}_{\mathrm{d}}(\mathrm{on})$ | See specified Test Circuit |  | 12 |  | ns |
| Rise Time | $\mathrm{t}_{\mathrm{r}}$ | See specified Test Circuit |  | 210 |  | ns |
| Turn-OFF Delay Time | $\mathrm{t}_{\mathrm{d}}$ (off) | See specified Test Circuit |  | 110 |  | ns |
| Fall Time | $\mathrm{t}_{\mathrm{f}}$ | See specified Test Circuit |  | 95 |  | ns |
| Total Gate Charge | Qg | $\mathrm{V}_{\mathrm{DS}}=10 \mathrm{~V}, \mathrm{~V}_{\mathrm{GS}}=10 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=8 \mathrm{~A}$ |  | 40 |  | nC |
| Gate-to-Source Charge | Qgs | $\mathrm{V}_{\mathrm{DS}}=10 \mathrm{~V}, \mathrm{~V}_{\mathrm{GS}}=10 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=8 \mathrm{~A}$ |  | 5 |  | nC |
| Gate-to-Drain "Miller" Charge | Qgd | $\mathrm{V}_{\mathrm{DS}}=10 \mathrm{~V}, \mathrm{~V}_{\mathrm{GS}}=10 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=8 \mathrm{~A}$ |  | 7 |  | nC |
| Diode Forward Voltage | $\mathrm{V}_{\text {SD }}$ | $\mathrm{I}_{\mathrm{S}}=8 \mathrm{~A}, \mathrm{~V}_{\mathrm{GS}}=0$ |  | 0.82 | 1.2 | V |

## Switching Time Test Circuit




RDS(on) - Ta


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