1700 V SiC MPS™ Diode

Silicon Carbide Power Schottky Diode



V_{RRM}	=	1700 V
I _{F (Tc = 135°C)}	=	65 A
Q_{C}	=	272 nC

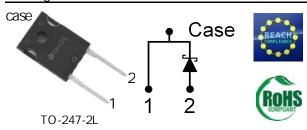
Features

High Avalanche (UIS) Capability Enhanced Surge Current Capability 175 °C Maximum Operating Temperature Temperature Independent Switching Behavior Positive Temperature Coefficient Of V_F Extremely Fast Switching Speeds Superior Figure of Merit $Q_C I_F$

Advantages

Low S tandby Power Losses
Improved Circuit E fficiency (Lower Overall Cost)
Low S witching Losses
E ase of Paralleling Devices without Thermal Runaway
Smaller Heat Sink Requirements
Low Reverse Recovery Current
Low Device Capacitance
Low Reverse Leakage Current at Operating Temperature

Package



Applications

Power Factor Correction (PFC)
Switched-Mode Power Supply (SMPS)
Solar Inverters
Wind Turbine Inverters
Motor Drives
Induction Heating
Uninterruptible Power Supply (UPS)
High Voltage Multipliers

Absolute Maximum Ratings

S				
Parameter	Symbol	Conditions	Values	Unit
Repetitive Peak Reverse Voltage	V_{RRM}		1700	V
		$T_C = 25 ^{\circ}C, D = 1$	130	
Continuous Forward Current	I_{F}	$T_C = 135 ^{\circ}C, D = 1$	65	Α
		$T_C = 168 ^{\circ}C, D = 1$	25	
Diode Ruggedness	dV.∕dt	$V_{R} = 0 \sim 960 V$	100	V/µs
Power Dissipation	P _{tot}	$T_C = 25 ^{\circ}C$	1227	W
Operating and Storage Temperature	T_i , T_{sta}		-55 to 175	°C

Electrical Characteristics

Parameter	Symbol	Conditions		Values			llni+
Parameter	Symbol			min.	typ.	max.	Unit
Diode Forward Voltage	V _F	I _F = 25 A, T _j =	I _F = 25 A, T _j = 25 °C		1.5	1.8	V
	V F	$I_F = 25 \text{ A}, T_j = 175 ^{\circ}\text{C}$			2.3	2.7	
Reverse Current	1	$V_R = 1700 V$, $T_j = 25 ^{\circ}C$		2	35		
	I _R	$V_R = 1700 V, T_j =$	T _j = 175 °C		20	237	μΑ
Total Capacitive Charge	Qc		V _R = 400 V		182		nC
	Q _C	I _F I _{F,MAX} dI _F /dt = 200 A /µs	$V_{R} = 800 V$		272	272	
Switching Time	+	T ₁ = 175 °C	$V_R = 400 V$		< 10		ns
	t _s	$V_{R} = 800 V$			< 10	_	113
Total Capacitance	С	$V_R = 1 \text{ V}, f = 1 \text{ MHz}, T_j = 25 ^{\circ}\text{C}$		2984		рF	
	C	$V_R = 800 V_r f = 1 MH_z$	z, T _i = 25 °C	200			

Thermal / Mechanical Characteristics

Thermal Resistance, Junction - Case R _{thJC}	0.12	°C <i>I</i> W

GeneSiC[™] SEMICONDUCTOR

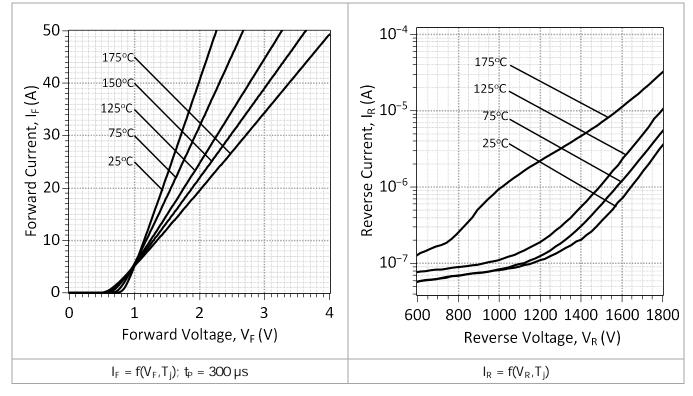


Figure 1: Typical Forward Characteristics

Figure 2: Typical Reverse Characteristics

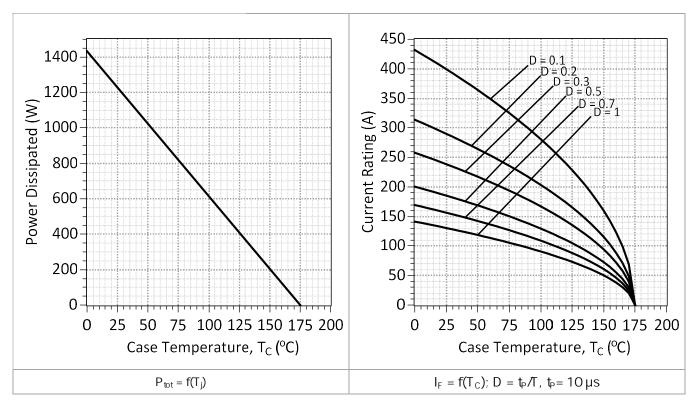


Figure 3: Power Derating Curve

Figure 4: Current Derating Curves

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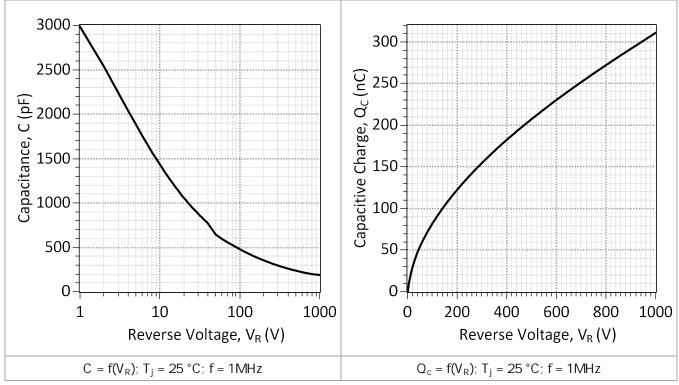


Figure 5: Typical Junction Capacitance vs Reverse Voltage Characteristics

Figure 6: Typical Capacitive Charge vs. Reverse Voltage Characteristics

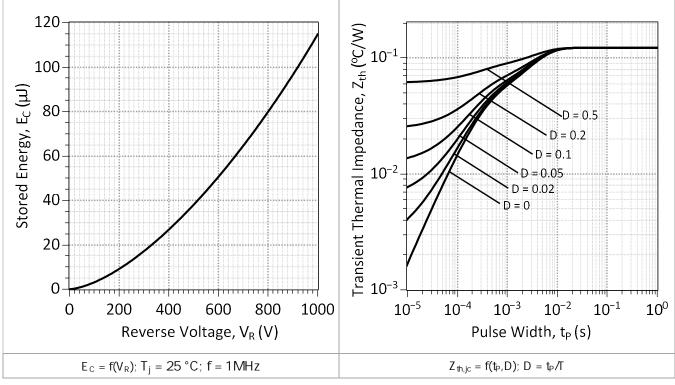


Figure 7: Typical Capacitive Energy vs. Reverse Voltage Characteristics

Figure 8: Transient Thermal Impedance

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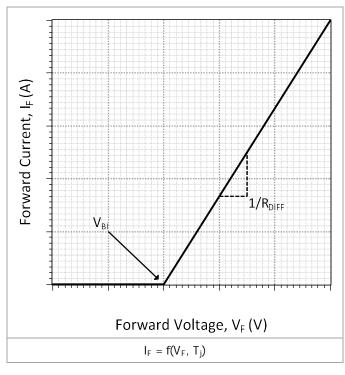


Figure 9: Forward Curve Model



$$I_F = (V_F - V_{BI}) / R_{DIFF}$$

Built-In Voltage (V_{BI}):

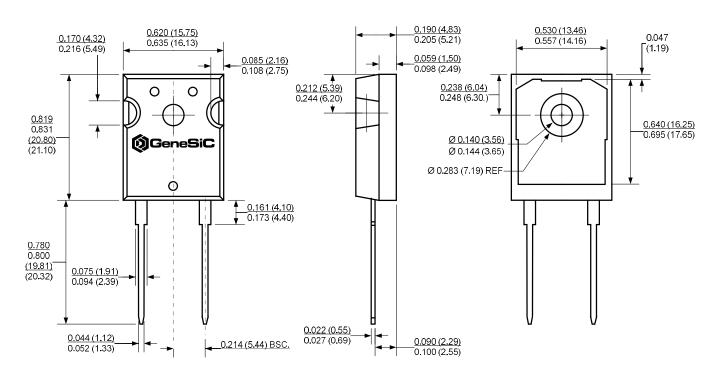
$$V_{BI}(T_i) = m^*T_i + b,$$

$$m = -1.21e-03$$
, $b = 0.913$

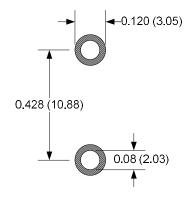
Differential Resistance (R_{DIFF}):

$$R_{DIFF}(T_j) = a * T_j^2 + b * T_j + c ();$$

$$a = 1.26e-04$$
, $b = 2.10e-02$, $c = 4.25$



Recommended Solder Pad Layout



NOTE

- 1. CONTROLLED DIMENSION IS INCH. DIMENSION IN BRACKET IS MILLIMETER.
- 2. DIMENSIONS DO NOT INCLUDE END FLASH, MOLD FLASH, MATERIAL PROTRUSIONS



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RoHS Compliance

The levels of RoHS restricted materials in this product are below the maximum concentration values (also referred to as the threshold limits) permitted for such substances, or are used in an exempted application, in accordance with EU Directive 2011/65/EC (RoHS2), as implemented January 2, 2013. RoHS Declarations for this product can be obtained from your GeneSiC representative.

REACH Compliance

REACH substances of high concern (SVHCs) information is available for this product. Since the European Chemical Agency (ECHA) has published notice of their intent to frequently revise the SVHC listing for the foreseeable future, please contact a GeneSiC representative to insure you get the most up-to-date REACH SVHC Declaration. REACH banned substance information (REACH Article 67) is also available upon request.

This product has not been designed or tested for use in, and is not intended for use in, applications implanted into the human body nor in applications in which failure of the product could lead to death, personal injury or property damage, including but not limited to equipment used in the operation of nuclear facilities, life-support machines, cardiac defibrillators or similar emergency medical equipment, aircraft navigation or communication or control systems, or air traffic control systems

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SPICE Model Parameters

This is a secure document Please copy this code from the SPICE model PDF file on our website (http://www.genesicsemi.com/sic_rectifiers_diodes/merged_pin_schottky/GB25MPS17-247_SPICE.pdf) into LTSPICE (version 4) software for simulation of the GB25MPS17-247.

- GeneSiC Semiconductor SiC MPS™ Rectifier Revision: 1.1 Date: February-2018 TO- 247- 2 package . SUBCKT GB25MPS17 A K Case 6. 5n L anode Α AD AD GB25MPS17 D1 Case Κ Case 6. 5n L_cat hode . ends . SUBCKT GB25MPS17 ANODE KATHODE D1 ANODE KATHODE GB25MPS17_SCHOTTKY . MODEL GB25MPS17_SCHOTTKY D + 1S1. 71F-14 0.027RS + N 1 I KF 500 + EG 1. 2 XTI 2 + TRS1 0.005684 TRS2 2. 717E-05 + CJO 4. 16E-9 0.879 VJ + M 0.438 FC 0.5 + TT 1. 00E-10 BV 1700 + I BV 2E-06 VPK 1700 Si C_MPSTM + I AVF 25 **TYPF** + MFG GeneSi C_Semi . ENDS * End of GB25MPS12-247 SPICE Model
- * This model is provided "ASIS, WHEREIS, AND WITH NO WARRANTY OF ANY KIND
- * EITHER EXPRESSED OR IMPLIED, INCLUDING BUT NOT LIMITED TO ANY IMPLIED
- * WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE."