

FEATURES

- Non Punch Through Silicon
- Isolated Copper Baseplate With Al₂O₃ Substrate
- Low Inductance Internal Construction
- Full 1800V Rating
- 800A Per Module

APPLICATIONS

- High Power Inverters
- Motor Controllers
- Induction Heating
- Resonant Converters

The Powerline range of high power modules includes dual and single switch configurations covering voltages from 600V to 3300V and currents up to 4800A.

The GP800FSS18 is a single switch 1800V, n channel enhancement mode, insulated gate bipolar transistor (IGBT) module. The IGBT has a wide reverse bias safe operating area (RBSOA) ensuring reliability in demanding applications.

The module incorporates an electrically isolated base plate and low inductance construction enabling circuit designers to optimise circuit layouts and utilise earthed heat sinks for safety.

ORDERING INFORMATION

Order As:

GP800FSS18

Note: When ordering, please use the complete part number.

KEY PARAMETERS

V_{CES}		1800V
$V_{CE(sat)}$	(typ)	3.5V
I_C	(max)	800A
$I_{C(PK)}$	(max)	1600A

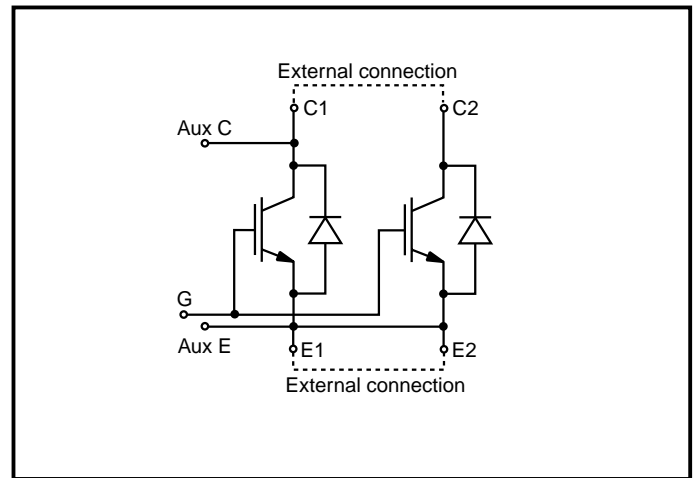


Fig. 1 Single switch circuit diagram

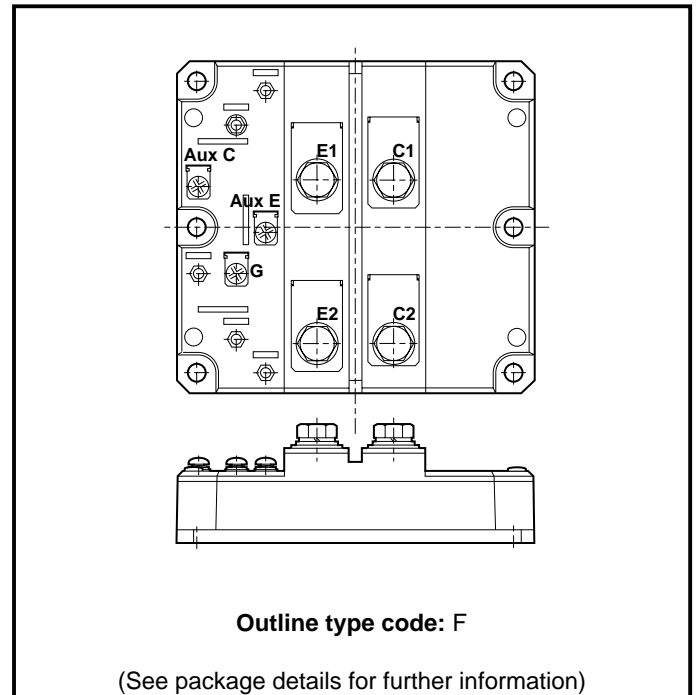


Fig. 2 Electrical connections - (not to scale)

ABSOLUTE MAXIMUM RATINGS

Stresses above those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture of the package. Appropriate safety precautions should always be followed. Exposure to Absolute Maximum Ratings may affect device reliability.

$T_{case} = 25^{\circ}\text{C}$ unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
V_{CES}	Collector-emitter voltage	$V_{GE} = 0V$	1800	V
V_{GES}	Gate-emitter voltage	-	± 20	V
I_C	Continuous collector current	$T_{case} = 55^{\circ}\text{C}$	800	A
$I_{C(PK)}$	Peak collector current	1ms, $T_{case} = 100^{\circ}\text{C}$	1600	A
P_{max}	Max. transistor power dissipation	$T_{case} = 25^{\circ}\text{C}$, $T_j = 150^{\circ}\text{C}$	6000	W
V_{isol}	Isolation voltage	Commoned terminals to base plate. AC RMS, 1 min, 50Hz	4000	V

THERMAL AND MECHANICAL RATINGS

Symbol	Parameter	Test Conditions	Min.	Max.	Units
$R_{th(j-c)}$	Thermal resistance - transistor	Continuous dissipation - junction to case	-	21	$^{\circ}\text{C}/\text{kW}$
$R_{th(j-d)}$	Thermal resistance - diode	Continuous dissipation - junction to case	-	40	$^{\circ}\text{C}/\text{kW}$
$R_{th(c-h)}$	Thermal resistance - case to heatsink (per module)	Mounting torque 5Nm (with mounting grease)	-	8	$^{\circ}\text{C}/\text{kW}$
T_j	Junction temperature	Transistor	-	150	$^{\circ}\text{C}$
		Diode	-	125	$^{\circ}\text{C}$
T_{stg}	Storage temperature range	-	-40	125	$^{\circ}\text{C}$
-	Screw torque	Mounting - M6	-	5	Nm
		Electrical connections - M4	-	2	Nm
		Electrical connections - M8	-	10	Nm

ELECTRICAL CHARACTERISTICS
 $T_{case} = 25^{\circ}\text{C}$ unless stated otherwise.

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
I_{CES}	Collector cut-off current	$V_{GE} = 0\text{V}, V_{CE} = V_{CES}$	-	-	1	mA
		$V_{GE} = 0\text{V}, V_{CE} = V_{CES}, T_{case} = 125^{\circ}\text{C}$	-	-	25	mA
I_{GES}	Gate leakage current	$V_{GE} = \pm 20\text{V}, V_{CE} = 0\text{V}$	-	-	4	μA
$V_{GE(TH)}$	Gate threshold voltage	$I_C = 40\text{mA}, V_{GE} = V_{CE}$	4.5	5.5	6.5	V
$V_{CE(sat)}$	Collector-emitter saturation voltage	$V_{GE} = 15\text{V}, I_C = 800\text{A}$	-	3.5	4	V
		$V_{GE} = 15\text{V}, I_C = 800\text{A}, T_{case} = 125^{\circ}\text{C}$	-	4.3	5	V
I_F	Diode forward current	DC	-	-	800	A
I_{FM}	Diode maximum forward current	$t_p = 1\text{ms}$	-	-	1600	A
V_F	Diode forward voltage	$I_F = 800\text{A}$	-	2.2	2.5	V
		$I_F = 800\text{A}, T_{case} = 125^{\circ}\text{C}$	-	2.3	2.6	V
C_{ies}	Input capacitance	$V_{CE} = 25\text{V}, V_{GE} = 0\text{V}, f = 1\text{MHz}$	-	90	-	nF
L_M	Module inductance	-	-	20	-	nH

ELECTRICAL CHARACTERISTICS

T_{case} = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
t _{d(off)}	Turn-off delay time	I _C = 800A V _{GE} = ±15V V _{CE} = 900V R _{G(ON)} = R _{G(OFF)} = 2.2Ω L ~ 100nH	-	1000	1200	ns
t _f	Fall time		-	200	300	ns
E _{OFF}	Turn-off energy loss		-	200	300	mJ
t _{d(on)}	Turn-on delay time		-	300	400	ns
t _r	Rise time		-	200	300	ns
E _{ON}	Turn-on energy loss		-	200	300	mJ
Q _{rr}	Diode reverse recovery charge	I _F = 800A, V _R = 50% V _{CES} , di _F /dt = 3500A/μs	-	180	240	μC
I _{rr}	Diode reverse current		-	450	-	A
E _{REC}	Diode reverse recovery energy		-	120	-	mJ

T_{case} = 125°C unless stated otherwise

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
t _{d(off)}	Turn-off delay time	I _C = 800A V _{GE} = ±15V V _{CE} = 900V R _{G(ON)} = R _{G(OFF)} = 2.2Ω L ~ 100nH	-	1200	1400	ns
t _f	Fall time		-	250	350	ns
E _{OFF}	Turn-off energy loss		-	300	400	mJ
t _{d(on)}	Turn-on delay time		-	400	550	ns
t _r	Rise time		-	250	350	ns
E _{ON}	Turn-on energy loss		-	350	450	mJ
Q _{rr}	Diode reverse recovery charge	I _F = 800A, V _R = 50% V _{CES} , di _F /dt = 3000A/μs	-	300	400	μC
I _{rr}	Diode reverse current		-	525	-	A
E _{REC}	Diode reverse recovery energy		-	190	-	mJ

TYPICAL CHARACTERISTICS

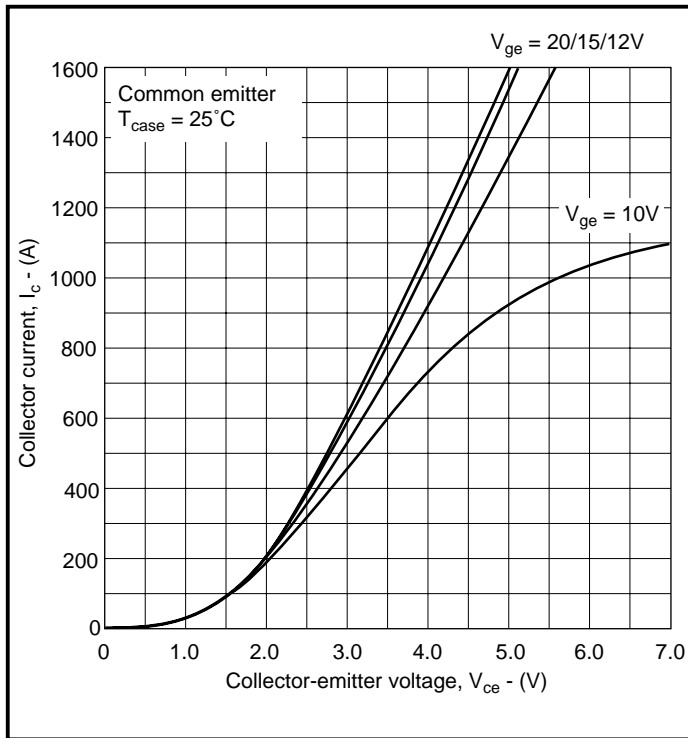


Fig.3 Typical output characteristics

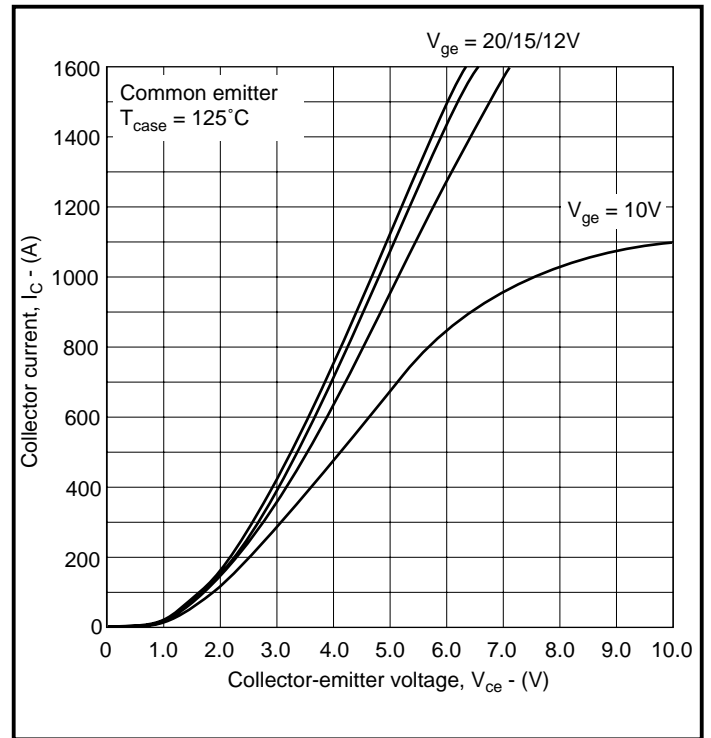


Fig.4 Typical output characteristics

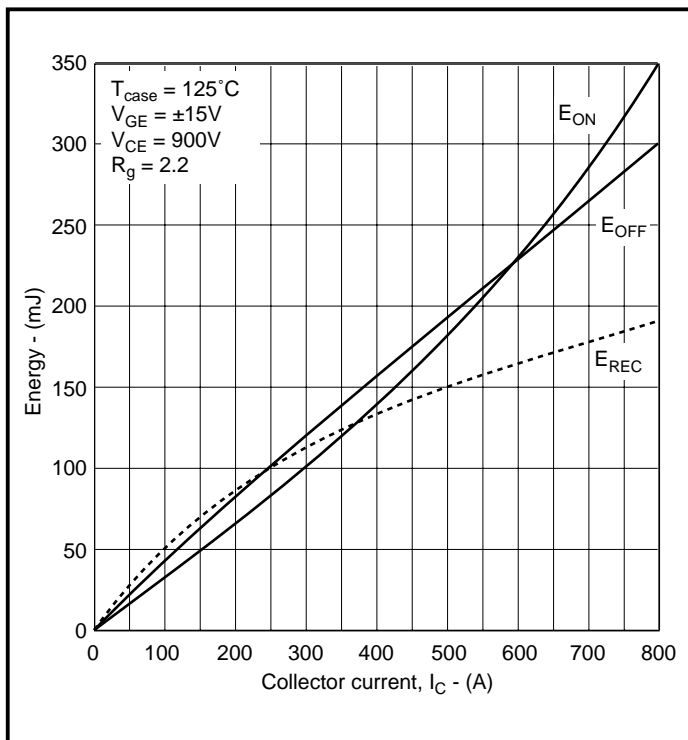


Fig.5 Typical switching energy vs collector current

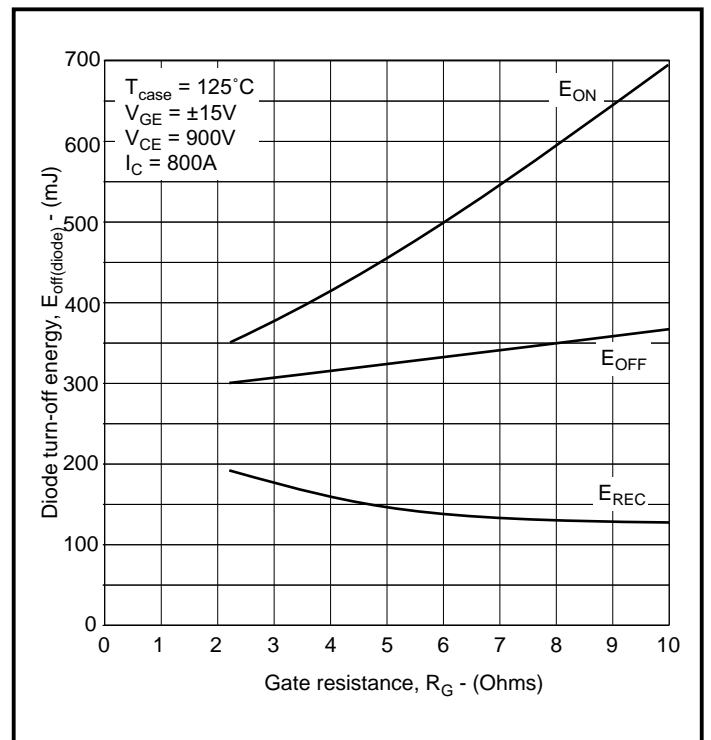


Fig.6 Typical switching energy vs gate resistance

Caution: This device is sensitive to electrostatic discharge. Users should follow ESD handling procedures.

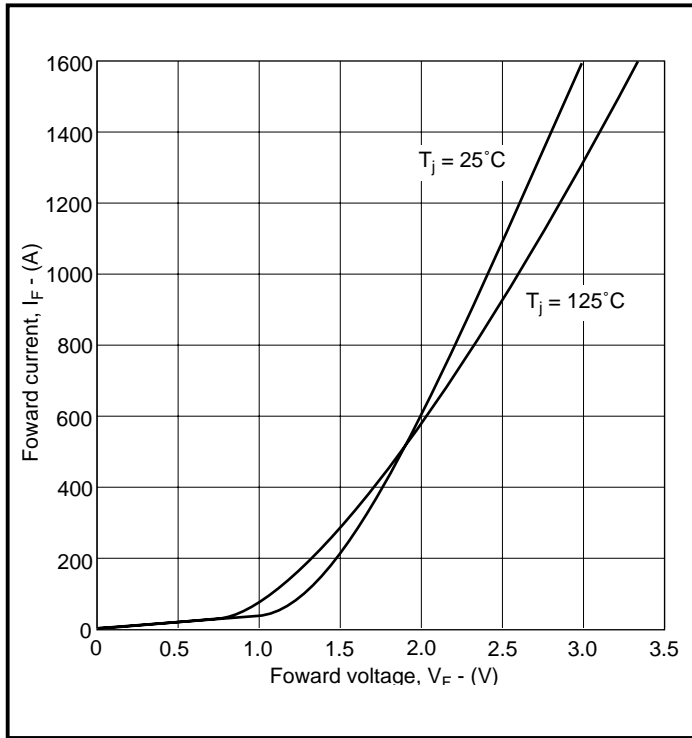


Fig.7 Diode typical forward characteristics

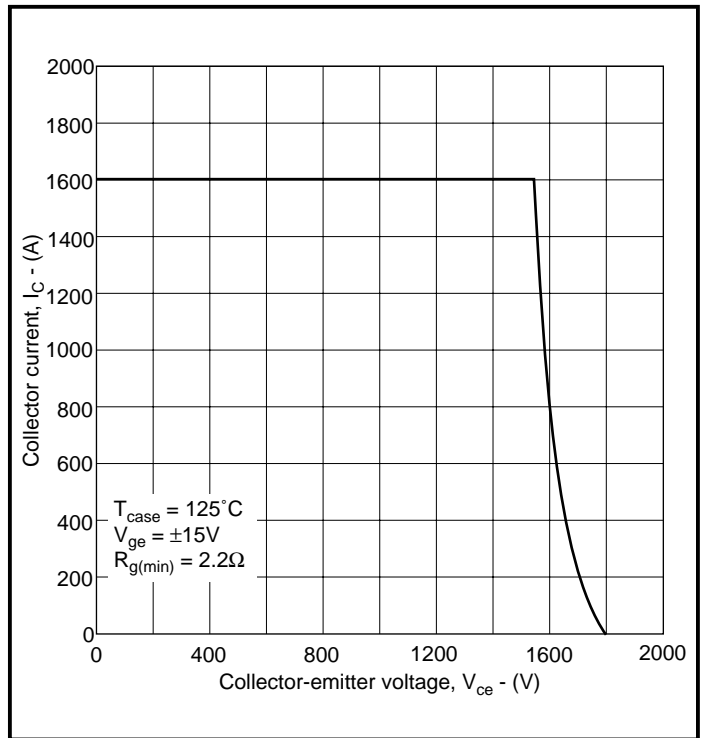


Fig.8 Reverse bias safe operating area

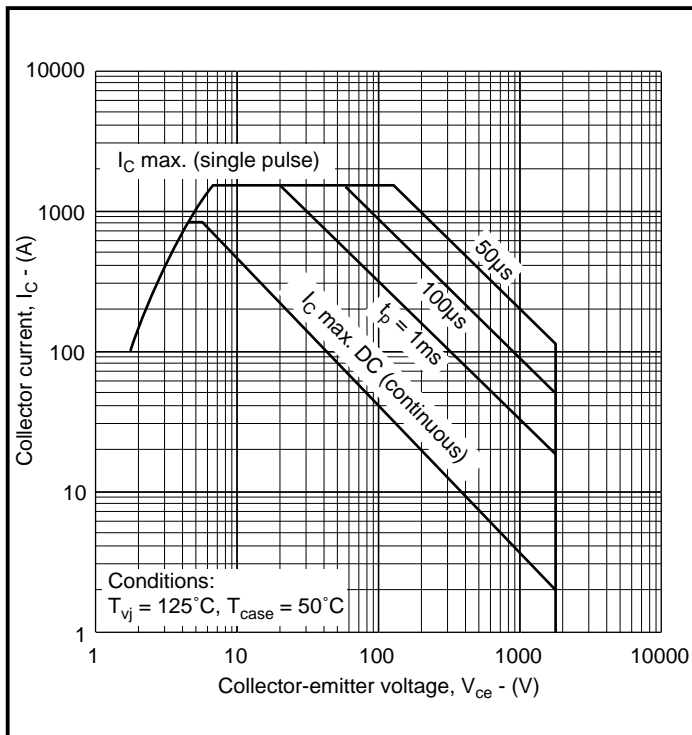


Fig.9 Forward bias safe operating area

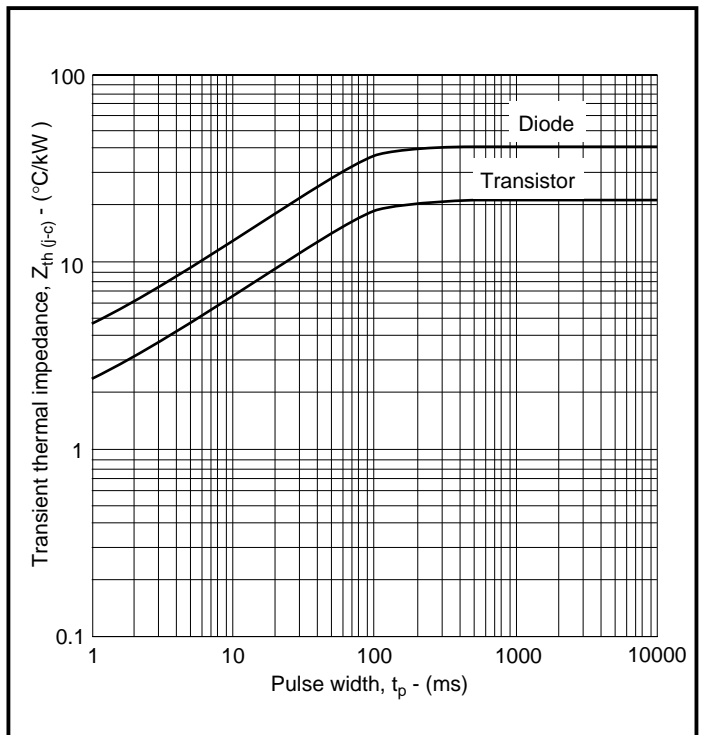


Fig.10 Transient thermal impedance

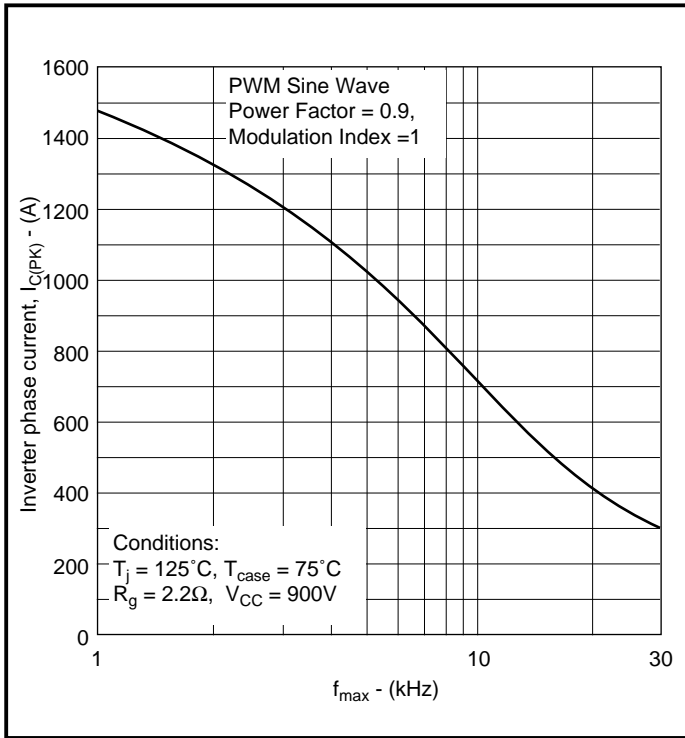


Fig.17 3-Phase inverter operating frequency

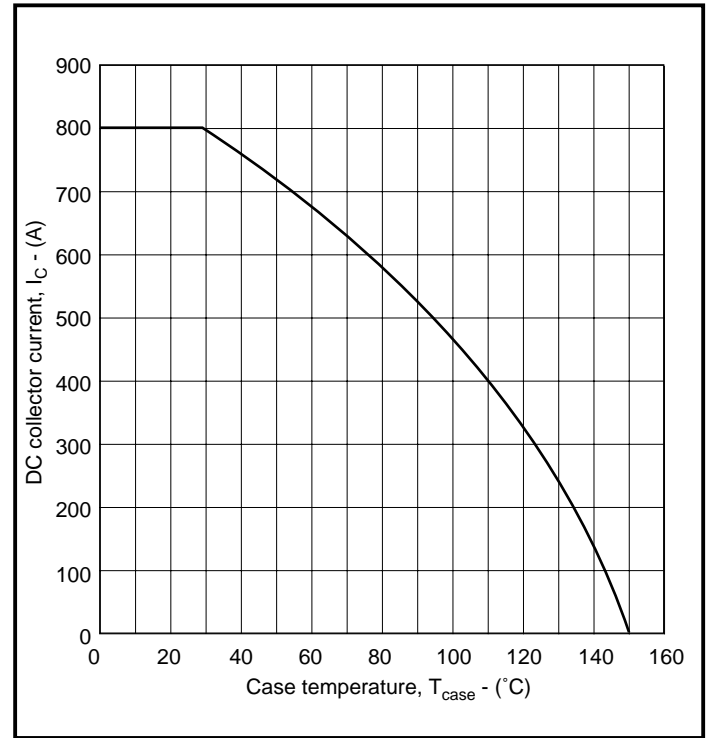
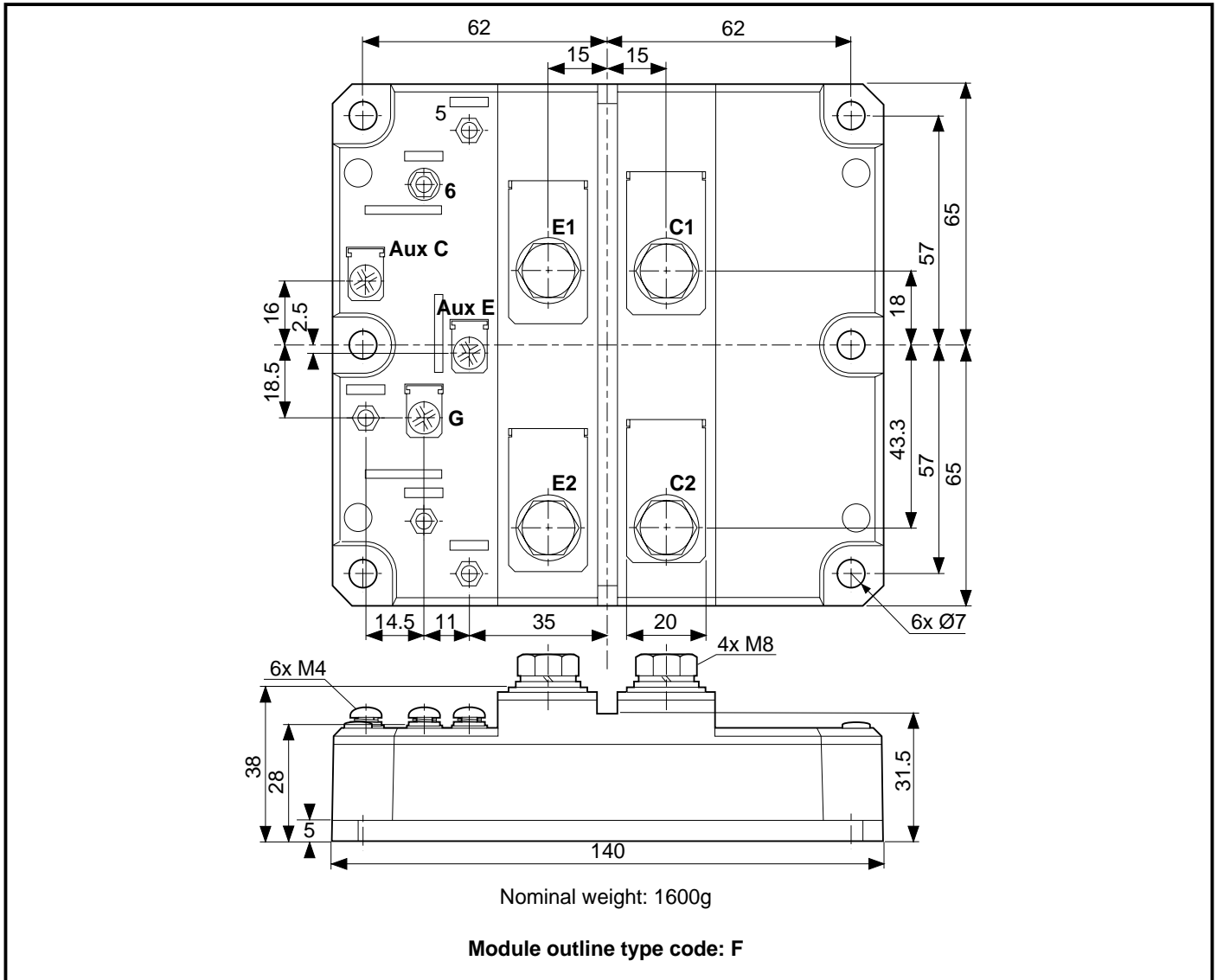


Fig.18 DC current rating vs case temperature

PACKAGE DETAILS

For further package information, please visit our website or contact your nearest Customer Service Centre. All dimensions in mm, unless stated otherwise. DO NOT SCALE.



ASSOCIATED PUBLICATIONS

Title	Application Note Number
Electrostatic handling precautions	AN4502
An introduction to IGBTs	AN4503
IGBT ratings and characteristics	AN4504
Heatsink requirements for IGBT modules	AN4505
Calculating the junction temperature of power semiconductors	AN4506
Gate drive considerations to maximise IGBT efficiency	AN4507
Parallel operation of IGBTs – punch through vs non-punch through characteristics	AN4508
Guidance notes for formulating technical enquiries	AN4869
Principle of rating parallel connected IGBT modules	AN5000
Short circuit withstand capability in IGBTs	AN5167
Driving Dynex Semiconductor IGBT modules with Concept gate drivers	AN5384

POWER ASSEMBLY CAPABILITY

The Power Assembly group was set up to provide a support service for those customers requiring more than the basic semiconductor, and has developed a flexible range of heatsink and clamping systems in line with advances in device voltages and current capability of our semiconductors.

We offer an extensive range of air and liquid cooled assemblies covering the full range of circuit designs in general use today. The Assembly group continues to offer high quality engineering support dedicated to designing new units to satisfy the growing needs of our customers.

Using the latest CAD methods our team of design and applications engineers aim to provide the Power Assembly Complete Solution (PACs).

HEATSINKS

The Power Assembly group has its own proprietary range of extruded aluminium heatsinks. They have been designed to optimise the performance of Dynex semiconductors. Data with respect to air natural, forced air and liquid cooling (with flow rates) is available on request.

For further information on device clamps, heatsinks and assemblies, please contact your nearest sales representative or customer service office.



<http://www.dynexsemi.com>

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Target Information: This is the most tentative form of information and represents a very preliminary specification. No actual design work on the product has been started.

Preliminary Information: The product is in design and development. The datasheet represents the product as it is understood but details may change.

Advance Information: The product design is complete and final characterisation for volume production is well in hand.

No Annotation: The product parameters are fixed and the product is available to datasheet specification.

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