



Features

- ▶ CMOS compatible output
- ▶ Low jitter and fast rise / fall
- ▶ Popular telecomms package layout
- ▶ Enable / disable tristate function

Enable / Disable Function

Input (pad 2)	Output (pad 4)
Open '1' level V_{IH} '0' level V_{IL}	Active Active High Impedance

Specifications

GVXO-32L: +3.3V supply

GVXO-32S: +5.0V supply

Parameters	Variant		Option Codes
	L	S	
Frequency range: 52.0 ~ 160MHz	■	■	
Voltage control (V_{CTL}): +1.65V \pm 1.5V, 10% linearity +2.5V \pm 2.0V, 10% linearity	■	■	
Frequency pullability: \pm 80ppm min \pm 100ppm min Other	■	■	specify
Frequency stability*: \pm 50ppm max Other	■	■	specify
Operating temperature range: -10 to +70°C	■	■	
Storage temperature range: -40 to +85°C	■	■	
Supply voltage (V_{DD}): +3.3V (\pm 5%) +5.0V (\pm 5%)	■	■	
Supply current (mA max):	60	80	
Driving ability: 15pF CMOS	■	■	
Logic levels: '0' level = 10% V_{DD} max '1' level = 90% V_{DD} min	■	■	
Waveform symmetry: 40:60 max @ 50% V_{DD}	■	■	
Start up time: 10ms max	■	■	
Rise / fall time: 3ns max (20% ~ 80% V_{DD})	■	■	
Enable / disable function: Tristate (control via pad 2) None (pad 2 NC)	■	■	N
Soldering condition: Reflow, 240°C max	■	■	

■ Standard. □ Optional - Please specify required code(s) when ordering

* Frequency stability is inclusive of calibration @ 25°C, operating temperature range, supply voltage change, load change and ageing, with $V_{CTL} = 50\% V_{DD}$

Ordering Information

Product name + variant + option codes (if any) + frequency
eg: **GVXO-32L 77.760MHz** 3.3V, \pm 80ppm pullability, with E/D

GVXO-32S/N 155.520MHz 5.0V, \pm 100ppm pullability, no E/D

Option code X (eg GVXO-32S/X) denotes a combination of values not listed above.

- ◆ Available on T&R - 1k pcs per reel. Refer to our website for details.
- ◆ Some combinations of stability/pullability are not available