Serial-In, Parallel-Out Shift Register

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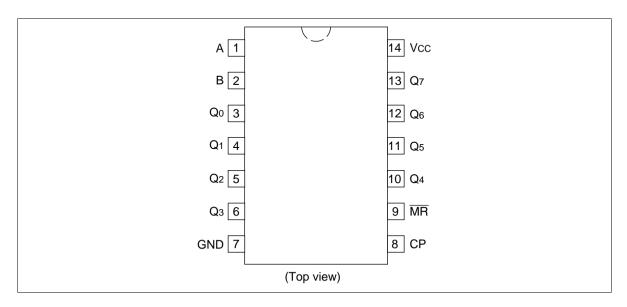
Description

The HD74AC164/HD74ACT164 is a high-speed 8-bit serial-in/parallel-out shift register. Serial data is entered through a 2-input AND gate synchronous with the Low-to-High transition of the clock. The device features an asynchronous Master Reset which clears the register, setting all outputs Low independent of the clock.

Features

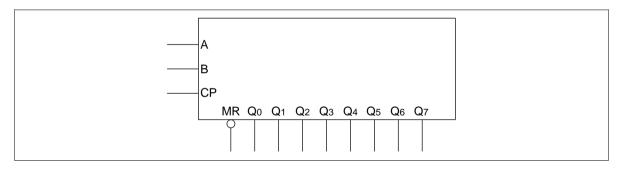
- Outputs Source/Sink 24 mA
- HD74ACT164 has TTL-Compatible Inputs

Pin Arrangement





Logic Symbol



Pin Names

A, B	Data Inputs
CP	Clock Pulse Input (Active Rising Edge)
$\overline{\mathrm{MR}}$	Master Reset Input (Active Low)
Q_0 to Q_7	Outputs

Functional Description

The HD74AC164/HD74ACT164 is an edge-triggered 8-bit shift register with serial data entry and an output from each of the eight stages. Data is entered serially through one of two inputs (A or B); either of these inputs can be used as an active High Enable for data entry through the other inputs. An unused input must be tied High.

Each Low-to-High transition on the Clock (CP) input shifts data one place to the right and enters into Q_0 the logical AND of the two data inputs (A•B) that existed before the rising clock edge. A Low level on the Master Reset (\overline{MR}) input overrides all other inputs and clears the register asynchronously, forcing all Q outputs Low.

Mode Select Table

	Inputs			Outputs	
Operating Mode	MR	Α	В	Q ₀	Q_1 to Q_7
Reset (Clear)	L	Х	Х	L	L to L
Shift	Н	L	L	L	q_0 to q_6
	Н	L	Н	L	q_0 to q_6
	Н	Н	L	L	q_0 to q_6
	Н	Н	Н	Н	q_0 to q_6

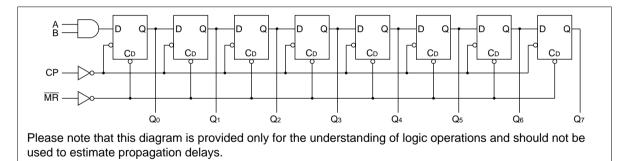
H : High Voltage Level

L : Low Voltage Level

X : Immaterial

q_n: Lower case letters indicate the state of the referenced input or output one setup time prior to the Low-to-High clock transition.

Logic Diagram



DC Characteristics (unless otherwise specified)

Item	Symbol	Max	Unit	Condition
Maximum quiescent supply current	I _{cc}	80	μΑ	$V_{IN} = V_{CC}$ or ground, $V_{CC} = 5.5 V$, Ta = Worst case
Maximum quiescent supply current	I _{cc}	8.0	μΑ	$V_{IN} = V_{CC}$ or ground, $V_{CC} = 5.5$ V, Ta = 25°C
Maximum additional I _{cc} /input (HD74ACT164)	I _{CCT}	1.5	mA	$V_{IN} = V_{CC} - 2.1 \text{ V}, V_{CC} = 5.5 \text{ V},$ Ta = Worst case

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AC Characteristics: HD74AC164

				Ta = +25°C C _∟ = 50 pF			Ta = –4 C _∟ = 50	
Item	Symbol	V _{cc} (V)* ¹	Min	Тур	Max	Min	Max	Unit
Maximum clock	f_{max}	3.3	125	_	_	100	_	MHz
frequency		5.0	150	_	_	125	_	
Propagation delay	t _{PLH}	3.3	1.0	8.5	13.0	1.0	13.5	ns
CP to Q _n		5.0	1.0	6.5	10.0	1.0	10.5	
Propagation delay	t _{PHL}	3.3	1.0	8.5	13.0	1.0	14.5	_
CP to Q _n		5.0	1.0	6.5	10.0	1.0	10.5	
Propagation delay	t _{PHL}	3.3	1.0	9.5	16.0	1.0	18.0	_
$\overline{\text{MR}}$ to Q_n		5.0	1.0	7.5	11.5	1.0	13.5	_

Note: 1. Voltage Range 3.3 is $3.3 \text{ V} \pm 0.3 \text{ V}$

Voltage Range 5.0 is 5.0 V \pm 0.5 V

AC Operating Requirements: HD74AC164

			Ta = +25°C C _∟ = 50 pF		Ta = -40°C to +85°C C _∟ = 50 pF	
Item	Symbol	V _{cc} (V)* ¹	Тур	Guaranteed	Minimum	Unit
Setup time A or B to CP	t _{su}	3.3	3.0	5.5	6.0	ns
		5.0	2.0	4.6	4.5	_
Hold time CP to A or B	t _h	3.3	-1.5	0.0	0.0	_
		5.0	-1.5	0.0	0.0	-
Pulse width CP or MR	t _w	3.3	2.0	5.5	7.0	-
		5.0	2.0	4.5	5.0	-
Recovery time MR or CP	t _{rec}	3.3	-2.5	0.0	0.0	-
		5.0	-1.5	0.0	0.0	-

Note: 1. Voltage Range 3.3 is 3.3 V \pm 0.3 V Voltage Range 5.0 is 5.0 V \pm 0.5 V

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AC Characteristics: HD74ACT164

				Ta = +25°C C _∟ = 50 pF			Ta = −40°C to +85°C C _∟ = 50 pF	
Item	Symbol	V _{cc} (V)* ¹	Min	Тур	Max	Min	Max	Unit
Maximum clock frequency	f_{max}	5.0	100	_	_	80	_	MHz
Propagation delay CP to Q _n	t _{PLH}	5.0	1.0	9.0	11.5	1.0	12.5	ns
Propagation delay CP to Q _n	t _{PHL}	5.0	1.0	9.0	11.5	1.0	12.5	
Propagation delay $\overline{\text{MR}}$ to Q_n	t _{PHL}	5.0	1.0	9.5	13.0	1.0	14.5	

Note: 1. Voltage Range 5.0 is $5.0 \text{ V} \pm 0.5 \text{ V}$

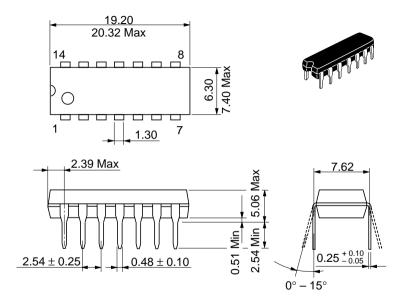
AC Operating Requirements: HD74ACT164

			Ta = +25°C C _∟ = 50 pF		Ta = −40°C to +85°C C _∟ = 50 pF	
Item	Symbol	V _{cc} (V)* ¹	Тур	Guaranteed	l Minimum	Unit
Setup time A or B to CP	t _{su}	5.0	2.5	7.0	8.0	ns
Hold time CP to A or B	t _h	5.0	0.0	1.5	1.5	_
Pulse width CP or MR	t _w	5.0	4.5	7.0	8.0	_
Recovery time MR or CP	t _{rec}	5.0	0.0	2.0	2.0	_

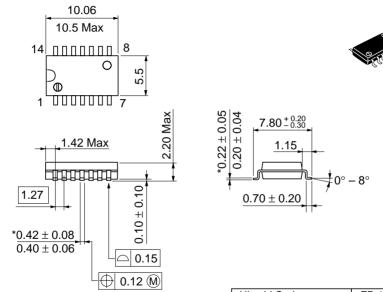
Note: 1. Voltage Range 5.0 is 5.0 V \pm 0.5 V

Capacitance

Item	Symbol	Тур	Unit	Condition
Input capacitance	CIN	4.5	pF	$V_{cc} = 5.5 V$
Power dissipation capacitance	C_{PD}	20.0	pF	$V_{cc} = 5.0 V$

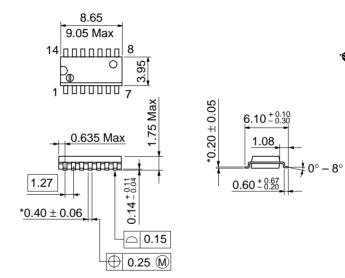


Hitachi Code	DP-14
JEDEC	Conforms
EIAJ	Conforms
Weight (reference value)	0.97 g



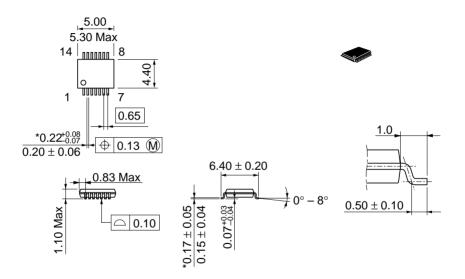
*Dimension including the plating thickness Base material dimension

Hitachi Code	FP-14DA
JEDEC	—
EIAJ	Conforms
Weight (reference value)	0.23 g



Hitachi Code	FP-14DN
JEDEC	Conforms
EIAJ	Conforms
Weight (reference value)	0.13 g

*Pd plating



*Dimension including the plating thickness Base material dimension

Hitachi Code	TTP-14D
JEDEC	
EIAJ	
Weight (reference value)	0.05 g

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