## **HD74AC32**

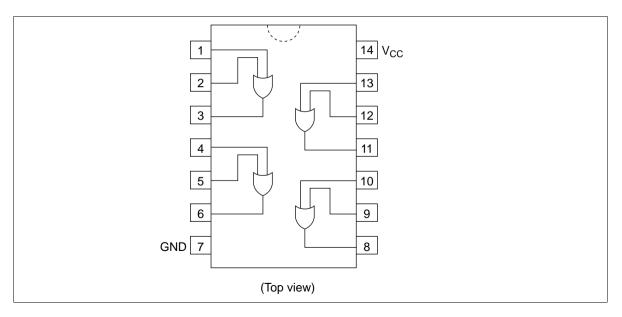
Quad 2-Input OR Gate

# **HITACHI**

#### **Feature**

Outputs Source/Sink 24 mA

### **Pin Arrangement**



### DC Characteristics (unless otherwise specified)

Item	Symbol	Max	Unit	Condition
Maximum quiescent supply current	I <sub>cc</sub>	40	μΑ	$V_{IN} = V_{CC}$ or ground, $V_{CC} = 5.5 \text{ V}$ , Ta = Worst case
Maximum quiescent supply current	I <sub>cc</sub>	4.0	μΑ	$V_{IN} = V_{CC}$ or ground, $V_{CC} = 5.5 \text{ V}$ , Ta = 25°C



## **HD74AC32**

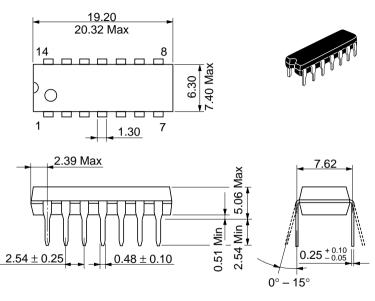
### **AC Characteristics**

			Ta = +25°C C <sub>L</sub> = 50 pF			Ta = $-40^{\circ}$ C to $+85^{\circ}$ C C <sub>L</sub> = 50 pF		
Item	Symbol	V <sub>cc</sub> (V)*1	Min	Тур	Max	Min	Max	Unit
Propagation delay	t <sub>PLH</sub>	3.3	1.0	7.0	9.0	1.0	10.0	ns
		5.0	1.0	5.5	7.5	1.0	8.5	<del>_</del>
Propagation delay	t <sub>PHL</sub>	3.3	1.0	7.0	8.5	1.0	9.0	ns
		5.0	1.0	5.0	7.0	1.0	7.5	<del>_</del>

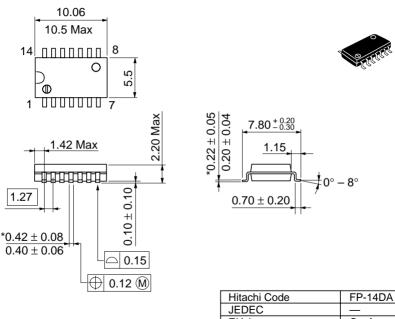
Note: 1. Voltage Range 3.3 is 3.3 V  $\pm$  0.3 V Voltage Range 5.0 is 5.0 V  $\pm$  0.5 V

## Capacitance

Item	Symbol	Тур	Unit	Condition
Input capacitance	C <sub>IN</sub>	4.5	pF	V <sub>CC</sub> = 5.5 V
Power dissipation capacitance	$C_{PD}$	20.0	pF	$V_{CC} = 5.0 \text{ V}$



Hitachi Code	DP-14
JEDEC	Conforms
EIAJ	Conforms
Weight (reference value)	0.97 g

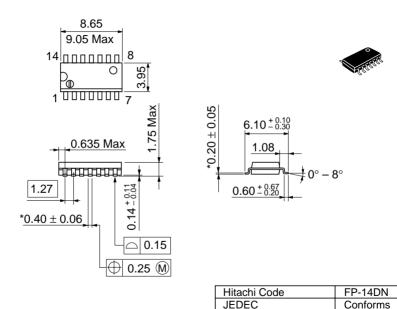


\*Dimension including the plating thickness
Base material dimension

\*Dimension including the plating thickness

Base material dimension

\*United States of The 14-57 of The 14



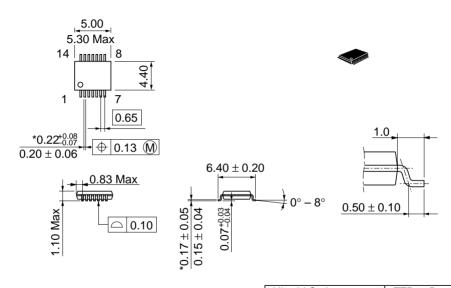
EIAJ

Weight (reference value)

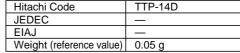
Conforms

0.13 g

\*Pd plating



\*Dimension including the plating thickness
Base material dimension



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