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April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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HD74HC123A

Dual Retriggerable Monostable Multivibrators (with Clear)

REJ03D0564-0200 (Previous ADE-205-438) Rev.2.00 Oct 11, 2005

Description

This multivibrator features both a negative, A, and a positive, B, transition triggered input, either of which can be used as an inhibit input. Also included is a clear input that when taken low resets the one shot. The HD74HC123A can be triggered on the positive transition of the clear while A is held low and B is held high.

The HD74HC123A is retriggerable. That is it may be triggered repeatedly while their outputs are generating a pulse and the pulse will be extended.

Pulse width stability over a wide range of temperature. The output pulse equation is simply: tw = (Rext) (Cext).

Features

• High Speed Operation

• High Output Current: Fanout of 10 LSTTL Loads

• Wide Operating Voltage: $V_{CC} = 2$ to 6 V

Low Input Current: 1 µA max
Low Quiescent Supply Current

• Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
HD74HC123AP	DILP-16 pin	PRDP0016AE-B (DP-16FV)	Р	_
HD74HC123AFPEL	SOP-16 pin (JEITA)	PRSP0016DH-B (FP-16DAV)	FP	EL (2,000 pcs/reel)

Note: Please consult the sales office for the above package availability.

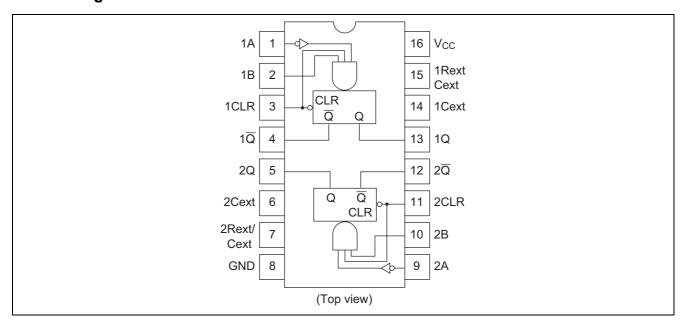
Function Table

	Inputs	Outputs			
Clear	Α	В	Q	Q	
L	X	Х	L	Н	
Χ	Н	X	L	Н	
Х	X	L	L	Н	
Н	L			T	
Н		Н	<u>I</u>	T	
	L	Н		T	

Note: External timing capacitance connects between Cext and Rext/Cext.

H: High levelL: Low levelX: Irrelevant

Pin Arrangement



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit
Supply voltage range	Vcc	-0.5 to 7.0	V
Input / Output voltage	Vin, Vout	-0.5 to V _{CC} +0.5	V
Input / Output diode current	I _{IK} , I _{OK}	±20	mA
Output current	lo	±25	mA
V _{CC} , GND current	I _{CC} or I _{GND}	±50	mA
Power dissipation	P _T	500	mW
Storage temperature	Tstg	-65 to +150	°C

Note: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.

Recommended Operating Conditions

Item	Symbol	Ratings	Unit	Conditions
Supply voltage	V _{CC}	2 to 6	V	
Input / Output voltage	V _{IN} , V _{OUT}	0 to V _{CC}	V	
Operating temperature	Та	-40 to 85	°C	
		0 to 1000		V _{CC} = 2.0 V
Input rise / fall time*1	t _r , t _f	0 to 500	ns	V _{CC} = 4.5 V
		0 to 400		V _{CC} = 6.0 V

Note: 1. This item guarantees maximum limit when one input switches.

Waveform: Refer to test circuit of switching characteristics.

Electrical Characteristics

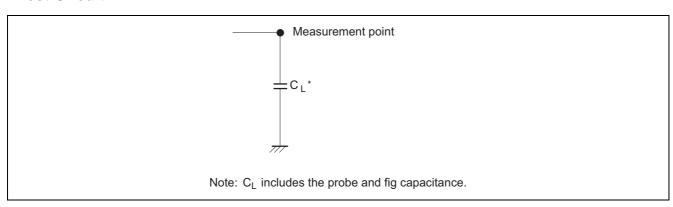
				Т	a = 25°	С	Ta = -40	to+85°C			
It	tem	Symbol	V _{cc} (V)	Min	Тур	Max	Min	Max	Unit	Test 0	Conditions
Input voltag	е	V _{IH}	2.0	1.5	_	_	1.5	_	V		
			4.5	3.15	_	_	3.15	_			
			6.0	4.2	_	_	4.2	_			
		V_{IL}	2.0	_	_	0.5	_	0.5	V		
			4.5	_	_	1.35	_	1.35			
			6.0	_	_	1.8	_	1.8			
Output volta	age	V _{OH}	2.0	1.9	2.0	_	1.9	_	V	$Vin = V_{IH}$	$I_{OH} = -20 \mu A$
			4.5	4.4	4.5	_	4.4	_		or V_{IL}	
			6.0	5.9	6.0	_	5.9	_			
			4.5	4.18	_	_	4.13	_			$I_{OH} = -4 \text{ mA}$
			6.0	5.68	_	_	5.63	_			$I_{OH} = -5.2 \text{ mA}$
		V _{OL}	2.0	_	0.0	0.1	_	0.1	V	$Vin = V_{IH}$	$I_{OL} = 20 \mu A$
			4.5	_	0.0	0.1	_	0.1		or V_{IL}	
			6.0	_	0.0	0.1	_	0.1			
			4.5	_	_	0.26	_	0.33			$I_{OL} = 4 \text{ mA}$
			6.0	_	_	0.26	_	0.33			$I_{OL} = 5.2 \text{ mA}$
Input currer	nt	lin	6.0	_	_	±0.1	_	±1.0	μΑ	$Vin = V_{CC}$	or GND
Quiescent	Standby state	I _{CC}	6.0	_	_	130	_	220	μΑ	$Vin = V_{CC}$	lout = 0 μA
supply	Active state		6.0	_	_	130	_	220		or GND	Rext/Cext =
current											0.5 V _{CC}

Switching Characteristics ($C_L = 50 \text{ pF}$, Input $t_r = t_f = 6 \text{ ns}$)

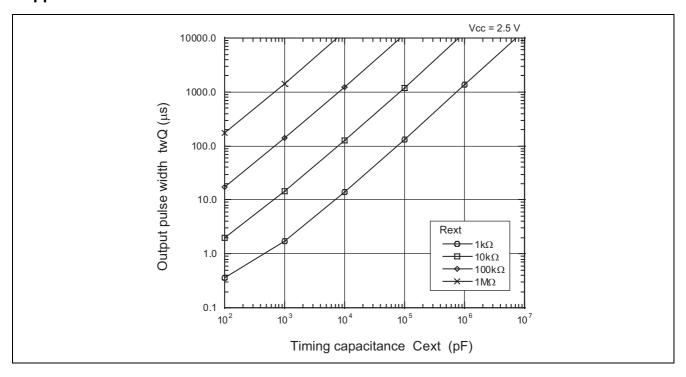
			Ta = 25°C		Ta = -40	to +85°C					
Item	Symbol	V _{cc} (V)	Min	Тур	Max	Min	Max	Unit	Test Cond	ditions	
Propagation delay	t _{PLH}	2.0	_	_	210	_	265	ns	A, B or Clear to C	Q	
time		4.5	_	22	42	_	53				
		6.0	_	_	36	_	45				
	t _{PHL}	2.0	_	_	240		300	ns	A, B or Clear to C	<u> </u>	
		4.5	_	23	48		60				
		6.0	_	_	41		51				
	t _{PHL}	2.0	_	_	170	_	215	ns	Clear to Q		
		4.5	_	18	34	_	43				
		6.0	_	_	29	_	37				
	t _{PLH}	2.0	_	_	180	_	225	ns	Clear to \overline{Q}		
		4.5	_	16	36	_	45				
		6.0	_	_	31	_	38				
Output rise/fall	t _{TLH} , t _{THL}	2.0	_	_	75	_	95	ns			
time		4.5	_	5	15	_	19				
		6.0	_	_	13	_	16				
Pulse width	t _w	2.0	150	_	_	190	_	ns	A, B, Clear		
		4.5	30	6	_	38	_				
		6.0	26	_	_	33	_				
Minimum output	$t_{wQ(min)}$	2.0	_	1.5	_	_	_	μs	Cext = 28 pF	Rext = $6 \text{ k}\Omega$	
pulse width		4.5	_	450	_	_	_	ns		Rext = $2 k\Omega$	
		6.0	_	380	_	_	_				
Output pulse width	t _{wQ}	4.5	_	1.0	_	_	_	ms	Cext = $0.1 \mu F$, Re	$ext = 10 k\Omega$	
Input capacitance	Cin	_	_	5	10	_	10	pF			

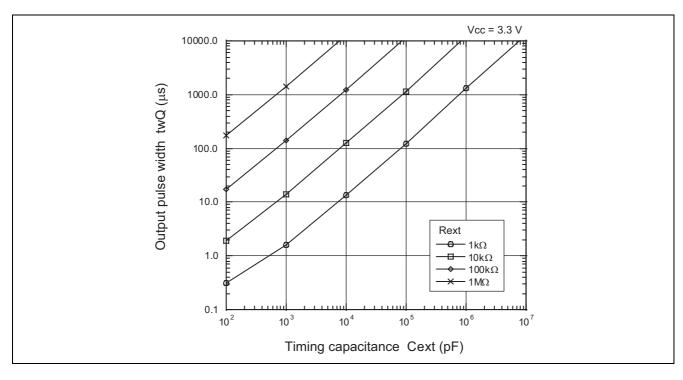
Caution in use: In order to prevent any malfunctions due to noise, connect a high-frequency performance capacitor between V_{CC} and GND, and keep the wiring between the External components and Cext, Rext/Cext pins as short as possible.

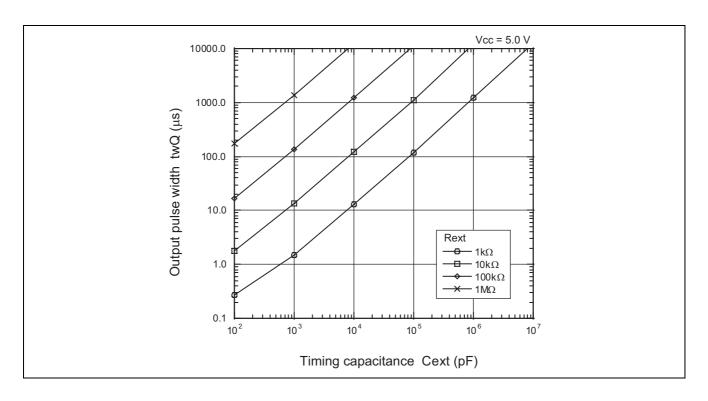
Test Circuit

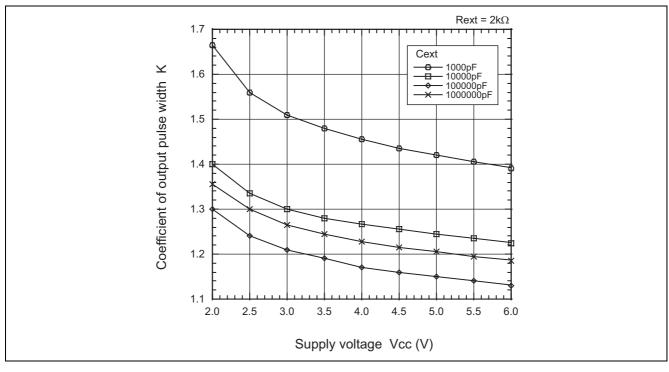


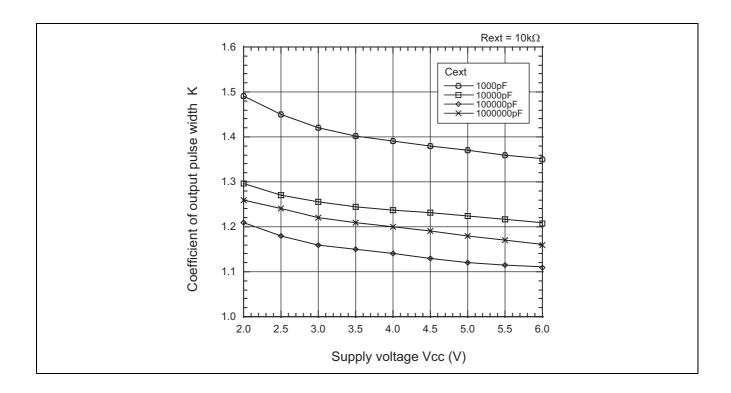
Application Data



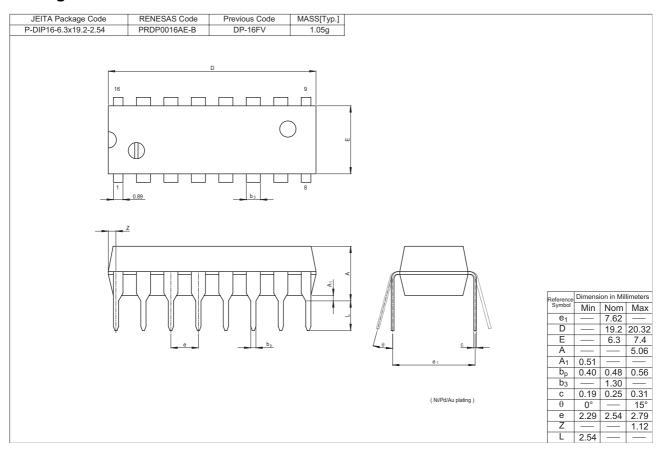


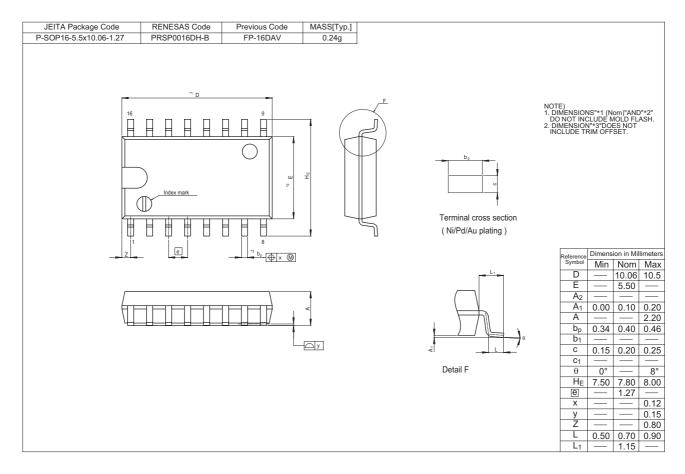






Package Dimensions





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