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April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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HD74HC138

3-to-8-line Decoder/Demultiplexer with Address Latch

REJ03D0570-0300 Rev.3.00 Mar 25, 2009

Description

The HD74HC138 has 3 binary select inputs (A, B and C). If the device is enabled these inputs determine which one of the eight normally high outputs will go low. Two active low and one active high enables (G_1 , G_{2A} and G_{2B}) are provided to ease the cascading of decoders.

Features

• High Speed Operation: t_{pd} (A, B, C to Y) = 16.5 ns typ ($C_L = 50 \text{ pF}$)

• High Output Current: Fanout of 10 LSTTL Loads

• Wide Operating Voltage: $V_{CC} = 2 \text{ V}$ to 6 V

• Low Input Current: 1 μA max

• Low Quiescent Supply Current: I_{CC} (static) = 4 μ A max (Ta = 25°C)

• Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
HD74HC138P	DILP-16 pin	PRDP0016AE-B (DP-16FV)	Р	
HD74HC138FPEL	SOP-16 pin (JEITA)	PRSP0016DH-B (FP-16DAV)	FP	EL (2,000 pcs/reel)
HD74HC138RPEL	PEL SOP-16 pin (JEDEC) PRSP0016DG-A (FP-16DNV) R		RP	EL (2,500 pcs/reel)
HD74HC138TELL	4HC138TELL TSSOP-16 pin		Т	ELL (2,000 pcs/reel)

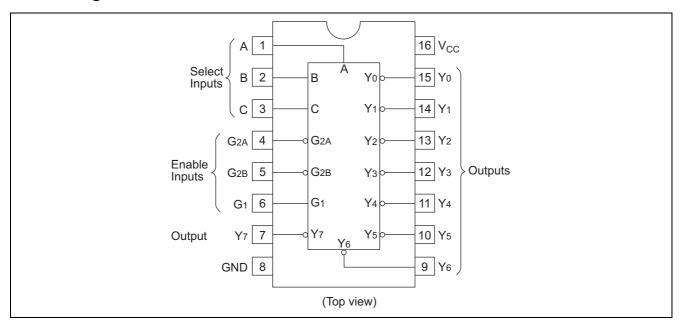
Note: Please consult the sales office for the above package availability.

Function Table

Inputs													
	Enable			Select		Outputs							
G1	G _{2A}	G _{2B}	С	В	Α	Y ₀	Y ₁	Y ₂	Y ₃	Y ₄	Y ₅	Y ₆	Y ₇
Х	Х	Н	Х	Χ	Х	Н	Н	Н	Н	Н	Н	Н	Н
Х	Н	Х	Х	Х	Х	Н	Н	Н	Н	Н	Н	Н	Н
L	Х	Х	Х	Х	Х	Н	Н	Н	Н	Н	Н	Н	Н
Н	L	L	L	L	L	L	Н	Н	Н	Н	Н	Н	Н
Н	L	L	L	L	Н	Н	L	Н	Н	Н	Н	Н	Н
Н	L	L	L	Н	L	Н	Н	L	Н	Н	Н	Н	Н
Н	L	L	L	Н	Н	Н	Н	Н	L	Н	Н	Н	Н
Н	L	L	Н	L	L	Н	Н	Н	Н	L	Н	Н	Н
Н	L	L	Н	L	Н	Н	Н	Н	Н	Н	L	Н	Н
Н	L	L	Н	Н	L	Н	Н	Н	Н	Н	Н	L	Н
Н	L	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	L

H: High level
L: Low level
X: Irrelevant

Pin Arrangement



Absolute Maximum Ratings

Item	Symbol	Rating	Unit
Supply voltage range	Vcc	-0.5 to +7.0	V
Input voltage	V _{IN}	-0.5 to V _{CC} + 0.5	V
Output voltage	V _{OUT}	-0.5 to V _{CC} + 0.5	V
Output current	l _{оит}	±25	mA
DC current drain per V _{CC} , GND	I _{CC} , I _{GND}	±50	mA
DC input diode current	I _{IK}	±20	mA
DC output diode current	lok	±20	mA
Power dissipation per package	P _T	500	mW
Storage temperature	Tstg	-65 to +150	°C

Note: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.



Recommended Operating Conditions

Item	Symbol	Ratings	Unit	Conditions
Supply voltage	V _{CC}	2 to 6	V	
Input / Output voltage	V _{IN} , V _{OUT}	0 to V _{CC}	V	
Operating temperature	Та	-40 to 85	°C	
		0 to 1000		V _{CC} = 2.0 V
Input rise / fall time ^{*1}	t _r , t _f	0 to 500	ns	V _{CC} = 4.5 V
		0 to 400]	V _{CC} = 6.0 V

Note: 1. This item guarantees maximum limit when one input switches.

Waveform: Refer to test circuit of switching characteristics.

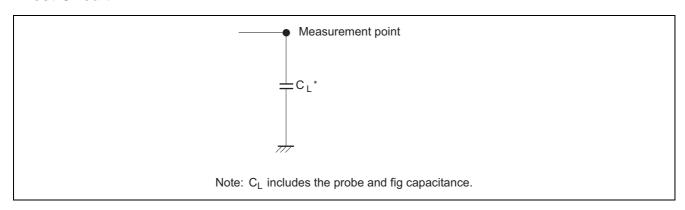
Electrical Characteristics

Item	Symbol	V _{cc} (V)	Т	a = 25°	С	Ta = -40	to+85°C	Unit	Test Conditions	
item	Symbol	ACC (A)	Min	Тур	Max	Min	Max	Oilit		
		2.0	1.5	_	_	1.5	_			
	V_{IH}	4.5	3.15	_	_	3.15	_	V		
Input voltage		6.0	4.2	1	_	4.2	_			
input voitage		2.0	_	_	0.5	_	0.5			
	V_{IL}	4.5		1	1.35	_	1.35	V		
		6.0		l	1.8	_	1.8			
		2.0	1.9	2.0	_	1.9	_			
	V _{OH}	4.5	4.4	4.5	_	4.4		٧		$I_{OH} = -20 \mu A$
		6.0	5.9	6.0	_	5.9			$Vin = V_{IH} \text{ or } V_{IL}$	
		4.5	4.18	l	_	4.13				$I_{OH} = -4 \text{ mA}$
Output voltage		6.0	5.68	l	_	5.63				$I_{OH} = -5.2 \text{ mA}$
Output voltage	V _{OL}	2.0		0.0	0.1	_	0.1			
		4.5		0.0	0.1	_	0.1			$I_{OL} = 20 \mu A$
		6.0		0.0	0.1	_	0.1	V	$Vin = V_{IH} \text{ or } V_{IL}$	
		4.5			0.26	_	0.33		$I_{OL} = 4$	$I_{OL} = 4 \text{ mA}$
		6.0		1	0.26	_	0.33			$I_{OL} = 5.2 \text{ mA}$
Input current	lin	6.0			±0.1	_	±1.0	μΑ	Vin = V _{CC} or GN	D
Quiescent supply current	I _{CC}	6.0	_	_	4.0	_	40	μА	Vin = V _{CC} or GN	D, lout = 0 μA

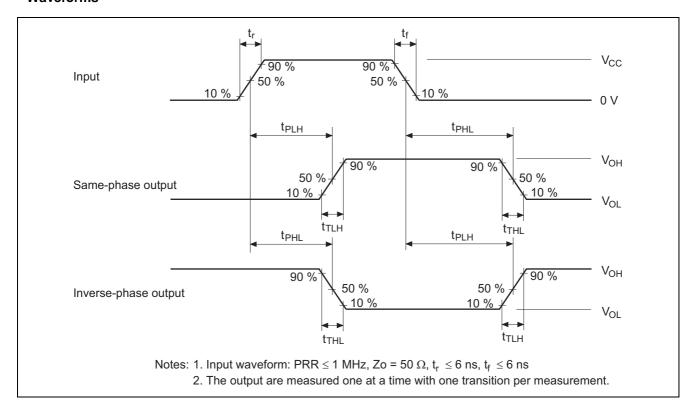
Switching Characteristics ($C_L = 50 \text{ pF}$, Input $t_r = t_f = 6 \text{ ns}$)

Item	Symbol	V _{cc} (V)	Т	a = 25°	С	Ta = -40	to +85°C	Unit	Test Conditions
item	Syllibol	ACC (A)	Min	Тур	Max	Min	Max	Onit	rest Conditions
		2.0	_	_	175	_	220		
	t _{PHL}	4.5	_	17	35		44	ns	
		6.0	_	_	30	_	37		A, B or C to Y
		2.0	_		150		190		A, B of C to 1
	t _{PLH}	4.5	_	16	30		38	ns	
		6.0	_		26		33		
		2.0	_		150		190		
	t _{PHL}	4.5	_	16	30	_	38	ns	
Propagation delay		6.0		_	26	—	33		G₁ to Y
time	t _{PLH}	2.0	_		150		190		
		4.5	_	17	30	_	38	ns	
		6.0	_	_	26	_	33		
		2.0	_		175	_	220		
	t _{PHL}	4.5	_	15	35	_	44	ns	
		6.0	_	_	30		37		G_{2A} or G_{2B} to Y
		2.0	_		150	_	190		G _{2A} of G _{2B} to 1
	t _{PLH}	4.5	_	17	30	_	38	ns	
Output rise/fall time		6.0	_		26		33		
		2.0	_		75		90		
	t _{TLH} , t _{THL}	4.5	_	5	15		19	ns	
		6.0			13		16		
Input capacitance	Cin	_	_	5	10	_	10	pF	

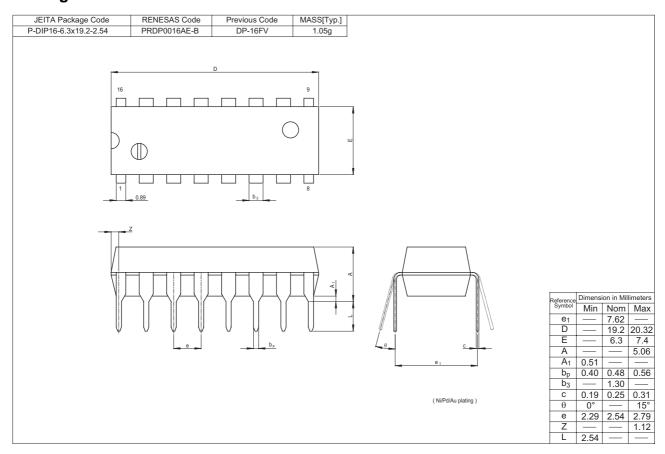
Test Circuit

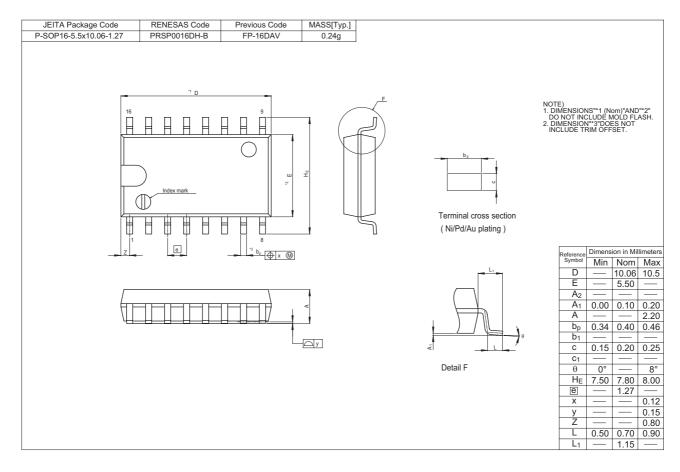


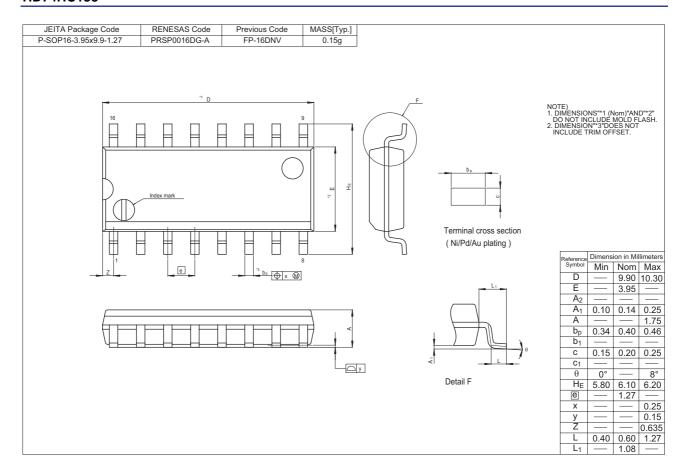
Waveforms

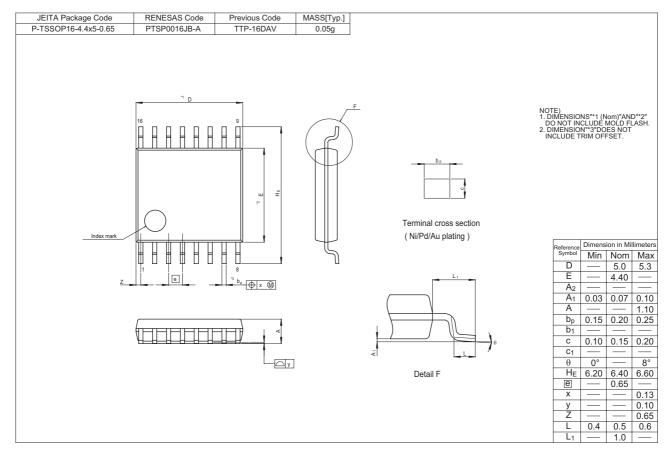


Package Dimensions









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