

# **HD74HC423A**

# **Dual Retriggerable Monostable Multivibrators**

REJ03D0626-0200 (Previous ADE-205-505) Rev.2.00 Mar 30, 2006

## **Description**

This multivibrator features output-pulse-duration control by two methods. The basic pulse duration is programmed by selection of external resistance and capacitance values. Once triggered, the basic pulse duration may be extended by retriggering the gated low-level-active (A) or high-level-active (B) inputs, or be reduced by use of the overriding clear. The B input is a Schmitt trigger enabling jitter-free triggering from input signals with slow transition rates.

#### **Features**

• High Speed Operation

• High Output Current: Fanout of 10 LSTTL Loads

• Wide Operating Voltage:  $V_{CC} = 2$  to 6 V

Low Input Current: 1 µA max
Low Quiescent Supply Current

• Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
HD74HC423AP	DILP-16 pin	PRDP0016AE-B (DP-16FV)	Р	_
HD74HC423AFPEL	SOP-16 pin (JEITA)	PRSP0016DH-B (FP-16DAV)	FP	EL (2,000 pcs/reel)

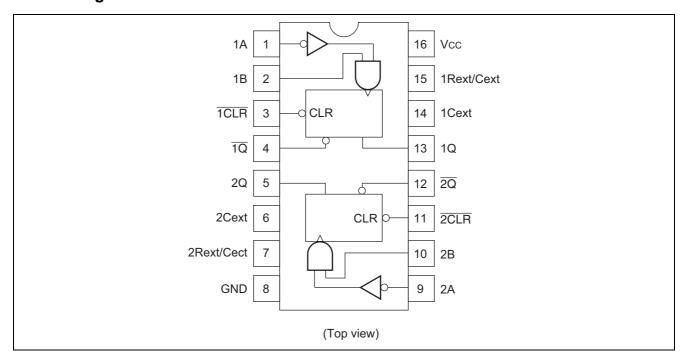
Note: Please consult the sales office for the above package availability.

#### **Function Table**

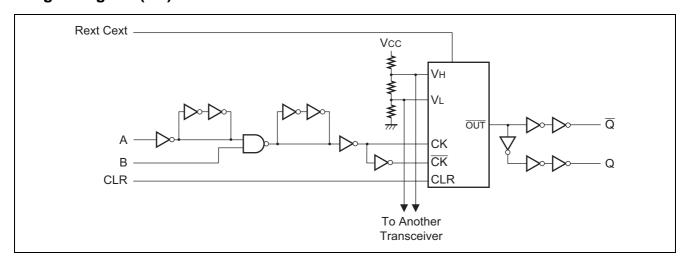
	Inputs	Outputs			
Clear	A	В	Q	Q	
L	X	X	L	Н	
X	Н	X	L	Н	
X	X	L	L	Н	
Н	L			T	
Н		Н		T	

Note: 1. H; High level, L; Low level, X; Irrelevant

## **Pin Arrangement**



## Logic Diagram (1/2)



## **Absolute Maximum Ratings**

Item	Symbol	Ratings	Unit
Supply voltage range	Vcc	-0.5 to 7.0	V
Input / Output voltage	V <sub>IN</sub> , V <sub>OUT</sub>	-0.5 to V <sub>CC</sub> +0.5	V
Input / Output diode current	I <sub>IK</sub> , I <sub>OK</sub>	±20	mA
Output current	l <sub>out</sub>	±25	mA
V <sub>CC</sub> , GND current	I <sub>CC</sub> or I <sub>GND</sub>	±50	mA
Power dissipation	P <sub>T</sub>	500	mW
Storage temperature	Tstg	-65 to +150	°C

Note: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.

## **Recommended Operating Conditions**

Item	Symbol	Ratings	Unit	Conditions
Supply voltage	V <sub>CC</sub>	2 to 6	V	
Input / Output voltage	V <sub>IN</sub> , V <sub>OUT</sub>	0 to V <sub>CC</sub>	V	
Operating temperature	Та	-40 to 85	°C	
		0 to 1000		$V_{CC} = 2.0 \text{ V}$
Input rise / fall time*1	$\operatorname{ne}^{*1}$ $\operatorname{t_r},\operatorname{t_f}$	0 to 500	ns	V <sub>CC</sub> = 4.5 V
		0 to 400		V <sub>CC</sub> = 6.0 V

Note: 1. This item guarantees maximum limit when one input switches.

Waveform: Refer to test circuit of switching characteristics.

#### **Electrical Characteristics**

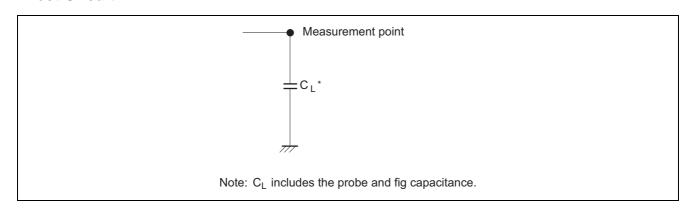
				Т	a = 25°	С	Ta = -40 to+85°C				
Item	า	Symbol	V <sub>CC</sub> (V)	Min	Тур	Max	Min	Max	Unit	Test Conditions	
Input voltag	je	V <sub>IH</sub>	2.0	1.5	_	_	1.5	_	V		
			4.5	3.15	_	_	3.15	_			
			6.0	4.2	_	_	4.2	_			
		$V_{IL}$	2.0			0.5		0.5	V		
			4.5			1.35		1.35			
			6.0			1.8		1.8			
Output volta	age	V <sub>OH</sub>	2.0	1.9	2.0	1	1.9		V	$Vin = V_{IH} or V_{IL}$	$I_{OH} = -20 \mu A$
			4.5	4.4	4.5	1	4.4				
			6.0	5.9	6.0	_	5.9	_			
			4.5	4.18	_	_	4.13	_			$I_{OH} = -4 \text{ mA}$
			6.0	5.68		_	5.63	_			$I_{OH} = -5.2 \text{ mA}$
		V <sub>OL</sub>	2.0	_	0.0	0.1	_	0.1	V	$Vin = V_{IH} or V_{IL}$	$I_{OL} = 20 \mu A$
			4.5	_	0.0	0.1	_	0.1			
			6.0	_	0.0	0.1	_	0.1			
			4.5	_	_	0.26	_	0.33			$I_{OH} = 4 \text{ mA}$
			6.0	_	_	0.26	_	0.33			$I_{OH} = 5.2 \text{ mA}$
Input curren	nt	lin	6.0	1	1	±0.1	—	±1.0	μΑ	Vin = V <sub>CC</sub> or GND	
Quiescent	Standby	I <sub>CC</sub>	6.0			130		220	μΑ	$Vin = V_{CC} or$	$I_{OUT} = 0 \mu A$
Supply	state									GND	
current	Active			_	_	130	_	220			Rext / Cext =
:	state										0.5 V <sub>CC</sub>

## **Switching Characteristics** ( $C_L = 50 \text{ pF}$ , Input $t_r = t_f = 6 \text{ ns}$ )

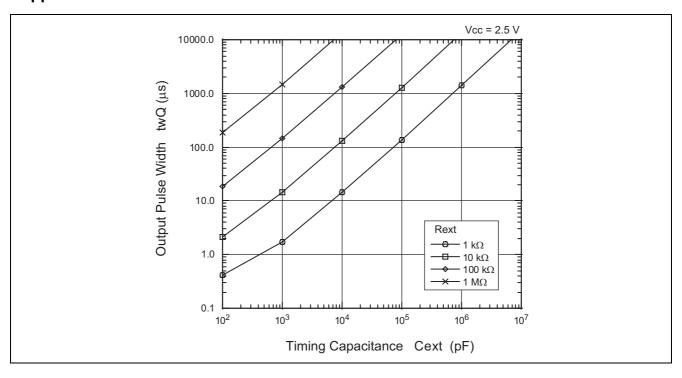
			Т	Ta = 25°C T		Ta = -40 to +85°C				
Item	Symbol	V <sub>CC</sub> (V)	Min	Тур	Max	Min	Max	Unit	Test Co	nditions
Propagation delay	t <sub>PLH</sub>	2.0	_	_	210	_	265	ns	A or B to Q	
time		4.5	_	20	42	_	53			
		6.0	_	_	36	_	45			
	t <sub>PHL</sub>	2.0	_	_	240	_	300	ns	A or B to Q	
		4.5	_	21	48	_	60			
		6.0	_	_	41	_	51			
	t <sub>PHL</sub>	2.0	_	_	170	_	215	ns	Clear to Q	
		4.5	_	17	34	_	43			
		6.0	_	_	29	_	37			
	t <sub>PLH</sub>	2.0	_	_	180	_	225	ns	Clear to Q	
		4.5	_	15	36	_	45			
		6.0	_	_	31	_	38			
Pulse width	t <sub>w</sub>	2.0	150	_	_	190		ns	A, B, Clear	
		4.5	30	6	_	38	_			
		6.0	26	_	_	33				
Removal time	t <sub>rem</sub>	2.0	0	_	_	5	_	ns	Clear	
		4.5	0	-3	_	5	_			
		6.0	0	_	_	5	_			
Minimum output	t <sub>WQ (min)</sub>	2.0	_	1.5	_	_	_	μs	Cext = 28 pF	Rext = $6 \text{ k}\Omega$
pulse width		4.5	_	450	_	_		ns		Rext = $2 k\Omega$
		6.0	1	380	_	_				
Output pulse width	t <sub>WQ</sub>	4.5	1	1.0	_	_	_	ms	Cext = 0.1 $\mu$ F, Rext = 10 $k\Omega$	
Output rise/fall	t <sub>TLH</sub>	2.0	1	_	75	_	95	ns		
time	t <sub>THL</sub>	4.5	_	5	15	_	19			
		6.0	_	_	13	_	16			
Input capacitance	Cin		_	_	20	_	20	pF	Pins 7 & 15	
		_		5	10	_	10		Other pins	

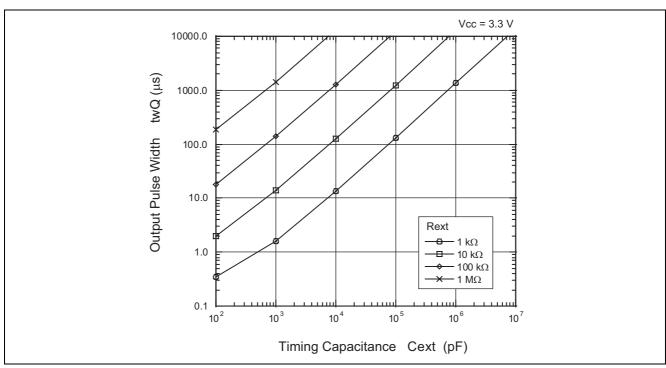
Caution in use: In order to prevent any malfunctions due to noise, connect a highfrequency performance capacitor between V<sub>CC</sub> and GND, and keep the wiring between the external components and Cext, Rext/Cext pins as short as possible.

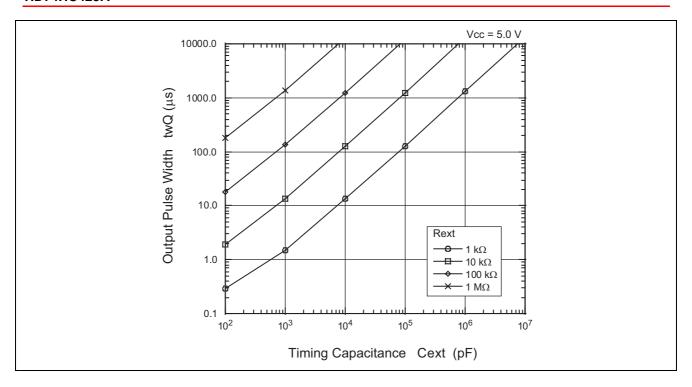
## **Test Circuit**

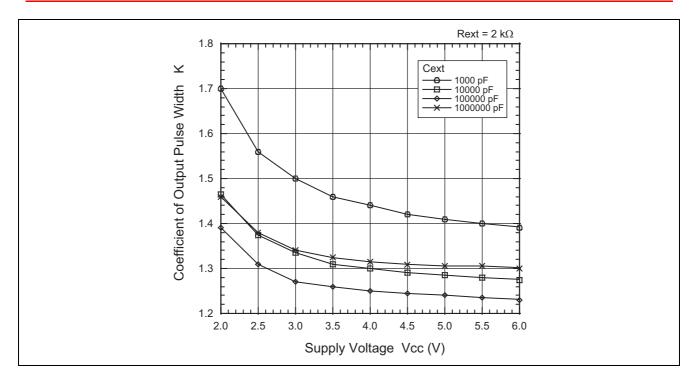


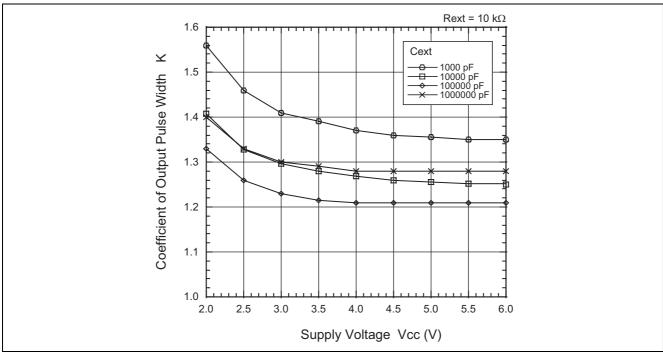
## **Application Data**



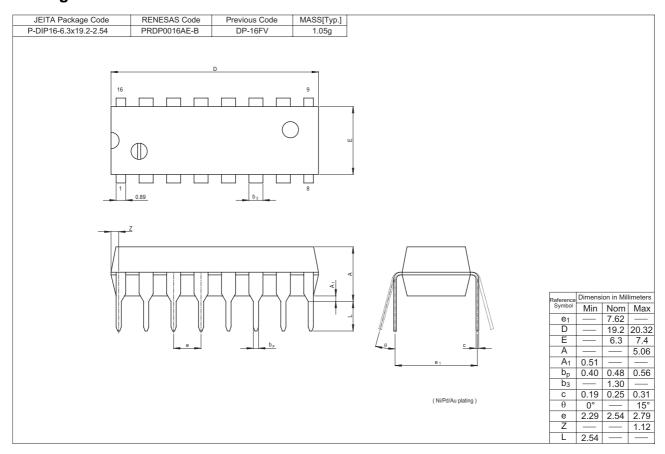


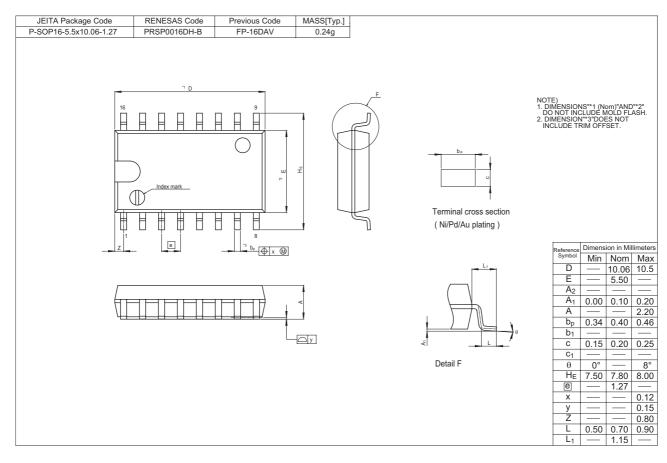






### **Package Dimensions**





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