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# HD74HCT688

## 8-bit Magnitude Comparator

# HITACHI

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### Description

The HD74HCT688 compares bit for bit two 8-bit words and indicate whether or not they are equal. The  $\overline{P=Q}$  output indicates equality when it is low. A single active low enable is provided to facilitate cascading of several packages and enable comparison of words greater than 8 bits. This device is useful in memory block decoding applications, where memory block enable signals must be generated from computer address information.

### Features

- LSTTL Output Logic Level Compatibility as well as CMOS Output Compatibility
- High Speed Operation:  $t_{pd}$  (Data to  $\overline{P=Q}$ ) = 18 ns typ ( $C_L = 50$  pF)
- High Output Current: Fanout of 10 LSTTL Loads
- Wide Operating Voltage:  $V_{CC} = 4.5$  to  $5.5$  V
- Low Input Current: 1  $\mu$ A max
- Low Quiescent Supply Current:  $I_{CC}$  (static) = 4  $\mu$ A max ( $T_a = 25^\circ\text{C}$ )

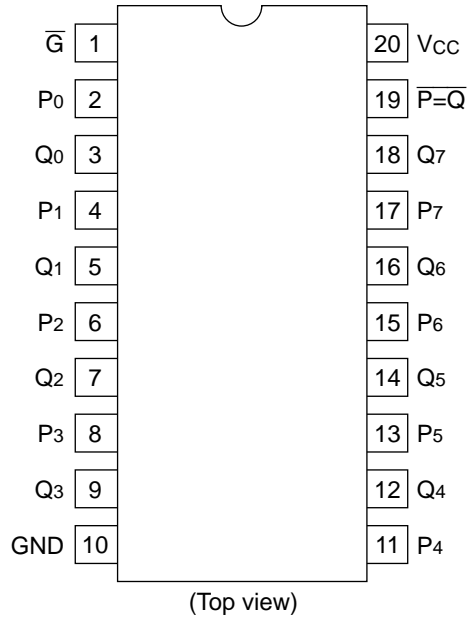
### Function Table

#### Inputs

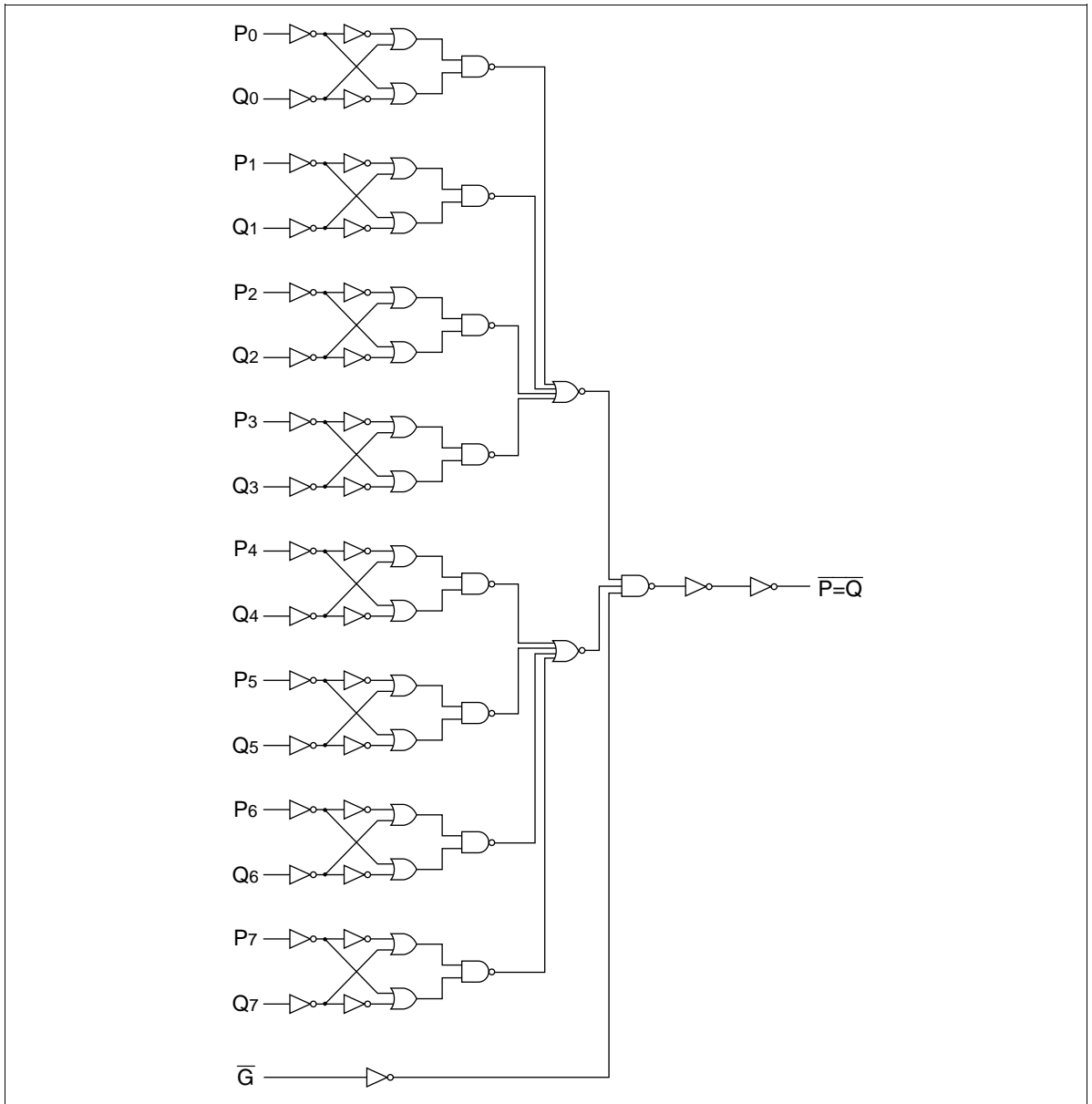
Data P, Q	Enable $\overline{G}$	$\overline{P=Q}$
P=Q	L	L
P>Q	L	H
P<Q	L	H
X	H	H

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## Pin Arrangement



Block Diagram

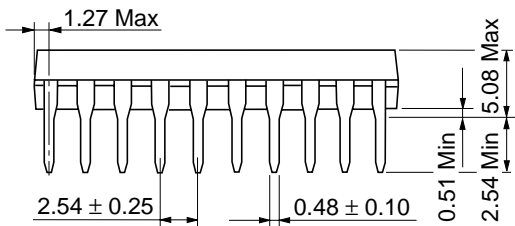
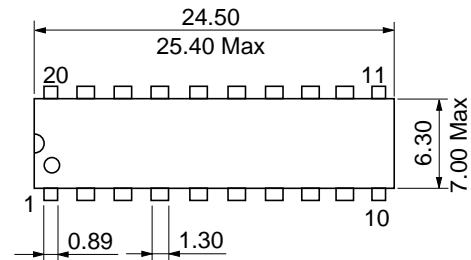


## DC Characteristics

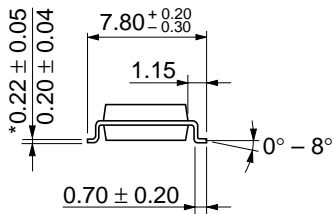
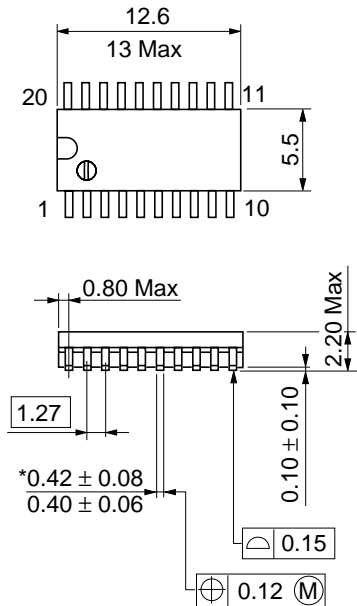
Item	Symbol	Ta = 25°C		Ta = -40 to +85°C		Unit	Test Conditions	
		Min	Typ	Max	Min		Max	V <sub>cc</sub> (V)
Input voltage	V <sub>IH</sub>	2.0	—	—	2.0	—	V	4.5 to 5.5
	V <sub>IL</sub>	—	—	0.8	—	0.8	V	4.5 to 5.5
Output voltage	V <sub>OH</sub>	4.4	—	—	4.4	—	V	4.5 Vin = V <sub>IH</sub> or V <sub>IL</sub> I <sub>OH</sub> = -20 μA
		4.18	—	—	4.13	—		4.5 I <sub>OH</sub> = -4 mA
	V <sub>OL</sub>	—	—	0.1	—	0.1	V	4.5 Vin = V <sub>IH</sub> or V <sub>IL</sub> I <sub>OL</sub> = 20 μA
		—	—	0.26	—	0.33		4.5 I <sub>OL</sub> = 4 mA
Input current	I <sub>in</sub>	—	—	±0.1	—	±1.0	μA	5.5 Vin = V <sub>cc</sub> or GND
Quiescent current	I <sub>cc</sub>	—	—	4.0	—	40	μA	5.5 Vin = V <sub>cc</sub> or GND, I <sub>out</sub> = 0 μA

## AC Characteristics (C<sub>L</sub> = 50 pF, Input t<sub>r</sub> = t<sub>f</sub> = 6 ns)

Item	Symbol	Ta = 25°C		Ta = -40 to +85°C		Unit	Test Conditions	
		Min	Typ	Max	Min		Max	V <sub>cc</sub> (V)
Propagation delay time	t <sub>PLH</sub>	—	17	42	—	53	ns	4.5 Por Q to output
	t <sub>PHL</sub>	—	19	42	—	53		4.5
	t <sub>PLH</sub>	—	9	24	—	30	ns	4.5 Enable to output
	t <sub>PHL</sub>	—	12	24	—	30		4.5
Output rise/fall time	t <sub>TLH</sub>	—	5	15	—	19	ns	4.5
	t <sub>THL</sub>	—	5	15	—	19		4.5
Input capacitance	C <sub>in</sub>	—	5	10	—	10	pF	—

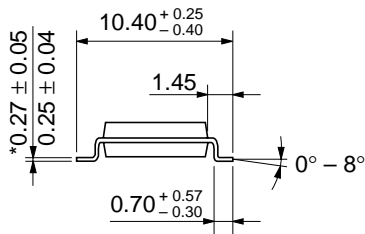
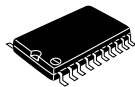
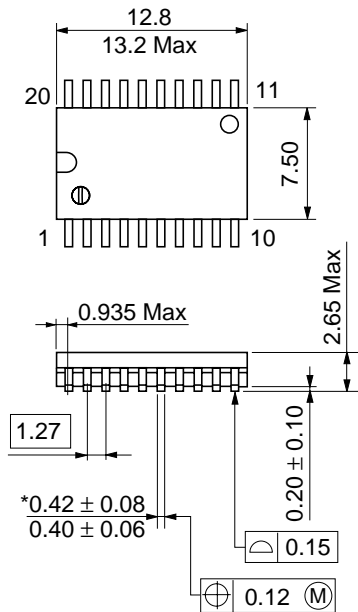


Hitachi Code	DP-20N
JEDEC	—
EIAJ	Conforms
Weight (reference value)	1.26 g



Hitachi Code	FP-20DA
JEDEC	—
EIAJ	Conforms
Weight (reference value)	0.31 g

\*Dimension including the plating thickness  
Base material dimension



Hitachi Code	FP-20DB
JEDEC	Conforms
EIAJ	—
Weight (reference value)	0.52 g

\*Dimension including the plating thickness  
 Base material dimension

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