# RENESAS

# HD74LS192

Synchronous Up / Down Decade Counter (dual clock lines)

REJ03D0454–0200 Rev.2.00 Feb.18.2005

Synchronous operation is provided by having all flip-flops clocked simultaneously so that the output change coincidently with each other when so instructed by the steering logic. This mode of operation eliminates the output counting spikes which are normally associated with asynchronous (ripple clock) counters. The outputs of the four master-slave flip-flops are triggered by a low-to-high-level transition of either count (clock) input. The direction of counting is determined by which count input is pulsed while the other count input is high. This counter is fully programmable; that is, each output may be preset to either level by desired data at the data inputs while the load inputs is low. The output will change to agree with the data inputs independently of the count pulses. This feature allows the counters to be used as modulo-N dividers by simply modifying the count length with the preset inputs. A clear input has been provided which forces all outputs to the low level when a high level is applied. The clear function is independent of the count and load inputs. The clear, count, and load inputs are buffered to lower the drive requirements. This reduces the number of clock drivers, etc., required for long words. This counter was designed to be cascaded without the need for external circuitry. Both borrow and carry outputs are available to cascade both the up-and down-counting functions.

The borrow output produces a pulse equal in width to the count-down input when the counter underflows. Similarly, the carry output produces a pulse equal in width to the count up input when an overflow condition exists.

The counters can be easily cascaded by feeding the borrow and carry outputs to the count-down and count-up inputs respectively of .the succeeding counter.

# Features

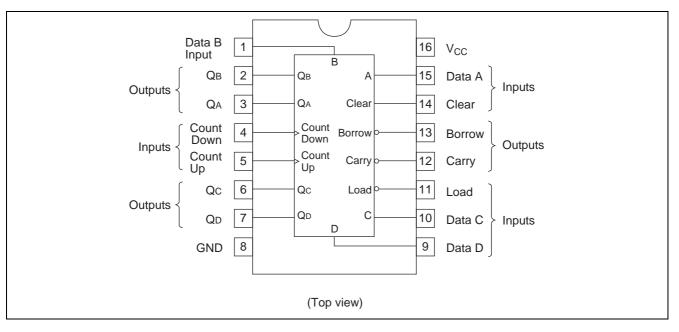
Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
HD74LS192P	DILP-16 pin	PRDP0016AE-B (DP-16FV)	Ρ	—
HD74LS192FPEL	SOP-16 pin (JEITA)	PRSP0016DH-B (FP-16DAV)	FP	EL (2,000 pcs/reel)

Note: Please consult the sales office for the above package availability.

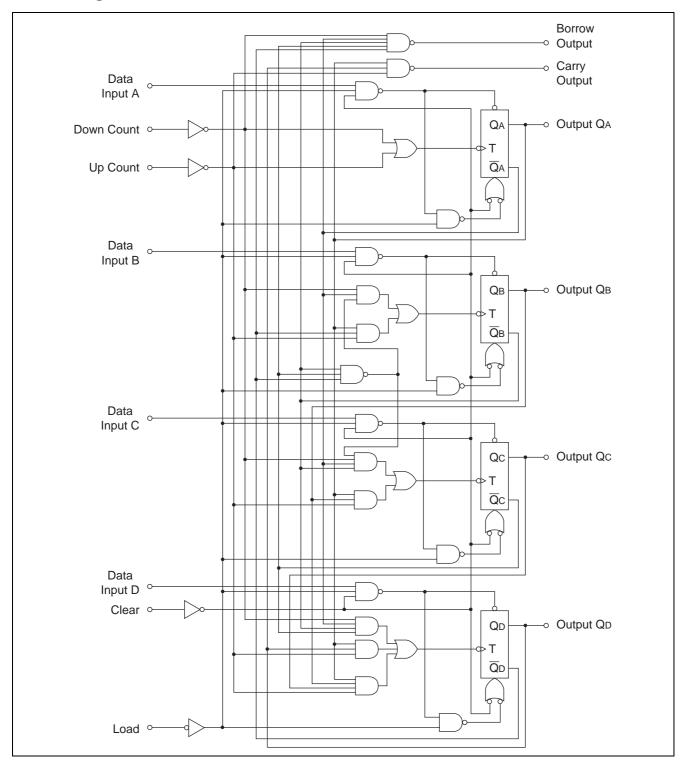


## **Pin Arrangement**





### **Block Diagram**



# **Absolute Maximum Ratings**

Item	Symbol	Ratings	Unit
Supply voltage	V <sub>CC</sub>	7	V
Input voltage	V <sub>IN</sub>	7	V
Power dissipation	P <sub>T</sub>	400	mW
Storage temperature	Tstg	-65 to +150	°C

Note: Voltage value, unless otherwise noted, are with respect to network ground terminal.



# **Recommended Operating Conditions**

ltem	Symbol	Min	Тур	Max	Unit
Supply voltage	V <sub>CC</sub>	4.75	5.00	5.25	V
Output current	I <sub>OH</sub>	_	—	-400	μA
Oulput current	I <sub>OL</sub>		—	8	mA
Operating temperature	T <sub>opr</sub>	-20	25	75	°C
Clock frequency	$f_{clock}$	0	—	25	MHz
Pulse width	t <sub>w</sub>	20	—	—	ns
Setup time (Clear)	t <sub>su (CLR)</sub>	40	—	—	ns
Setup time	t <sub>su</sub>	20	—	—	ns
Hold time	t <sub>h</sub>	3	—	—	ns

# **Electrical Characteristics**

 $(Ta = -20 \text{ to } +75 \ ^{\circ}\text{C})$ 

Item	Symbol	min.	typ.*	max.	Unit	Condition
Input voltage	VIH	2.0	—	—	V	
Input voltage	VIL	_	—	0.8	V	
	V <sub>он</sub>	2.7			V	$V_{CC} = 4.75 \text{ V}, V_{IH} = 2 \text{ V}, V_{IL} = 0.8 \text{ V},$
	∨он	2.7	_		v	I <sub>OH</sub> = -400 μA
Output voltage	V <sub>OL</sub>	_	—	0.4	V	$I_{OL} = 4 \text{ mA}$ $V_{CC} = 4.75 \text{ V}, V_{IH} = 2 \text{ V},$
		_	_	0.5	v	$I_{OL} = 8 \text{ mA}$ $V_{IL} = 0.8 \text{ V}$
	I <sub>IH</sub>	_	—	20	μA	$V_{CC} = 5.25 \text{ V}, \text{ V}_{I} = 2.7 \text{ V}$
Input current	IIL	_	—	-0.4	mA	$V_{CC} = 5.25 \text{ V}, \text{ V}_{I} = 0.4 \text{ V}$
	I <sub>I</sub>	_	—	0.1	mA	$V_{CC} = 5.25 \text{ V}, \text{ V}_{I} = 7 \text{ V}$
Short-circuit output	la a	-20		-100	mA	$V_{CC} = 5.25 V$
current	I <sub>OS</sub>	-20		-100	MA	$v_{\rm CC} = 5.25 v$
Supply current**	I <sub>CC</sub>		19	34	mA	$V_{CC} = 5.25 V$
Input clamp voltage	VIK	_		-1.5	V	$V_{CC} = 4.75 \text{ V}, \text{ I}_{IN} = -18 \text{ mA}$

Notes: \*  $V_{CC} = 5 V$ , Ta = 25°C

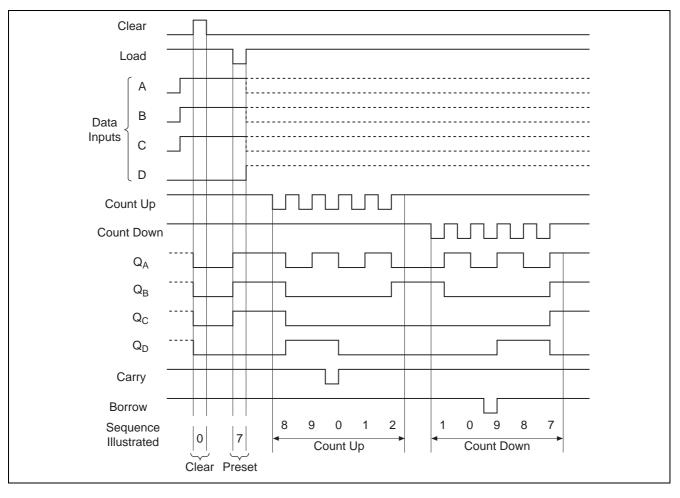
 $^{**}$  I<sub>CC</sub> is measured with all outputs open, clear and load inputs grounded, and all other inputs at 4.5 V.

# **Switching Characteristics**

 $(V_{CC} = 5 V, Ta = 25^{\circ}C)$ 

Item	Symbol	Inputs	Outputs	min.	typ.	max.	Unit	Condition
Maximum clock frequency	$f_{\sf max}$			25	32	—	MHz	
	t <sub>PLH</sub>	Countin	Carry	_	17	26	ns	C <sub>L</sub> = 15 pF, R <sub>L</sub> = 2 kΩ
	t <sub>PHL</sub>	Count-up		_	18	24		
	t <sub>PLH</sub>	Count-down	Borrow	_	16	24	ns	
	t <sub>PHL</sub>				15	24		
Propagation delay time	t <sub>PLH</sub>	Either Count	Q		27	38	ns	
	t <sub>PHL</sub>			_	30	47		
	t <sub>PLH</sub>	Load	Q		24	40	ns	
	t <sub>PHL</sub>			_	25	40		
	t <sub>PHL</sub>	Clear	Q	_	23	35	ns	

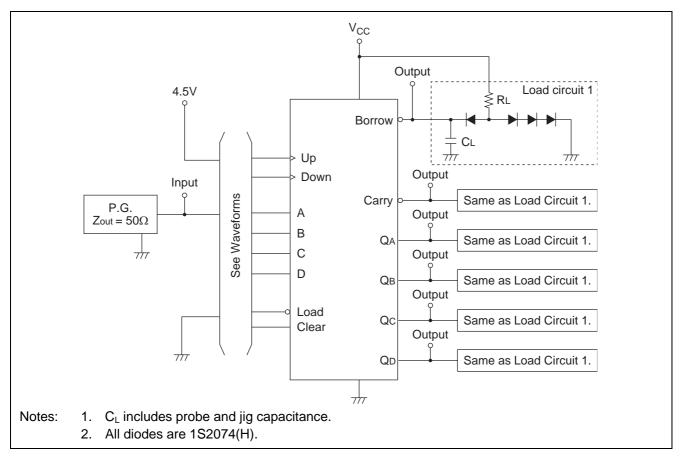
# **Count Sequence**





# **Testing Method**

### **Test Circuit**



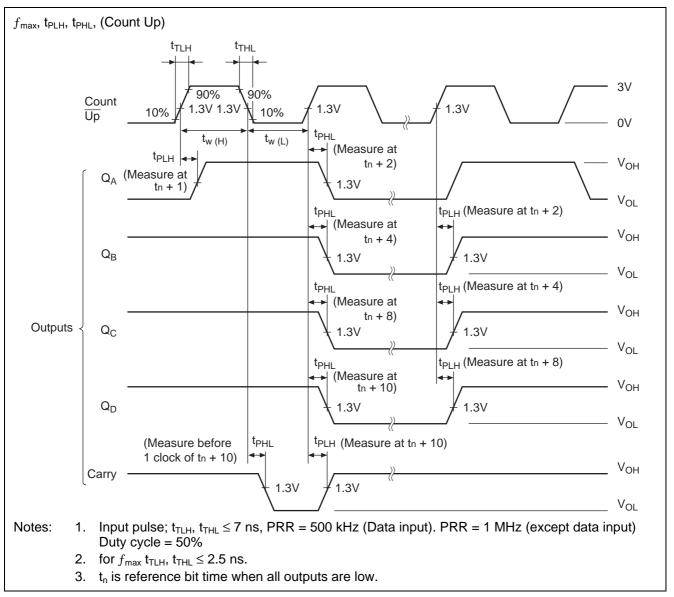
### **Testing Table**

ltem	From input to output	Inputs									
		CLR	Load	Up	Down	Α	В	С	D		
$f_{\sf max}$		GND	4.5V	IN	4.5V	GND	GND	GND	GND		
		GND	4.5V	4.5V	IN	GND	GND	GND	GND		
	Up Count	GND	4.5V	IN	4.5V	GND	GND	GND	GND		
t <sub>PLH</sub>	Down Count	GND	4.5V	4.5V	IN	GND	GND	GND	GND		
t <sub>PHL</sub>	Load→Q	GND	IN	GND	GND	IN	IN	IN	IN		
	Clear→Q	IN	IN*	GND	GND	4.5V	4.5V	4.5V	4.5V		

Note: \*. For initialized

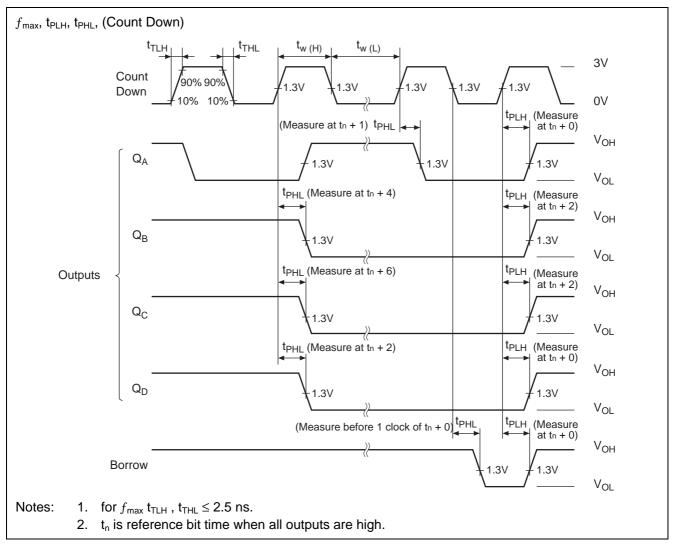
Item	From input to output	Outputs									
nem		Q <sub>A</sub>	Q <sub>B</sub>	Q <sub>C</sub>	QD	Carry	Borrow				
£		OUT	OUT	OUT	OUT	OUT	—				
Jmax		OUT	OUT	OUT	OUT	—	OUT				
	Up Count	OUT	OUT	OUT	OUT	OUT	—				
t <sub>PLH</sub>	Down Count	OUT	OUT	OUT	OUT	—	OUT				
t <sub>PHL</sub>	Load→Q	OUT	OUT	OUT	OUT	—	—				
	Clear→Q	OUT	OUT	OUT	OUT	—	—				

### Waveforms 1



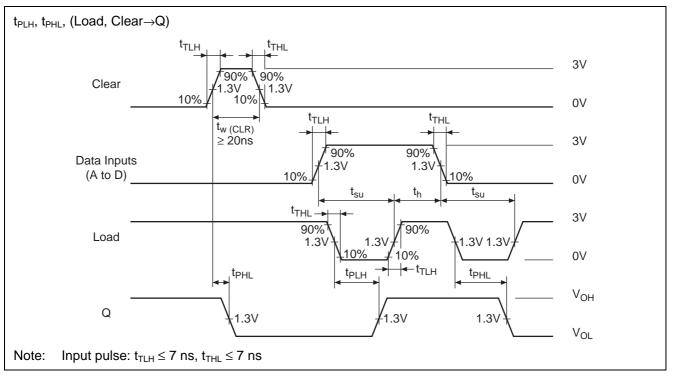


### Waveforms 2



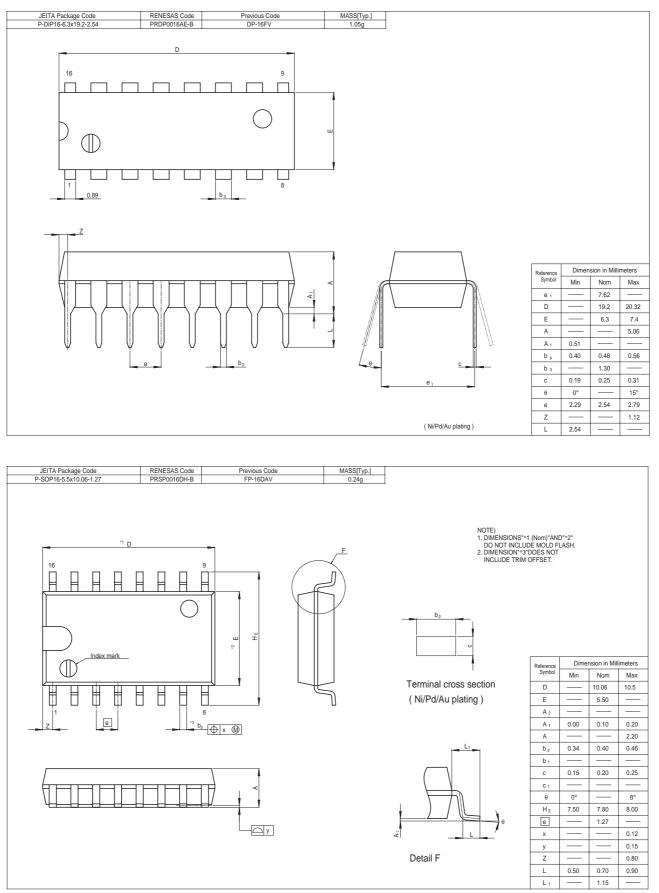


### Waveforms 3





# **Package Dimensions**





# Renesas Technology Corp. Sales Strategic Planning Div. Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan

Keep safety first in your circuit designs! 1. Renesas Technology Corp. puts the maximum effort into making semiconductor products better and more reliable, but there is always the possibility that trouble may occur with them. Trouble with semiconductors may lead to personal injury, fire or property damage. Remember to give due consideration to safety when making your circuit designs, with appropriate measures such as (i) placement of substitutive, auxiliary circuits, (ii) use of nonflammable material or (iii) prevention against any malfunction or mishap.

### Notes regarding these materials

- Notes regarding these materials
  1. These materials are intended as a reference to assist our customers in the selection of the Renesas Technology Corp. product best suited to the customer's application; they do not convey any license under any intellectual property rights, or any other rights, belonging to Renesas Technology Corp. ar a third party.
  2. Renesas Technology Corp. assumes no responsibility for any damage, or infringement of any third-party's rights, originating in the use of any product data, diagrams, charts, programs, algorithms, or circuit application examples contained in these materials.
  3. All information contained in these materials, including product data, diagrams, charts, programs and algorithms represents information on products at the time of publication of these materials, and are subject to change by Renesas Technology Corp. without notice due to product improvements or other reasons. It is therefore recommended that customers contact Renesas Technology Corp. or an authorized Renesas Technology Corp. product distributor for the latest product information before purchasing a product listed herein.
  The information described here may contain technical inaccuracies or typographical errors. Renesas Technology Corp. assumes no responsibility for any damage, liability, or other loss rising from these inaccuracies or errors. Please also pay attention to information before making a final decision on the applicability of the information and products. Renesas Technology Corp. Semiconductor home page (http://www.renesas.com).
  4. When using any or all of the information contained in these materials, including product data, diagrams, charts, programs, and algorithms, please be sure to evaluate all information actual system before making a final decision on the applicability of the information and products. Renesas Technology Corp. assumes no responsibility for any damage or manufactured for use in a device or system that is used under circumstanc

- use. 6. The prior written approval of Renesas Technology Corp. is necessary to reprint or reproduce in whole or in part these materials. 7. If these products or technologies are subject to the Japanese export control restrictions, they must be exported under a license from the Japanese government and cannot be imported into a country other than the approved destination. Any diversion or reexport contrary to the export control laws and regulations of Japan and/or the country of destination is prohibited. 8. Please contact Renesas Technology Corp. for further details on these materials or the products contained therein.



### **RENESAS SALES OFFICES**

Refer to "http://www.renesas.com/en/network" for the latest and detailed information.

Renesas Technology America, Inc. 450 Holger Way, San Jose, CA 95134-1368, U.S.A Tel: <1> (408) 382-7500, Fax: <1> (408) 382-7501

Renesas Technology Europe Limited Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K. Tel: <44> (1628) 585-100, Fax: <44> (1628) 585-900

Renesas Technology Hong Kong Ltd. 7th Floor, North Tower, World Finance Centre, Harbour City, 1 Canton Road, Tsimshatsui, Kowloon, Hong Kong Tel: <852> 2265-6688, Fax: <852> 2730-6071

Renesas Technology Taiwan Co., Ltd. 10th Floor, No.99, Fushing North Road, Taipei, Taiwan Tel: <886> (2) 2715-2888, Fax: <886> (2) 2713-2999

Renesas Technology (Shanghai) Co., Ltd. Unit2607 Ruijing Building, No.205 Maoming Road (S), Shanghai 200020, China Tel: <86> (21) 6472-1001, Fax: <86> (21) 6415-2952

Renesas Technology Singapore Pte. Ltd. 1 Harbour Front Avenue, #06-10, Keppel Bay Tower, Singapore 098632 Tel: <65> 6213-0200, Fax: <65> 6278-8001

http://www.renesas.com